APPROVAL SHEET

WLPN242412 Series
Shielded SMD Power Inductors

*Contents in this sheet are subject to change without prior notice.

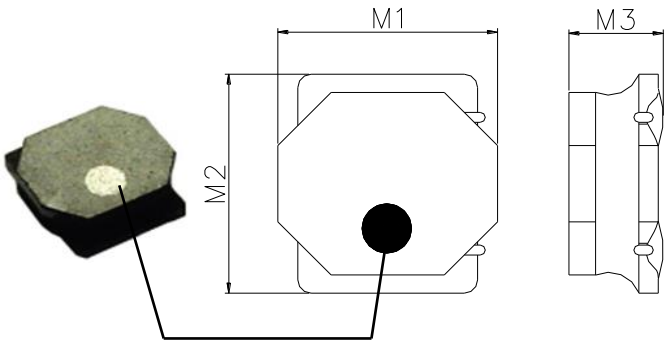
Features

1. Close magnetic loop with magnetic resin shielded.
2. Low profile, High inductance.

Applications

1. General propose power inductor in DC power system.
2. Inductor in DC/DC converter.
3. Low profile for portable and wearable device.
4. LC filter in Audio D class Amplifier.

Shape and Dimension

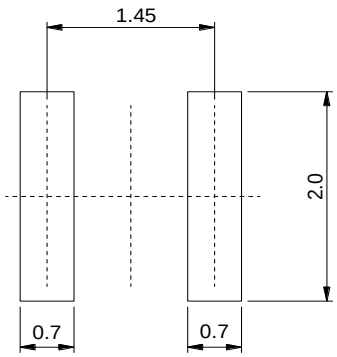


Polarity mark
Color: Silver

Unit: mm

	DIM.	TOL.
M1	2.4	±0.1
M2	2.4	±0.1
M3	1.2	MAX.
M4	0.6	±0.2
M5	1.45	±0.2

Recommended Land-Pattern



(Unit: mm)

Caution
Excessive solder has a risk of occurrence of mounting failure like slant in consequence of reflow condition.
In this case, please adjust solder quantity by design Change of stencil size and/or its thickness, etc.

Stencil thickness	※Stencil area
0.10mm	70%
0.12mm	60%
0.15mm	50%

*The above values are shown as scale ratio from our recommended land pattern

Ordering Information

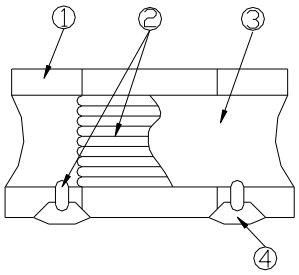
WL	PN	2424	12	N	R47	P	B
Product Code	**Series**	**Dimensions**	**Thickness**	**Tolerance**	**Value**	**Packing Code**	
WL: Inductor	Shielded SMD Power Inductors	2.4 * 2.4 mm	1.2 mm	M: ± 20% N: ± 30%	R47 = 0.47uH 2R2 = 2.2uH 100 = 10uH	P=7" Reeled (Embossed Tape)	B:STD

Electrical Characteristics

WLPN242412 Series	L (uH)	Inductance Tolerance	Test Freq (KHz)	DCR (Ω ± 20%)	SRF (MHz)Min	Rated Current (mA) Max	
						Saturation Current Idc1	Temperature Rise Current Idc2
WLPN242412NR47PB	0.47	±30%	100	0.050	180	2900	2100
WLPN242412N1R0PB	1.0	±30%	100	0.077	101	2350	1300
WLPN242412N1R5PB	1.5	±30%	100	0.100	89	2100	1150
WLPN242412M2R2PB	2.2	±20%	100	0.140	72	1700	1000
WLPN242412M3R3PB	3.3	±20%	100	0.225	56	1400	750
WLPN242412M4R7PB	4.7	±20%	100	0.300	45	1150	650
WLPN242412M6R8PB	6.8	±20%	100	0.420	34	950	550
WLPN242412M100PB	10	±20%	100	0.600	29	810	450

1. Test Frequency: 100KHz.
2. Test Equipment:
   Inductance: Chroma3302+1320+16502 or equivalent.
   DCR: Chroma16502 or equivalent.
   SRF: HP4291B or equivalent.
3. Saturation Current Idc1: The value of current causes a 30% inductance reduction from initial value.
4. Temperature rise current Idc2: The value of current causes a 40℃ temperature rise.
5. Rated Current: Either Idc1 or Idc2 whichever is smaller.
6. Operating Temperature Range:-25℃ to +120℃ (Including self-temperature rise).
7. Storage Temp. Range：-40℃ to +85℃.
8. MSL：Level 1.

Structural Drawing



① Ferrite core ：Ni-Zn ferrite.

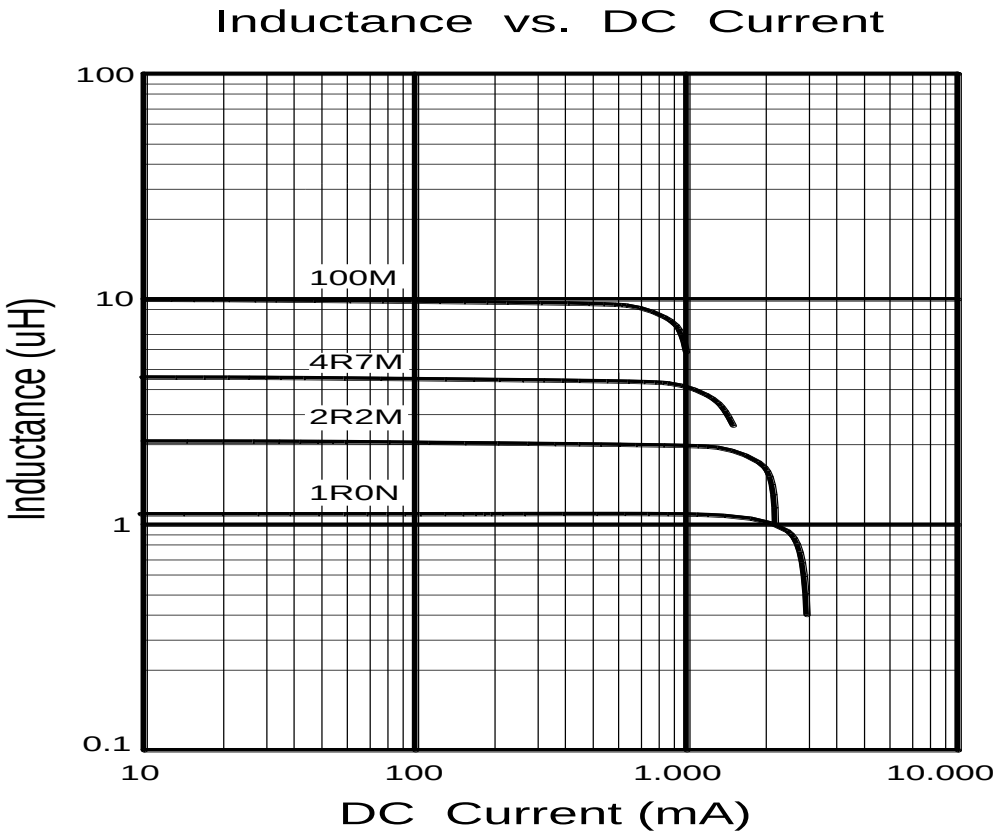
② Winding wire：Polyurethane-copper wire.

③ Over-coating resin：Epoxy resin, containing ferrite powder.

④ Electrode：External electrode (substrate) Cu.

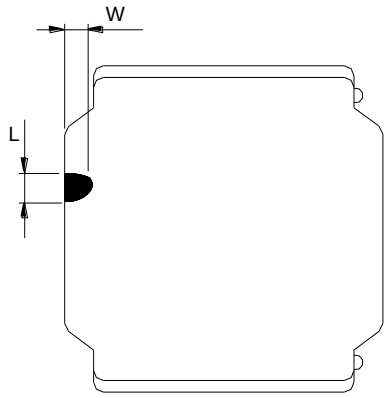
External electrode (top surface solder coating) Sn-Ag-Cu.

Characteristic Curve



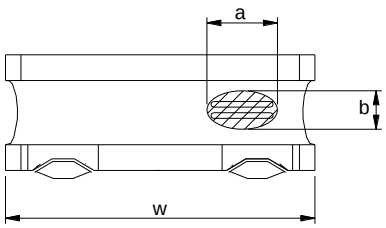
Core Chipping:

The appearance standard of the chipping size in top side, of bottom side ferrite core is following dimension.



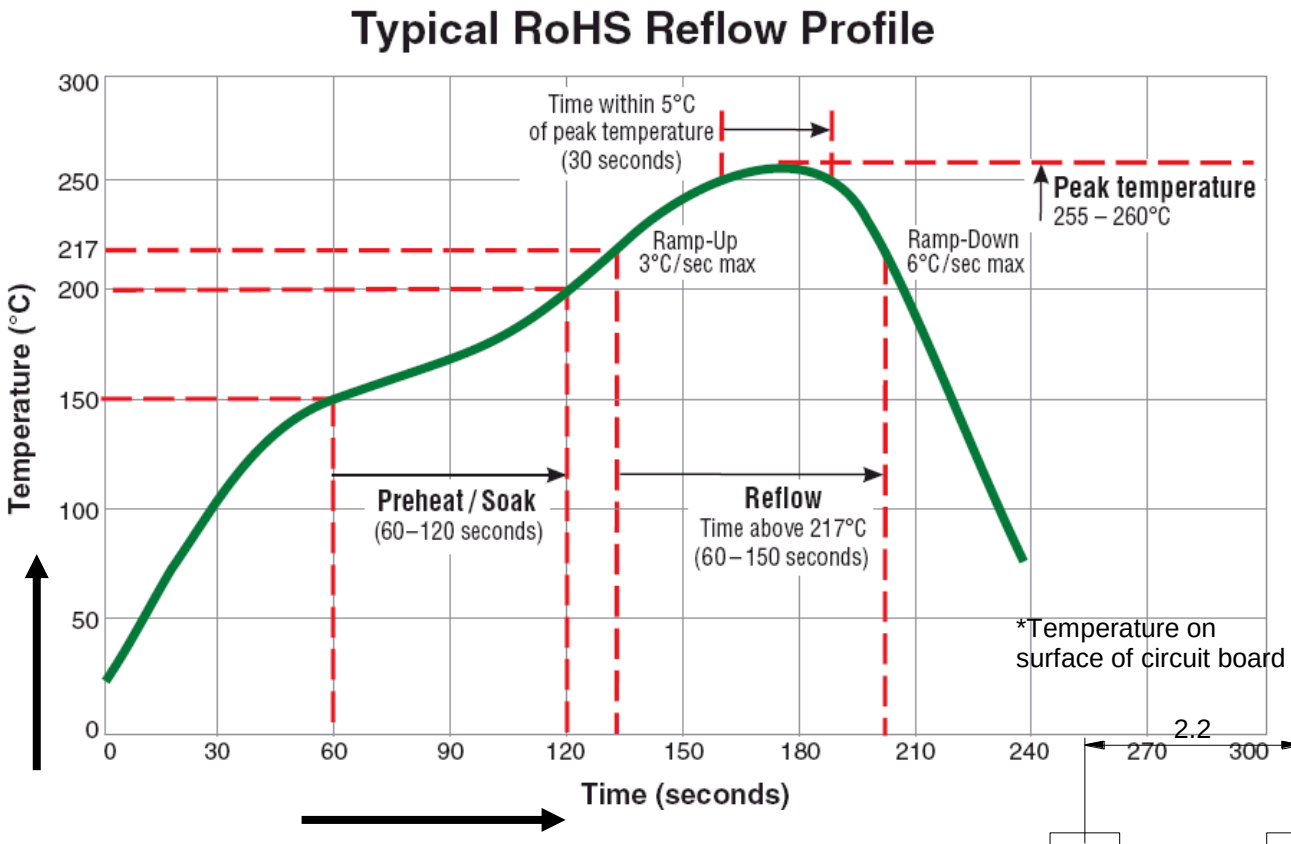
L	W
0.5mmMax.	0.5mmMax.

Exposed wire tolerance limit of coating resin part on product side
Size of exposed wire occurring to coating resin is specified below.



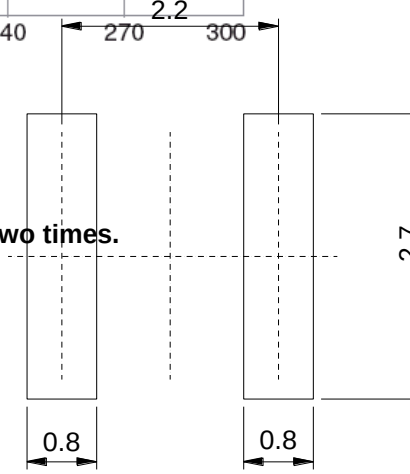
① Width direction (dimension a): Acceptable when a<=w/2
Nonconforming when a>w/2
② Length direction (dimension b): Dimension b is not specified.
③ When total area of exposed wire occurring to each sides is not greater than 50% of coating resin area, that is acceptable.

Reflow Profile Chart (Reference):

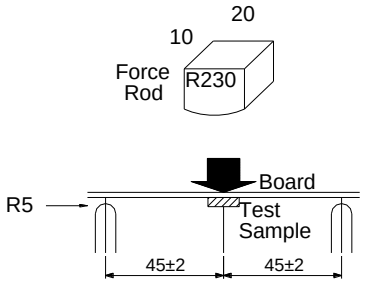


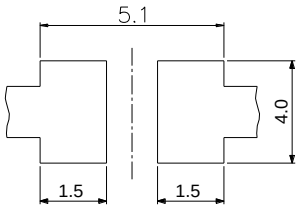
(Table 1)

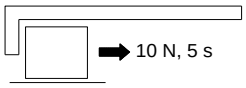
The products may be exposed to reflow soldering process of above profile up to two times.

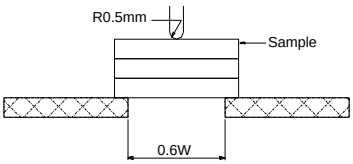


Mechanical Performance /Environmental Test Performance Specifications: (WLPN242412 series)

<table>
<tr><th>No.</th><th>Item</th><th>Test condition</th><th>Requirements</th></tr>
<tr><td>1</td><td>Resistance to Deflection.</td><td>No damage.</td><td>The test samples shall be soldered to the test board by the reflow soldering conditions show in Table 1.
As illustrated below, apply force in the direction of the Arrow indicating until deflection of the test board Reaches to 2 mm.
 



Land dimensions
Unit: mm
Test board size :100×40×10
Test board material l: glass epoxy-resin
Solder cream thickness:0.1</td></tr>
<tr><td>2</td><td>Adhesion of Terminal Electrode.</td><td>Shall not come off PC board.</td><td>The test samples shall be soldered to the test board by the reflow soldering conditions shown in Table 1.
 

Applied force: 10 N to X and Y directions Duration: 5 s.
Solder cream thickness:0.1 mm.
(Refer to recommended Land Pattern Dimensions Defined in “Precaution”)</td></tr>
<tr><td>3</td><td>Body strength.</td><td>No damage.</td><td>Applied force :20 N.
Duration :10 s.
 
</td></tr>
<tr><td>4</td><td>Resistance to Vibration.</td><td>△L/L:within±10%
No abnormality observed In appearance.</td><td>The test samples shall be soldered to the test board by the reflow soldering conditions shown in Table 1.Then it shall be submitted to below test conditions.
<table>
<tr><td>Frequency range</td><td>10Hz~55Hz</td></tr>
<tr><td>Total Amplitude</td><td>1.5mm(May not exceed acceleration 196 m/S2)</td></tr>
<tr><td>Sweeping Method</td><td>10Hz to 55Hz to 10 Hz for 1 min.</td></tr>
<tr><td>Time</td><td>For 2 hours on each X, Y, and Z axis.</td></tr>
</table></td></tr>
<tr><td>5</td><td>Resistance to Soldering heat (Reflow).</td><td>△L/L:within±10%
No abnormality observed In appearance.</td><td>The test sample shall be exposed to reflow oven at 230±5 deg C for 40 seconds, with peak temperature at 260±5 deg C for 5 seconds, 2 times.

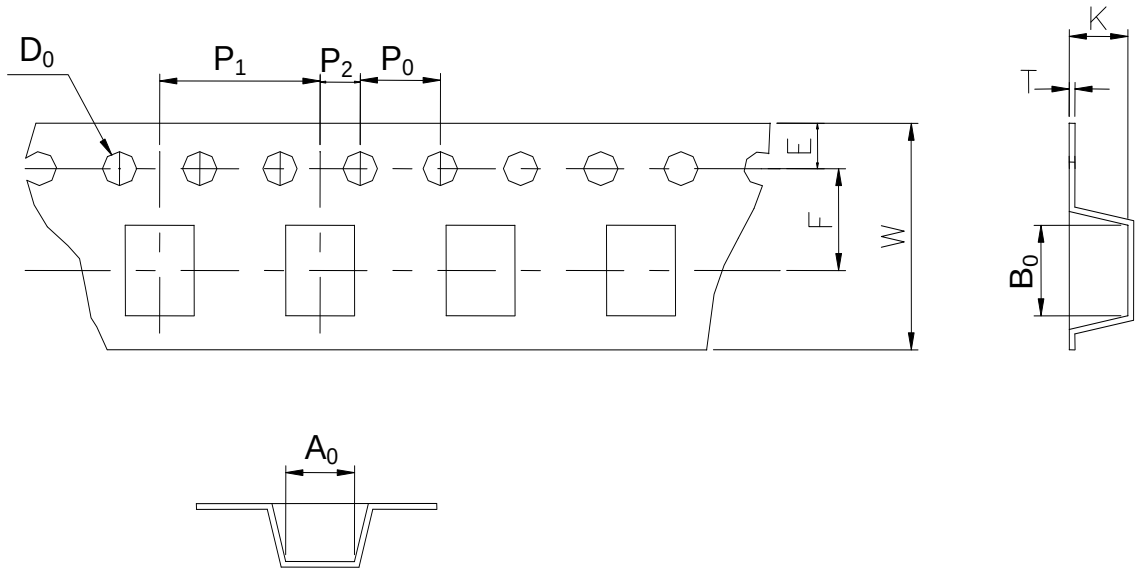
Test board thickness: 1.0 mm.
Test board material: glass epoxy-resin.</td></tr>
</table>

<table>
<tr><td>6</td><td>Solder ability.</td><td>At least 90% of surface of terminal electrode is covered by new solder.</td><td>The test samples shall be dipped in flux, and then Immersed in molten solder as shown in below table.
Flux: Methanol solution containing rosin 25%.<table><tr><td>Solder Temperature</td><td>245±deg C</td></tr><tr><td>Time</td><td>5±1.0 S.</td></tr><tr><td>Immersing Speed</td><td>25 mm/s</td></tr></table></td></tr>
<tr><td>7</td><td>Temperature Characteristics.</td><td>△L/L:within±20%
No abnormality observed in appearance.</td><td>Measurement of inductance shall be taken at temperature range within -25 deg C to +85 deg C.
With reference to inductance value at +20 deg C, change rate shall be calculated.</td></tr>
<tr><td>8</td><td>Thermal shock.</td><td>△L/L:within±10%
No abnormality observed in appearance.</td><td>The test samples shall be soldered to test board by the reflow soldering conditions shown in Table 1.
The test samples shall be placed at specified shown in below table in sequence.
The temperature cycle shall be repeated 100 cycles.

Conditions of steps for 1 cycle<table><tr><td>Step</td><td>Temperature</td><td>Time(min)</td></tr><tr><td>1</td><td>-40±3 deg C</td><td>30±3</td></tr><tr><td>2</td><td>Room Temp</td><td>3 maximum</td></tr><tr><td>3</td><td>85±2 deg C</td><td>30±3</td></tr><tr><td>4</td><td>Room Temp</td><td>3 maximum</td></tr></table></td></tr>
<tr><td>9</td><td>Low Temperature life Test.</td><td>△L/L:within±10%
No abnormality observed in appearance.</td><td>The test samples shall be soldered to the test board by the reflow soldering conditions shown in Table 1.
After that, the test samples shall be placed at test conditions as shown in below table.<table><tr><td>Temperature</td><td>-40±2 deg C</td></tr><tr><td>Time</td><td>500 +24/-0 h</td></tr></table></td></tr>
<tr><td>10</td><td>Loading at high temperature life test.</td><td>△L/L:within±10%
No abnormality observed in appearance.</td><td>The test samples shall be soldered to the test board by the reflow soldering conditions shown in Table 1.
The test samples shall be placed in thermostatic oven set at specified temperature and applied the rated current continuously as shown in below table.<table><tr><td>Temperature</td><td>85±2 deg C</td></tr><tr><td>Applied current</td><td>Rated current
(Refer to Page 3)</td></tr><tr><td>Time</td><td>500+24/-0 h</td></tr></table></td></tr>
<tr><td>11</td><td>Damp heat life test.</td><td>△L/L:within±10%
No abnormality observed in appearance.</td><td>The test samples shall be soldered to the test board by the reflow soldering conditions shown in Table 1.
The test samples shall be placed in thermostatic oven set at specified temperature and humidity as shown in below table.<table><tr><td>Temperature</td><td>60±2 deg C</td></tr><tr><td>Humidity</td><td>90~95%RH</td></tr><tr><td>Time</td><td>500+24/-0 h</td></tr></table></td></tr>
<tr><td>12</td><td>Loading under Damp heat life test.</td><td>△L/L:within±10%
No abnormality observed in appearance.</td><td>The test samples shall be soldered to the test board by the reflow soldering conditions shown in Table 1.
The test samples shall be placed in thermostatic oven set at specified temperature and humidity and applied the rated current continuously as shown in below table.<table><tr><td>Temperature</td><td>60±2 deg C</td></tr><tr><td>Humidity</td><td>90~95%RH</td></tr><tr><td>Applied current</td><td>Rated current (Refer to Page 3)</td></tr><tr><td>Time</td><td>500+24/-0 h</td></tr></table></td></tr>
</table>

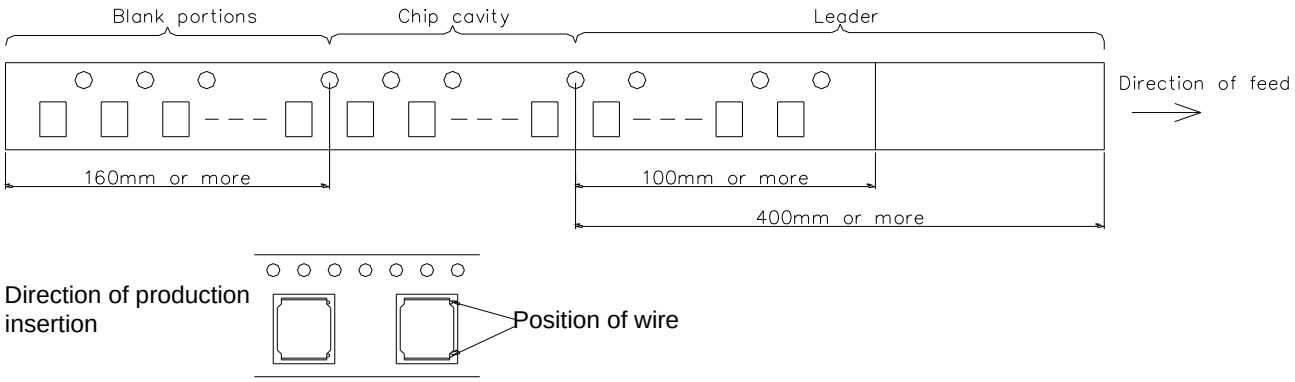
Tape & Reel Packaging Dimensions:

Dimensions Unit: mm

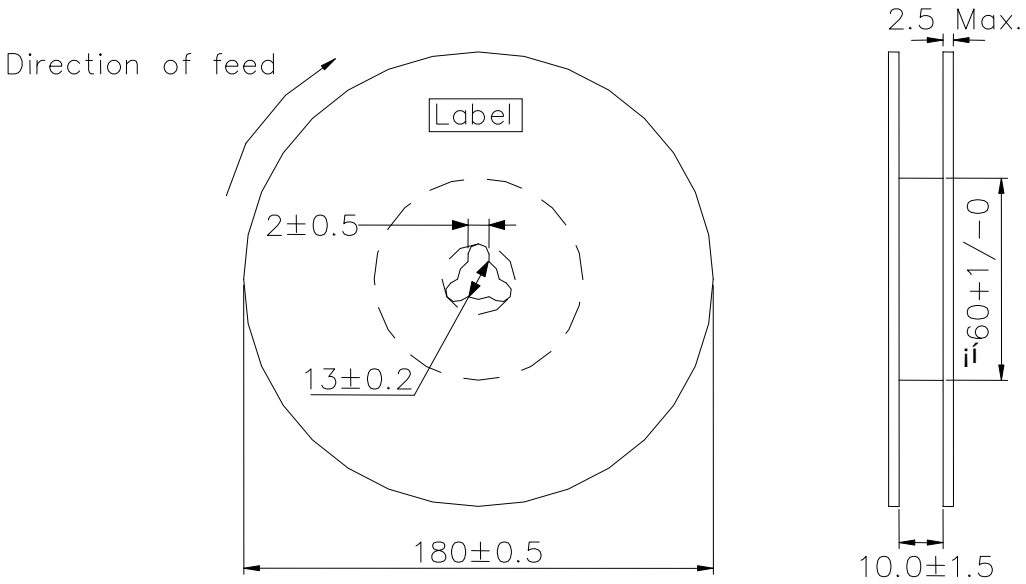


A_0	B_0	W	F	E	P_1	P_2	P_0	D_0	T	K
2.6 ±0.1	2.6 ±0.1	8.0 ±0.2	3.5 ±0.1	1.75 ±0.1	4.0 ±0.1	2.0 ±0.05	4.0± 0.1	Φ1.5 +0.1 -0	0.25 ±0.05	1.3 ±0.1

Direction of rolling

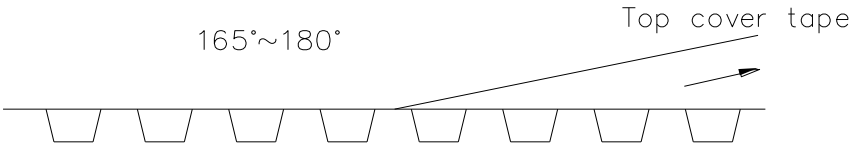


Reel



Label position:on the opposite sie of sprocket holes side of reel

Top tape strength



Peel-off strength: 0.1N~0.7N

Peel-off angle:165°~180°

Peel-off speed: 300mm/mm

Quantity per reel：2.5K pcs