

APPROVAL SHEET

WLBD1005HC High Current Chip Bead

*Contents in this sheet are subject to change without prior notice.



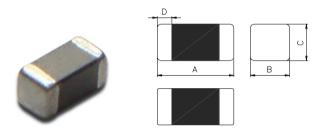
FEATURES

- 1. Closed magnetic circuit.
- 2. High current

APPLICATIONS

1. Noise reduction for general signal and DC line for General electronic circuits. Ex:PCs \ Networking and Consumer electronics.

SHAPE and DIMENSION



Chip Size			
A 1.00±0.10			
В	0.50±0.10		
С	0.50±0.10		
D	0.25±0.10		

Units: mm

Ordering Information

WL	BD	1005	НС	U	100	Т	В
Product Code	Series	Dimensions	Series extension	Tolerance	Value	Packing Code	
WL: Inductor	BD: Chip Bead.	1.0 * 0.5 mm 1005 :EIA 0402	HC: High Current. Refer to characteristic	U: ±25%	100 =10 OHM 121 =120 OHM	T = 7" Paper Tape	B:STD

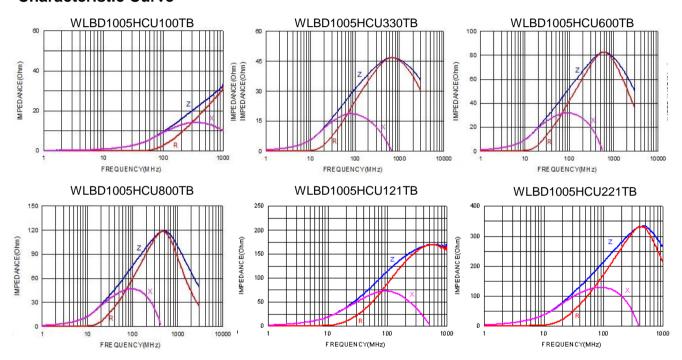


Electrical Characteristics

WLBD1005HC series

Walsin Part Number	Impedance (Ω)	Test Frequency (MHz)	DC Resistance (Ω) max.	Rated Current (mA) max.
WLBD1005HCU100TB	10±25%	100	0.05	2500
WLBD1005HCU330TB	33±25%	100	0.022	3000
WLBD1005HCU600TB	60±25%	100	0.032	2500
WLBD1005HCU800TB	80±25%	100	0.038	2300
WLBD1005HCU121TB	120±25%	100	0.095	2000
WLBD1005HCU221TB	220±25%	100	0.15	1500

Characteristic Curve





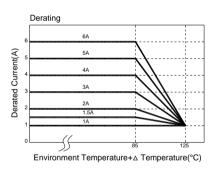
Test condition & Requirements

Item	Performance	Test Condition				
Operating Temperature	-40~+125℃ (Including self-temperature rise)					
Transportation Storage Temperature	-40~+125℃ (on board)	For long s			ons, please	see the
Impedance (Z)		Agilent429	1			
Inductance (Ls)		Agilent E49				
Q Factor	Peter to standard all strict all shore starting link	Agilent4287				
DC Resistance	Refer to standard electrical characteristics list	Agilent 4338				
Rated Current		DC Power	Supply d Curre		uirements, t	here will
Temperature Rise Test	Rated Current < 1A Δ T 20 $^{\circ}$ C Max Rated Current \geq 1A Δ T 40 $^{\circ}$ C Max	Applied the allowed DC current. Temperature measured by digital surface thermometer.				urface
		Number of	heat cy	cles: 1		
		Temperatu (°C)		Γime s)	Temperaturamp/imme	ersion
Designation of Soldenium	Appearance : No damage. Impedance : within±15% of initial value	260 ±5 (solder tem	np) 1	10 ±1	25mm/s ±	±6 mm/s
Heat Impedance: within±15% of initial value Inductance: within±10% of initial value Q: Shall not exceed the specification value. RDC: within ±15% of initial value and shall not exceed the specification value		Depth: com	npletely	cover th	ne terminatio	on
Solderability	More than 95% of the terminal electrode should be covered with solder.	Preheat: 15 Solder: SnS Solder temp Flux for lea Depth: com Dip time: 4:	96.5%- peratur d free: apletely	Ag3%-C e: 245±8 Rosin. 9	5℃	on.
Terminal strength	Appearance: No damage. Impedance: within±15% of initial value Inductance: within±10% of initial value Q: Shall not exceed the specification value. RDC: within±15% of initial value and shall not exceed the specification value	Preconditioning: Run through IR reflow for times.(IPC/JEDEC J-STD-020D Classification Reflow Profiles) Component mounted on a PCB apply a force (>0805:1kg <=0805:0.5kg)to the side of device being tested. This force shall be applied for 60 +1 seconds. Also the force shall be applied gradually as not to shock the component being tested.			sification y a force de of a shall be he force	
Bending	Appearance: No damage. Impedance: within±10% of initial value Inductance: within±10% of initial value Q: Shall not exceed the specification value. RDC: within±15% of initial value and shall not exceed the specification value Shall be mounted on a FR following dimensions:>=08 608 Bending depth:>=0805:1.2 0805:0.8 Duration of 10 sec for a minute of the specification value.				305:40x100x 305:40x100x 2mm 8mm	x1.2mm
Vibration Test	Appearance: No damage. Impedance: within±15% of initial value Inductance: within±10% of initial value Q: Shall not exceed the specification value. RDC: within ±15% of initial value and shall not exceed the specification value	Preconditioning: Run through IR reflow for times.(IPC/JEDEC J-STD-020D Classificat Reflow Profiles) Oscillation Frequency: 10~2K~10Hz for minutes Equipment: Vibration checker Total Amplitude:1.52mm±10% Testing Time: 12 hours(20 minutes, 12 cycleach of 3 orientations)			sification	
		Test cond	lition:			
Shock	Appearance: No damage. Impedance: within±10% of initial value Inductance: within±10% of initial value Q: Shall not exceed the specification value. RDC: within±15% of initial value and shall not exceed the specification value	Type V	/alue	Normal duration (D) (ms)	Wave form	Velocity change (Vi)ft/sec 11.3

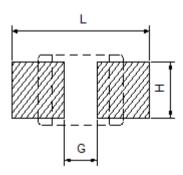
Item	Performance	Test Condition
Life test Load Humidity	Appearance: no damage. Impedance: within±15%of initial value. Inductance: within±10%of initial value. Q: Shall not exceed the specification value. RDC: within ±15% of initial value and shall not exceed the specification value	Preconditioning: Run through IR reflow for 2 times.(IPC/JEDEC J-STD-020D Classification Reflow Profiles) Temperature: 125±2°C (bead), 105±2°C (Inductor) Applied current: rated current. Duration: 1000±12hrs. Measured at room temperature after placing for 24±2 hrs. Preconditioning: Run through IR reflow for 2 times.(IPC/JEDEC J-STD-020D Classification Reflow Profiles) Humidity: 85±2°R.H. Temperature: 85±2°C. Duration: 1000hrs Min. with 100% rated current. Measured at room temperature after placing for 24±2 hrs.
Thermal shock	Appearance: no damage. Impedance: within±15%of initial value. Inductance: within±10%of initial value. Q: Shall not exceed the specification value. RDC: within ±15% of initial value and shall not exceed the specification value	Preconditioning: Run through IR reflow for 2 times.(IPC/JEDEC J-STD-020D Classification Reflow Profiles) Condition for 1 cycle Step1: $-40\pm2\%$ 30 ± 5 min. Step2: $25\pm2\%$ ≤ 0.5 min Step3: $+125\pm2\%$ 30 ± 5 min. (Bead) Step3: $+125\pm2\%$ 30 ± 5 min. (Inductor). Number of cycles: 500 Measured at room temperature after placing for 24 ± 2 hrs.

**Derating Curve

For the ferrite chip bead which withstanding current over 1.5A, as the operating temperature over $85^{\circ}\mathrm{C}$, the derating current information is necessary to consider with. For the detail derating of current, please refer to the Derated Current vs. Operating Temperature curve.



Soldering and Mounting



	L (mm)	G (mm)	H (mm)
WLBD1005HC	1.50	0.40	0.55



Soldering

Mildly activated rosin fluxes are preferred. The terminations are suitable for re-flow soldering systems. If hand soldering cannot be avoided, the preferred technique is the utilization of hot air soldering tools.

If wave soldering is used ,there will be some risk.

Re-flow soldering temperatures below 240 degrees, there will be non-wetting risk

Lead Free Solder re-flow

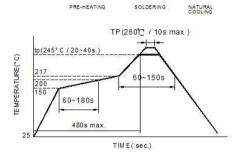
Recommended temperature profiles for lead free re-flow soldering in Figure 1. (Refered to J-STD-020C)

Soldering Iron:

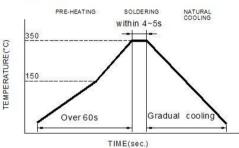
Products attachment with a soldering iron is discouraged due to the inherent process control limitations. If a soldering iron must be employed the following precautions are recommended. for Iron Soldering in Figure 2.

- Preheat circuit and products to 150°C
- Preheat circuit and process
 350°C tip temperature (max)
 Reflow Soldering
- · Never contact the ceramic with the iron tip
- · 1.0mm tip diameter (max)
- Use a 20 watt soldering iron with tip diameter of 1.0mm
- · Limit soldering time to 4~5sec.





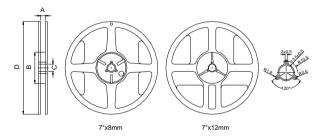
Reflow times: 3 times max-Fig.1



Iron Soldering times: 1 times max-Fig.2

Packaging Specification

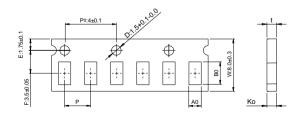
Reel Dimension



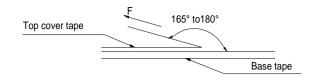
Туре	A(mm)	B(mm)	C(mm)	D(mm)
7"x8mm	9.0±0.5	60±2	13.5±0.5	178±2
7"x12mm	13.5±0.5	60±2	13.5±0.5	178±2

Tape Dimension / 8mm

■Material of taping is paper



Size	Bo(mm)	Ao(mm)	Ko(mm)	P(mm)	t(mm)
WLBD1005HC	1.12±0.03	0.62±0.03	0.60±0.03	2.0±0.05	0.60±0.03



The force for tearing off cover tape is 15 to 60 grams in the arrow direction under the following conditions

Room Temp. (°ℂ)	Room Humidity (%)	Room atm (hPa)	Tearing Speed mm/min
5~35	45~85	860~1060	300

Products meet IPC/JEDEC J-STD-020D standard-MSL, level 1.

Quantity per reel : 10K pcs / reel