TFT DISPLAY SPECIFICATION



WINSTAR Display Co.,Ltd. 華凌光電股份有限公司





WEB: https://www.winstar.com.tw E-mail: sales@winstar.com.tw

SPECIFICATION

CUSTOMER :								
MODULE NO.:	WF80QTIFGDBNB#							
	1							
APPROVED BY:								
(FOR CUSTOMER USE ONLY)								
	PCB VERSION: DATA:							

SALES BY APPROVED BY	CHECKED BY	PREPARED BY
		葉虹蘭

ISSUED DATE: 2023/11/06

TFT Display Inspection Specification: https://www.winstar.com.tw/technology/download.html
Precaution in use of TFT module: https://www.winstar.com.tw/technology/download/declaration.html



MODLE NO:

REC	ORDS OF REV	ISION	DOC. FIRST ISSUE
VERSION	DATE	REVISED PAGE NO.	SUMMARY
0	2023/09/06		First issue
A	2023/11/06		Modify Summary.

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1.Module Classification Information

80 Q T F G N W F I D В В # 1 3 4 8 (11) (12) 2 (5) 6 7 9 10 (13)

①	Brand: WINSTA	R DISPLAY	CORI	PORAT	TION	1						
2	Display Type: F-	→TFT Type,	J→Cu	ıstom T	FT							
3	Display Size: 8.0)" TFT										
4	Model serials no.											
(5)	Backlight	F→CCFL, W	hite				T-	\rightarrow L	ED, White	e		
	Type:	S→LED, Hig	gh Lig	tht Whi	ite		Z	Z→Nichia LED, White				
	LCD Polarize	A→Transmissive, N.T, IPS TFT						\rightarrow T	ransmissi	ve, S	Super W.T,	12:00
	Type/	C→Transmis	ssive,	N. T, 6	:00 ;		R	\rightarrow T	ransmissiv	ve, S	uper W.T,	O-TFT
	Temperature	F→Transmis	sive, l	N.T,12	:00;		V	\rightarrow T	ransmissi	ve, S	Super W.T,	VA TFT
6	range/ Gray	I→Transmiss	sive, V	V. T, 6:	00		W	/→]	Γransmissi	ve,	Super W.T,	IPS TFT
	Scale Inversion	K→Transfleo	ctive,	W.T,12	2:00		X	—T	ransmissi	ve, V	V.T, VA TF	T
	Direction	L→Transmis	sive,	W.T,12	:00		Y	\rightarrow T	ransmissi	ve, V	V.T, IPS TI	FT
	Direction	N→Transmis	ssive,	Super '	W.T,	6:00	Z	→Tı	ransmissiv	ve, V	V.T, O-TFT	1
	A: TFT LCD										L BOAR	.D
	B: TFT+SCREW HOLES+CONTROL BOARD G: TFT+ SCREW HOLES											
7	C: TFT+ SCREW HOLES +A/D BOARD H: TFT+D/V BOARD											
	D: TFT+ SCREW HOLES +A/D BOARD+CONTROL BOARD I: TFT+ SCREW HOLES +D/V BOARD											
	E: TFT+ SCREV	V HOLES +P	OWE	R BC	OAR	Ď	J	: TI	T+POWI	ER E	BD	
	Resolution:											
	A 128160 B	320234	32	0240	D	4802	34	Е	480272	F	640480	
8	G 800480 H	1024600	I 32	0480	J	2403	20	K	800600	L	240400	
	M 1024768 N	128128 I	128	80800	Q	4808	00	R	640320	S	480128	
	T 800320 U	8001280 V	V 17	6220	W	12803	98	X	1024250	Y	1920720	
	Z 800200 2	1024324	3 720	01280	4	19201	200	5	1366768	6	1280320	
9	D: Digital L:	LVDS M:M	IIPI									
	Interface:	/		_								
10	N Without co	ntrol board	A	8Bit		В		16E	Bit	Н	HDMI	
	I I2C Interfa	ce	R	RS232	2	S	SPI	Inte	erface	U	USB	
	TS:											
	N Without TS		T	Resist	ive t	ouch p	anel	1	C Capaci	itive	touch pane	el (G-F-F)
11)	G Capacitive to	ouch panel (G-	-G)			C1	Ca	pac	itive touch	n par	nel (G-F-F)	+OCA
	C2 Capacitive to	ouch panel (G-	-F-F)+	-OCR		G1	Ca	apac	itive toucl	n pai	nel (G-G)+	OCA
G2 Capacitive touch panel (G-G)+OCR B CTP+GG+USB												
12	Version: X:Ras	berry pi										
13	Special Code	#:Fit in wi	th RO	HS dir	ectiv	ve regu	latio	ons				
-		<u> </u>										



2.Summary

TFT 8.0" is a transmissive type color active matrix liquid crystal display (LCD) which uses amorphous thin film transistor (TFT) as switching devices. This product is a composed of a TFT-LCD Panel, driver ICs and a backlight unit.



WF80QTIFGDBNB#

3.General Specifications

ltem	Dimension	Unit						
Size	8.0	inch						
Dot Matrix	800 x RGBx480(TFT)	dots						
Module dimension	192.8(W) x 116.9(H) x 12.1(D) mm							
Active area	176.64 x 99.36	mm						
Pixel size	0.2208(H) x 0.2070(V)	mm						
LCD type	TFT, Normally White ,Transmissive	•						
View Direction	12 o'clock							
Gray Scale Inversion Direction	6 o'clock							
Aspect Ratio	16:9							
Backlight Type	LED,Normally White							
Controller IC	SSD1963							
Interface	Digital 8080 family MPU 8bit/16bit							
With /Without TP	Without TP							
Surface	Anti-Glare							

^{*}Color tone slight changed by temperature and driving voltage.

4.Absolute Maximum Ratings

Item	Symbol	Min	Тур	Max	Unit
Operating Temperature	TOP	-20	_	+70	$^{\circ}\!\mathbb{C}$
Storage Temperature	TST	-30	_	+80	$^{\circ}\!\mathbb{C}$

Note: Device is subject to be damaged permanently if stresses beyond those absolute maximum ratings listed above

1. Temp. $\leq\!60^{\circ}\!\!\!\mathrm{C}$, 90% RH MAX. Temp. $>\!60^{\circ}\!\!\mathrm{C}$, Absolute humidity shall be less than 90% RH at $60^{\circ}\!\!\mathrm{C}$

5.Electrical Characteristics

5.1. Operating conditions: (CON3.Pin1=GND, Pin2=VDD)

Item	Symbol	Min	Тур	Max	Unit	Remark
Supply Voltage For LCM	VDD	3.0	3.1	3.3	V	_
Supply Current For LCM	IDD		120	180	mA	Note1

Note 1 : This value is test for VDD=3.3V , Ta=25°C only

5.2. Backlight driving conditions (CON3.Pin1=GND, Pin2=VDD)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Remark
Operation Current For LED Driver	VLED=5V	_	480	720	mA	Note 1,2
Power Consumption	VLED=5V	_	2400	3600	mW	Note 1,2
Supply Voltage For LED Driver	VLED+	_	5	-	V	_
LED Life Time	_	_	20,000		Hr	Note 2,3,4

Note 1 : Base on VLED= 5V for the back light driver IC specification

Note 2 : Ta = 25 °C

Note 3: Brightness to be decreased to 50% of the initial value

Note 4: The single LED lamp case

6.DC CHARATERISTICS

Parameter	Symbol		Rating	Unit	Condition	
1 ar anicter	Symbol	Min	Min Typ Max		Omt	Condition
Low level input voltage	VIL	0	-	0.3VDD	V	
High level input voltage	V _{IH}	0.7VDD	-	VDD	V	A.

7.Interface timing

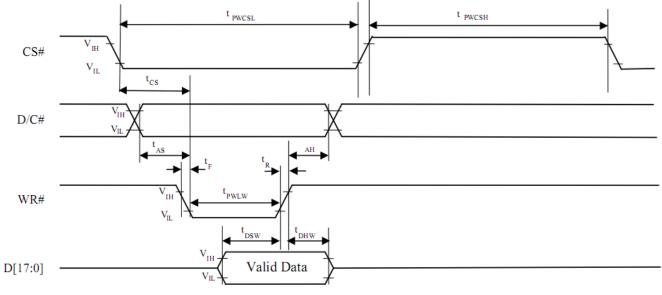
7.1. 8080 Mode

The 8080 mode MCU interface consist of CS#, D/C#, RD#, WR#, Data bus and TE signals (Please refer to Table 6-1 for pin multiplexed with 6800 mode). This interface use WR# to define a write cycle and RD# for read cycle. If the WR# goes low when the CS# signal is low, the data or command will be latched into the system at the rising edge of WR#. Similarly, the read cycle will start when RD# goes low and end at the rising edge of RD#.

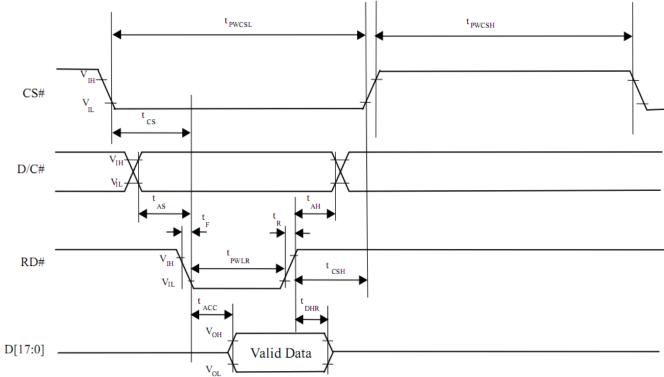
7.2. 8080 Mode Write Cycle

Symbol	Parameter	Min	Тур	Max	Unit
fMCLK	System Clock Frequency	1	- ,	110	MHz
tMCLK	System Clock Period	1/ fMCLK		, 5	ns
tPWCSH	Control Pulse High Width Write Read	13 30	1.5* t MCLK 3.5* t MCLK	-	ns
tPWCSL	Control Pulse Low Width Write (next write cycle) Write (next read cycle) Read	13 80 80	1.5* tMCLK 9* tMCLK 9* tMCLK	-	ns
tAS	Address Setup Time	1) -	-	ns
tAH	Address Hold Time	2	-	-	ns
tDSW	Write Data Setup Time	4			ns
tDHW	Write Data Hold Time	1	ı	-	ns
tPWLW	Write Low Time	12			ns
tDHR	Read Data Hold Time	1	•	-	ns
tACC	Access Time	32			ns
tPWLR	Read Low Time	36	-	-	ns
tR	Rise Time	-		0.5	ns
tF	Fall Time	-	-	0.5	ns
tCS	Chip select setup time	2		-	ns
tCSH	Chip select hold time to read signal	3	-	-	ns

7.3. Parallel 8080-series Interface Timing Diagram(Write Cycle)



7.4. Parallel 8080-series Interface Timing Diagram(Read Cycle)



7.5. Pixel Data Format

Interface	Cycle	D[15]	D[14]	D[13]	D[12]	D[11]	D[10]	D[9]	D[8]	D[7]	D[6]	D[5]	D[4]	D[3]	D[2]	D[1]	D[0
16 bits (565 format)	1 st	R5	R4	R3	R2	R1	G5	G4	G3	G2	G1	G0	B5	B4	В3	B2	B1
300	1 st	R7	R6	R5	R4	R3	R2	R1	R0	G7	G6	G5	G4	G3	G2	G1	G0
16 bits	2 nd	B7	B6	B5	B4	ВЗ	B2	B1	В0	R7	R6	R5	R4	R3	R2	R1	R0
	3 rd	G7	G6	G5	G4	G3	G2	G1	G0	В7	B6	B5	B4	ВЗ	B2	B1	BO
	1 st									R7	R6	R5	R4	R3	R2	R1	RO
8 bits	2 nd									G7	G6	G5	G4	G3	G2	G1	G0
	3 rd									B7	B6	B5	В4	В3	B2	B1	BO

8.Optical Characteristics

Item		Symbol	Condition.	Min	Тур.	Max.	Unit	Remark		
Door on on time		Tr	θ=0° · Φ=0°	-	10	20	ms	Note 0		
Response time		Tf	θ-0 (Ψ-0	-	15	30	ms	Note 3		
Contrast ratio		CR	At optimized viewing angle	400	500	-	ı	Note 4		
Color	\\/\b:to	Wx	θ=0° \ Ф=0	0.261	0.311	0.361	>	Note 0.07		
Chromaticity	White	Wy	θ-0 , Φ-0	0.293	0.343	0.393	×(Note 2,6,7		
	11	ΘR		55 70 -						
Viewing angle (Gray Scale	Hor.	ΘL	CD > 10	55	70	-				
Inversion Direction)		ΦТ	CR≧10	35	50		Deg	Note 1		
Birodiony	Ver.	ФВ		55	70	2	,			
Brightness	•	-	-	350	420) -	cd/m ²	Center Of Display		
Uniformity		(U)	-	70	1	-	%	Note 5		

Ta= $25\pm2^{\circ}$ C, VLED / ILED= 5V / 480mA

Note 1: Definition of viewing angle range

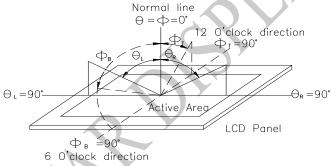


Fig 8.1. Definition of viewing angle

Note 2: Test equipment setup:

After stabilizing and leaving the panel alone at a driven temperature for 10 minutes, the measurement should be executed. Measurement should be executed in a stable, windless, and dark room. Optical specifications are measured by Topcon BM-7orBM-5 luminance meter 1.0° field of view at a distance of 50cm and normal direction.

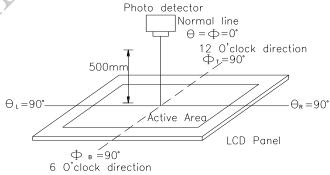
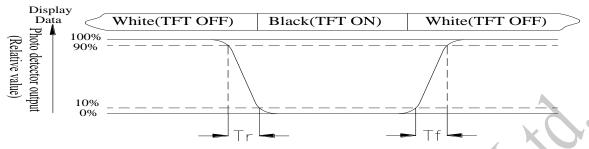


Fig 8.2. Optical measurement system setup

Note 3: Definition of Response time:

The response time is defined as the LCD optical switching time interval between "White" state and "Black" state. Rise time, Tr, is the time between photo detector output intensity changed from 90%to 10%. And fall time, Tf, is the time between photo detector output intensity changed from 10%to 90%



Note 4: Definition of contrast ratio:

The contrast ratio is defined as the following expression.

Contrast ratio (CR) = Luminance measured when LCD on the "White" state

Luminance measured when LCD on the "Black" state

Note 5: Definition of Luminance Uniformity

Active area is divided into 9 measuring areas (reference the picture in below). Every measuring point is placed at the center of each measuring area.

Luminance Uniformity (U) = Lmin/Lmax x100%

L = Active area length

W = Active area width

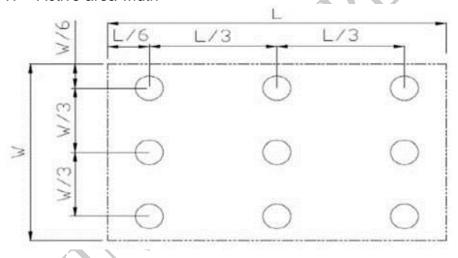


Fig 8.3. Definition of uniformity

Note 6: Definition of color chromaticity (CIE 1931) Color coordinates measured at the center point of LCD

Note 7: Measured at the center area of the panel when all the input terminals of LCD panel are electrically opened.

9.Interface

9.1. LCM PIN Definition (CON3)

Pin	Symbol	Function	Remark	
1	GND	System ground pin of the IC .Connect to system ground.		
2	VDD	Power Supply: +3.3V		
3	BLE	Backlight control signal , H: On \ L: Off		
4	D/C	Data/Command select		
5	WR	Write strobe signal	J Y	
6	RD	Read strobe signal		
7	DB0	Data bus		
8	DB1	Data bus		
9	DB2	Data bus		
10	DB3	Data bus		
11	DB4	Data bus		
12	DB5	Data bus		
13	DB6	Data bus		
14	DB7	Data bus		
15	DB8	Data bus (When select 8bits Mode, this pin is NC)	Note1	
16	DB9	Data bus (When select 8bits Mode, this pin is NC)	Note1	
17	DB10	Data bus (When select 8bits Mode, this pin is NC)	Note1	
18	DB11	Data bus (When select 8bits Mode, this pin is NC)	Note1	
19	DB12	Data bus (When select 8bits Mode, this pin is NC)	Note1	
20	DB13	Data bus (When select 8bits Mode, this pin is NC)	Note1	
21	DB14	Data bus (When select 8bits Mode, this pin is NC)	Note1	
22	DB15	Data bus (When select 8bits Mode, this pin is NC)	Note1	
23	NC	No connect		
24	NC	No connect		
25	cs	Chip select		
26	RESET	Hardware reset		
27	L/R	Left / right selection; Default L/R=H	Note 2,3	
28	U/D	Up/down selection; ; Default U/D=L	Note 2,3	
29	NC	No connect		
30	NC	No connect		

31	NC	No connect	
32	NC	No connect	
33	VLED-	Power for LED Driver IC(GND)	
34	VLED-	Power for LED Driver IC(GND)	
35	VLED+	Power for LED Driver IC(+5V)	
36	VLED+	Power for LED Driver IC(+5V)	

Note1: When select 8bit mode, DB0~DB7 be used, DB8~DB15 no connect When select 16bit mode, DB0~DB15 be used

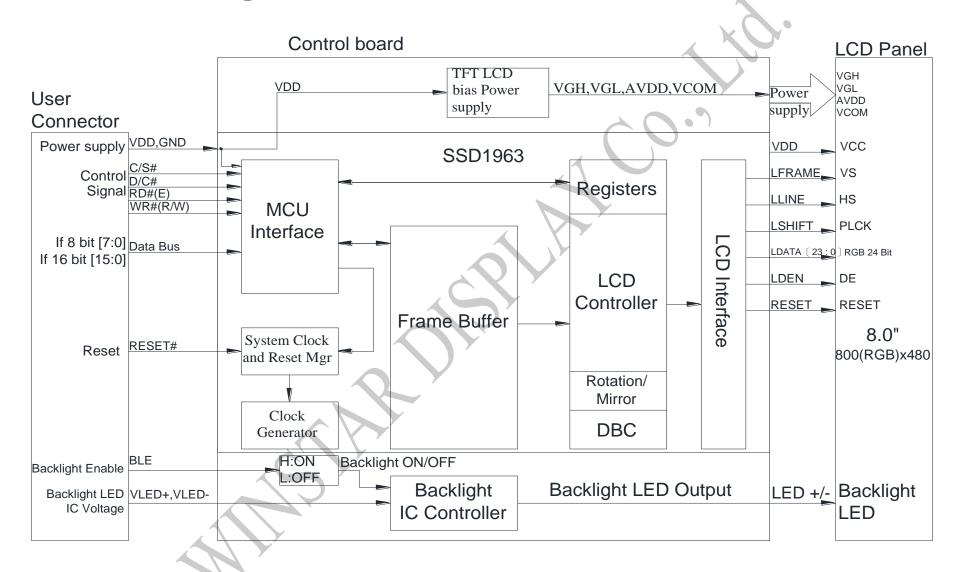
Note 2: Selection of scanning mode

Setting of scar	n control input	Coopping direction
U/D	L/R	Scanning direction
GND	VDD	Up to down, left to right
VDD	GND	Down to up, right to left
GND	GND	Up to down, right to left
VDD	VDD	Down to up, left to right

Note 3: Definition of scanning direction. Refer to the figure as below:



10.Block Diagram



11.Reliability

Content of Reliability Test (Wide temperature, -20°C ~70°C)

Environmental Test				
Test Item	Content of Test	Test Condition	Note	
High Temperature storage	Endurance test applying the high storage temperature for a long time.	80°ℂ 200hrs	2	
Low Temperature storage	Endurance test applying the low storage temperature for a long time.	-30°ℂ 200hrs	1,2	
High Temperature Operation	Endurance test applying the electric stress (Voltage & Current) and the thermal stress to the element for a long time.	70℃ 200hrs		
Low Temperature Operation	Endurance test applying the electric stress under low temperature for a long time.	-20℃ 200hrs	1	
High Temperature/ Humidity Operation	The module should be allowed to stand at 60°C,90%RH max	60℃,90%RH 96hrs	1,2	
Thermal shock resistance	The sample should be allowed stand the following 10 cycles of operation -20°C 25°C 70°C 30min 5min 30min 1 cycle	-20°ℂ/70°ℂ 10 cycles		
Vibration test	Endurance test applying the vibration during transportation and using.	Total fixed amplitude: 1.5mm Vibration Frequency: 10~55Hz One cycle 60 seconds to 3 directions of X,Y,Z for Each 15 minutes	3	
Static electricity test	Endurance test applying the electric stress to the terminal.	VS=±600V(contact), ±800v(air), RS=330Ω CS=150pF 10 times		

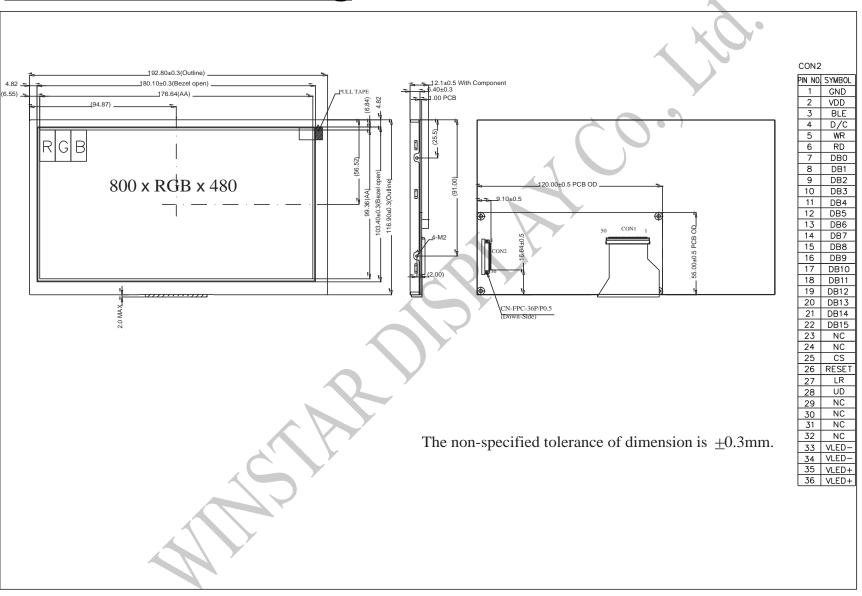
Note1: No dew condensation to be observed.

Note2: The function test shall be conducted after 4 hours storage at the normal

Temperature and humidity after remove from the test chamber.

Note3: The packing have to including into the vibration testing.

12.Contour Drawing



13.Initial Code For Reference

```
void Initial SSD1963()
         Write Command(0x01);
         Delay ms(10);
         Write Command(0xe0);
                                   //START PLL
         Write Parameter(0x01);
         Delay_{ms}(50);
         Write Command(0xe0);
                                   //START PLL
         Write_Parameter(0x03);
         Delay_ms(5);
         Write Command(0xb0);
         Write Parameter(0x20);
         Write_Parameter(0x80);
         Write Parameter(0x03);
         Write Parameter(0x1f);
         Write_Parameter(0x01);
         Write Parameter(0xdf);
         Write_Parameter(0x00);
         Write_Command(0xf0);
         Write_Parameter(0x03); //pixel data format, 0x03 is 16bit(565 format);0x00 is for 8-bit
         //Set the MN of PLL
         Write Command(0xe2);
         Write Parameter(0x1d);
         Write_Parameter(0x02);
         Write Parameter(0x54);
         Write Command(0xe6);
         Write_Parameter(0x04);
         Write_Parameter(0x6f);
         Write Parameter(0x47);
         //Set front porch and back porch
         Write_Command(0xb4);
         Write Parameter(0x04);
         Write Parameter(0x20);
         Write Parameter(0x00);
         Write Parameter(0x2e);
         Write Parameter(0xd2);
         Write Parameter(0x00);
         Write Parameter(0x00);
         Write_Parameter(0x00);
```

Write_Command(0xb6);

```
Write_Parameter(0x02);
 Write_Parameter(0x0d);
 Write_Parameter(0x00);
 Write_Parameter(0x17);
 Write_Parameter(0x16);
 Write_Parameter(0x00);
 Write_Parameter(0x00);
Write_Command(0x2a);
Write_Parameter(0x00);
Write_Parameter(0x00);
Write_Parameter(0x03);
Write_Parameter(0x1f);
Write_Command(0x2b);
Write_Parameter(0x00);
Write_Parameter(0x00);
Write_Parameter(0x01);
Write_Parameter(0xdf);
 Write Command(0xb8);
 Write_Parameter(0x0f);
 Write_Parameter(0x01);
 Write_Command(0xba);
 Write_Parameter(0x01);
 Write_Command(0x29);
Write_Command(0x2c);
```

}

■ ■ Modu	winstar <u>LCM Sar</u> ule Number:	nple Es	imate Feedback S	<u>Sheet</u> Page: 1
	Panel Specification:		_	1 age. 1
1.	Panel Type:	☐ Pass	\sqcap NG	
2.	View Direction:	☐ Pass		
3.	Numbers of Dots:	Pass		
<i>3</i> . 4.	View Area:	☐ Pass		
5 .	Active Area:	☐ Pass		
<i>5</i> .	Operating Temperature:	☐ Pass		Y () •
7.	Storage Temperature:	☐ Pass		4 1
8.	Others:		NO ,	
_	Mechanical Specification:			
2 · <u>1</u> 1.	PCB Size:	☐ Pass	\bigcap NG,	20.9
2.	Frame Size:	☐ Pass	☐ NG ,	
3.	Material of Frame:	☐ Pass		
3. 4.	Connector Position:	☐ Pass	□ NG ,	
5.	Fix Hole Position:	☐ Pass	□ NG ,	
<i>5</i> . 6.	Backlight Position:	☐ Pass		
7.	Thickness of PCB:	☐ Pass		
8.	Height of Frame to PCB:	☐ Pass		
9.	Height of Module:	Pass		
	Others:	Pass	y	
	Relative Hole Size:		NO ,	
	Pitch of Connector:	Pass		
	Hole size of Connector:	Pass		
	Mounting Hole size :	Pass		
	Mounting Hole Type:	Pass		
	Others:	☐ Pass		
	acklight Specification:		□ NG ,	
	B/L Type:	☐ Pass		
	B/L Color:	☐ Pass		
	B/L Driving Voltage (Referen	_		□ NG ,
	B/L Driving Current:	Pass		
	Brightness of B/L:	☐ Pass		
	B/L Solder Method:	☐ Pass		
	Others:	☐ Pass		
1.	Outers .			
		// (o to page 2 <<	

	winstar				
Iodu	winstar le Number :		Page: 2		
5、	Electronic Characteristics of	f Module :			
1.	Input Voltage:	Pass	□ NG ,		
2.	Supply Current:	Pass	□ NG ,		
3.	Driving Voltage for LCD:	Pass	□ NG ,		
4.	Contrast for LCD:	Pass	□ NG ,		
5.	B/L Driving Method:	Pass	□ NG ,		
6.	Negative Voltage Output:	Pass	□ NG ,		
7.	Interface Function:	Pass	□ NG ,		
8.	LCD Uniformity:	Pass	□ NG ,		
9.	ESD test:	Pass	□ NG,		
10.	Others:	Pass	□ NG ,		
6 · <u>Summary</u> :					
	Sales signature :				