晶采光電科技股份有限公司 AMPIRE CO., LTD.

SPECIFICATIONS FOR LCD MODULE

CUSTOMER	
CUSTOMER PART NO.	
AMPIRE PART NO.	AM-800480RJTMQW-B0H
APPROVED BY	
DATE	

□Approved For Specifications

□ Approved For Specifications & Sample

AMPIRE CO., LTD.

4F., No.116, Sec. 1, Xintai 5th Rd., Xizhi Dist., New Taipei City 221, Taiwan (R.O.C.) 22181 新北市 汐止區新台五路一段 116 號 4F TEL:886-2-26967269 , FAX:886-2-26967196 or 26967270

APPROVED BY	CHECKED BY	ORGANIZED BY

RECORD OF REVISION

Revision Date	Page	Contents	Editor
2012/06/21		New Release	Emil

1. INTRODUCTION

Ampire Display Module is a color active matrix TFT-LCD that uses amorphous silicon TFT as a switching device . This model is composed of a TFT-LCD panel, timing controller . This TFT-LCD has a high resolution (800(R.G.B) X 480) and can display up to16.7M colors.

1-1. Features

• 7" WVGA (16:9 diagonal) configuration

• Input interface voltage: 3.3V

• Data enable mode

1-2. Applications

- Portable TV
- Car user DVD
- Industrial application
- HMI (Human machine interface)

2. PHYSICAL SPECIFICATIONS

Item	Specifications	unit
Display resolution(dot)	800RGB (W) x 480(H)	dots
Active area	152.4 (W) x 91.44 (H)	mm
Pixel pitch	0.1905 (W) x 0.1905 (H)	mm
Color configuration	R.G.B Vertical stripe	
Overall dimension	165.0(W)x104.44(H)X6.76(T)	mm
Brightness	500 nit	cd/m ²
Contrast ratio	400 : 1	
Backlight unit	LED	
Display color	16.7M	colors

3. ABSOLUTE MAX. RATINGS

ITEM	SYMBOL	MIN	MAX	UNIT
Power Supply Voltage for LCD	Vcc	-0.5	6.0	V
Signal input voltage	DCLK DE R0~R7 G0~G7 B0~B7	-0.5	VCC+0.3	>
Operation Temperature	Тор	-20	70	$^{\circ}\!\mathbb{C}$
Storage Temperature	Tstg	-30	80	$^{\circ}\!\mathbb{C}$

The following values are maximum operation conditions , If exceeded , it may cause faulty operation or damage $\,$

4. ELECTRICAL CHARACTERISTICS

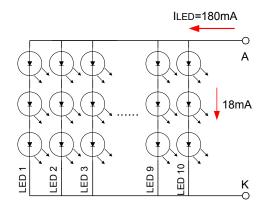
4-1 TFT LCD Module voltage

ITEM		SYMBOL	MIN	TYP	MAX	UNIT	CONDITION
Power Supply Voltage For LCD		Vcc	3.0	3.3	3.6	>	
Power S For LCD	upply Current	Icc	ı	170	220	mA	Black pattern
Logic	Input Voltage	V _{IN}	0	1	Vcc	٧	Logic Input Voltage
Input Voltage	Threshold Voltage(High)	V_{TH}	0.7Vcc	-	Vcc	V	
	Threshold Voltage(Low)	V_{TL}	0	ı	0.3Vcc	V	

4-2 LED B/L Driving Conditions

ITEM	SYMBOL	MIN	TYP	MAX	UNIT	CONDITION
LED Backlight Voltage	V_{AK}	-	9.6	-	V	I _{BL} = 180mA
LED Backlight Current	I _{AK}	-	180	-	mA	Ta=25°ℂ
LED Life Time			40K		Hr	Note*

Note* : Brightness to be decreased to 50% of the initial value.



5. INTERFACE

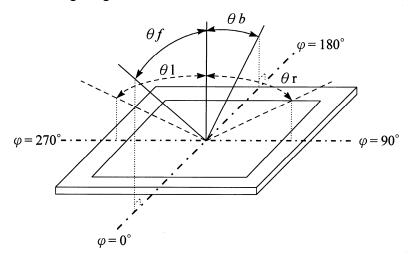
Pin No Symbol Function 1 Vss Power Ground 2 Vss Power Ground 3 Vcc Power Supply for LCD 4 Vcc Power Supply for LCD 5 PD16 Data 16 6 PD17 Data 17 7 PD18 Data 18 8 PD19 Data 19 9 PD20 Data 20 10 PD21 Data 21 11 PD22 Data 22 12 PD23 Data 23 13 PD8 Data 23 13 PD8 Data 8 14 PD9 Data 9 15 PD10 Data 10 16 PD11 Data 11 17 PD12 Data 12 18 PD13 Data 13 19 PD14 Data 15 21 PD0 Data 0 22 PD1 Data 1 23 PD2<		7102	
2 Vss Power Ground 3 Vcc Power Supply for LCD 4 Vcc Power Supply for LCD 5 PD16 Data 16 6 PD17 Data 17 7 PD18 Data 18 8 PD19 Data 19 9 PD20 Data 20 10 PD21 Data 21 11 PD22 Data 22 12 PD23 Data 23 13 PD8 Data 8 14 PD9 Data 23 13 PD8 Data 8 14 PD9 Data 9 15 PD10 Data 10 16 PD11 Data 11 17 PD12 Data 12 18 PD13 Data 13 19 PD14 Data 14 20 PD15 Data 15 21 PD0 Data 0 22 PD1 Data 1 23 PD2	Pin No	Symbol	Function
3	1	Vss	Power Ground
4 Vcc Power Supply for LCD 5 PD16 Data 16 6 PD17 Data 17 7 PD18 Data 18 8 PD19 Data 19 9 PD20 Data 20 10 PD21 Data 21 11 PD22 Data 21 11 PD22 Data 23 13 PD8 Data 8 14 PD9 Data 9 15 PD10 Data 9 15 PD10 Data 10 16 PD11 Data 11 17 PD12 Data 12 18 PD13 Data 13 19 PD14 Data 14 20 PD15 Data 15 21 PD0 Data 0 22 PD1 Data 1 23 PD2 Data 2 24 PD3 Data 3 25 PD4 Data 4 26 PD5 Data 5	2	Vss	Power Ground
5 PD16 Data 16 6 PD17 Data 17 7 PD18 Data 18 8 PD19 Data 19 9 PD20 Data 20 10 PD21 Data 21 11 PD22 Data 22 12 PD23 Data 23 13 PD8 Data 8 14 PD9 Data 8 14 PD9 Data 9 15 PD10 Data 10 16 PD11 Data 11 17 PD12 Data 12 18 PD13 Data 13 19 PD14 Data 14 20 PD15 Data 15 21 PD0 Data 0 22 PD1 Data 1 23 PD2 Data 2 24 PD3 Data 3 25 PD4 Data 4 26 PD5 Data 5 27 PD6 Data 6 <	3	Vcc	Power Supply for LCD
6 PD17 Data 17 7 PD18 Data 18 8 PD19 Data 20 10 PD20 Data 20 10 PD21 Data 21 11 PD22 Data 22 12 PD23 Data 23 13 PD8 Data 8 14 PD9 Data 9 15 PD10 Data 10 16 PD11 Data 11 17 PD12 Data 12 18 PD13 Data 13 19 PD14 Data 13 19 PD14 Data 14 20 PD15 Data 15 21 PD0 Data 0 22 PD1 Data 1 23 PD2 Data 2 24 PD3 Data 3 25 PD4 Data 4 26 PD5 Data 5 27 PD6 Data 6 28 PD7 Data 7	4	Vcc	Power Supply for LCD
7 PD18 Data 18 8 PD19 Data 19 9 PD20 Data 20 10 PD21 Data 21 11 PD22 Data 22 12 PD23 Data 23 13 PD8 Data 8 14 PD9 Data 9 15 PD10 Data 10 16 PD11 Data 11 17 PD12 Data 12 18 PD13 Data 13 19 PD14 Data 13 19 PD15 Data 15 21 PD0 Data 0 22 PD1 Data 1 23 PD2 Data 2 24 PD3 Data 3 25 PD4 Data 4 26 PD5 Data 6 28 PD7 Data 7 29 Vss Power Ground 30 DCLK Clock Signals 31 NC NC <td>5</td> <td>PD16</td> <td>Data 16</td>	5	PD16	Data 16
8 PD19 Data 19 9 PD20 Data 20 10 PD21 Data 21 11 PD22 Data 22 12 PD23 Data 23 13 PD8 Data 8 14 PD9 Data 8 14 PD9 Data 9 15 PD10 Data 10 16 PD11 Data 11 17 PD12 Data 12 18 PD13 Data 12 19 PD14 Data 14 20 PD15 Data 15 21 PD0 Data 0 22 PD1 Data 1 23 PD2 Data 2 24 PD3 Data 3 25 PD4 Data 4 26 PD5 Data 5 27 PD6 Data 6 28 PD7 Data 7 29 Vs Power Ground 30 DCLK Clock Signals <	6	PD17	Data 17
9 PD20 Data 20 10 PD21 Data 21 11 PD22 Data 22 12 PD23 Data 23 13 PD8 Data 8 14 PD9 Data 9 15 PD10 Data 10 16 PD11 Data 11 17 PD12 Data 12 18 PD13 Data 13 19 PD14 Data 14 20 PD15 Data 15 21 PD0 Data 0 22 PD1 Data 1 23 PD2 Data 2 24 PD3 Data 3 25 PD4 Data 4 26 PD5 Data 5 27 PD6 Data 6 28 PD7 Data 7 29 Vss Power Ground 30 DCLK Clock Signals 31 NC NC 32 Hsync Honizontal SYNC. (Sy	7	PD18	Data 18
10 PD21 Data 21 11 PD22 Data 22 12 PD23 Data 23 13 PD8 Data 8 14 PD9 Data 8 15 PD10 Data 10 16 PD11 Data 11 17 PD12 Data 12 18 PD13 Data 13 19 PD14 Data 13 19 PD15 Data 15 21 PD0 Data 0 22 PD1 Data 1 23 PD2 Data 2 24 PD3 Data 3 25 PD4 Data 4 26 PD5 Data 5 27 PD6 Data 6 28 PD7 Data 7 29 Vss Power Ground 30 DCLK Clock Signals 31 NC NC 32 Hsync Honizontal SYNC. (Sync mode used) 34 DE	8	PD19	Data 19
11 PD22 Data 22 12 PD23 Data 23 13 PD8 Data 8 14 PD9 Data 9 15 PD10 Data 10 16 PD11 Data 11 17 PD12 Data 12 18 PD13 Data 13 19 PD14 Data 14 20 PD15 Data 15 21 PD0 Data 0 22 PD1 Data 1 23 PD2 Data 2 24 PD3 Data 3 25 PD4 Data 4 26 PD5 Data 5 27 PD6 Data 6 28 PD7 Data 7 29 Vss Power Ground 30 DCLK Clock Signals 31 NC NC 32 Hsync Honizontal SYNC. (Sync mode used) 33 Vsync Vertical SYNC. (Sync mode used) 34	9	PD20	Data 20
12 PD23 Data 23 13 PD8 Data 8 14 PD9 Data 9 15 PD10 Data 10 16 PD11 Data 11 17 PD12 Data 12 18 PD13 Data 13 19 PD14 Data 14 20 PD15 Data 15 21 PD0 Data 0 22 PD1 Data 1 23 PD2 Data 2 24 PD3 Data 3 25 PD4 Data 4 26 PD5 Data 5 27 PD6 Data 6 28 PD7 Data 7 29 Vss Power Ground 30 DCLK Clock Signals 31 NC NC 32 Hsync Honizontal SYNC. (Sync mode used) 33 Vsync Vertical SYNC. (Sync mode used) 34 DE Data Enable 35	10	PD21	Data 21
13 PD8 Data 8 14 PD9 Data 9 15 PD10 Data 10 16 PD11 Data 11 17 PD12 Data 12 18 PD13 Data 13 19 PD14 Data 14 20 PD15 Data 15 21 PD0 Data 0 22 PD1 Data 1 23 PD2 Data 2 24 PD3 Data 3 25 PD4 Data 4 26 PD5 Data 5 27 PD6 Data 6 28 PD7 Data 7 29 Vss Power Ground 30 DCLK Clock Signals 31 NC NC 32 Hsync Honizontal SYNC. (Sync mode used) 34 DE Data Enable 35 LED_A LED anode 36 LED_A LED anode 37 Vss <td>11</td> <td>PD22</td> <td>Data 22</td>	11	PD22	Data 22
14 PD9 Data 9 15 PD10 Data 10 16 PD11 Data 11 17 PD12 Data 12 18 PD13 Data 13 19 PD14 Data 14 20 PD15 Data 15 21 PD0 Data 0 22 PD1 Data 1 23 PD2 Data 2 24 PD3 Data 3 25 PD4 Data 4 26 PD5 Data 5 27 PD6 Data 6 28 PD7 Data 7 29 Vss Power Ground 30 DCLK Clock Signals 31 NC NC 32 Hsync Honizontal SYNC. (Sync mode used) 34 DE Data Enable 35 LED_A LED anode 36 LED_A LED anode 37 Vss Power Ground 38 V	12	PD23	Data 23
15 PD10 Data 10 16 PD11 Data 11 17 PD12 Data 12 18 PD13 Data 13 19 PD14 Data 14 20 PD15 Data 15 21 PD0 Data 0 22 PD1 Data 1 23 PD2 Data 2 24 PD3 Data 3 25 PD4 Data 4 26 PD5 Data 5 27 PD6 Data 6 28 PD7 Data 7 29 Vss Power Ground 30 DCLK Clock Signals 31 NC NC 32 Hsync Honizontal SYNC. (Sync mode used) 33 Vsync Vertical SYNC. (Sync mode used) 34 DE Data Enable 35 LED_A LED anode 36 LED_A LED anode 37 Vss Power Ground	13	PD8	Data 8
16 PD11 Data 11 17 PD12 Data 12 18 PD13 Data 13 19 PD14 Data 14 20 PD15 Data 15 21 PD0 Data 0 22 PD1 Data 1 23 PD2 Data 2 24 PD3 Data 3 25 PD4 Data 4 26 PD5 Data 5 27 PD6 Data 6 28 PD7 Data 7 29 Vss Power Ground 30 DCLK Clock Signals 31 NC NC 32 Hsync Honizontal SYNC. (Sync mode used) 33 Vsync Vertical SYNC. (Sync mode used) 34 DE Data Enable 35 LED_A LED anode 36 LED_A LED anode 37 Vss Power Ground 38 Vss Power Ground	14	PD9	Data 9
17 PD12 Data 12 18 PD13 Data 13 19 PD14 Data 14 20 PD15 Data 15 21 PD0 Data 0 22 PD1 Data 1 23 PD2 Data 2 24 PD3 Data 3 25 PD4 Data 4 26 PD5 Data 5 27 PD6 Data 6 28 PD7 Data 7 29 Vss Power Ground 30 DCLK Clock Signals 31 NC NC 32 Hsync Honizontal SYNC. (Sync mode used) 34 DE Data Enable 35 LED_A LED anode 36 LED_A LED anode 37 Vss Power Ground 38 Vss Power Ground 39 LED_K LED cathode	15	PD10	Data 10
18 PD13 Data 13 19 PD14 Data 14 20 PD15 Data 15 21 PD0 Data 0 22 PD1 Data 1 23 PD2 Data 2 24 PD3 Data 3 25 PD4 Data 4 26 PD5 Data 5 27 PD6 Data 6 28 PD7 Data 7 29 Vss Power Ground 30 DCLK Clock Signals 31 NC NC 32 Hsync Honizontal SYNC. (Sync mode used) 34 DE Data Enable 35 LED_A LED anode 36 LED_A LED anode 37 Vss Power Ground 38 Vss Power Ground 39 LED_K LED cathode	16	PD11	Data 11
19 PD14 Data 14 20 PD15 Data 15 21 PD0 Data 0 22 PD1 Data 1 23 PD2 Data 2 24 PD3 Data 3 25 PD4 Data 4 26 PD5 Data 5 27 PD6 Data 6 28 PD7 Data 7 29 Vss Power Ground 30 DCLK Clock Signals 31 NC NC 32 Hsync Honizontal SYNC. (Sync mode used) 33 Vsync Vertical SYNC. (Sync mode used) 34 DE Data Enable 35 LED_A LED anode 36 LED_A LED anode 37 Vss Power Ground 38 Vss Power Ground 39 LED_K LED cathode	17	PD12	Data 12
20 PD15 Data 15 21 PD0 Data 0 22 PD1 Data 1 23 PD2 Data 2 24 PD3 Data 3 25 PD4 Data 4 26 PD5 Data 5 27 PD6 Data 6 28 PD7 Data 7 29 Vss Power Ground 30 DCLK Clock Signals 31 NC NC 32 Hsync Honizontal SYNC. (Sync mode used) 34 DE Data Enable 35 LED_A LED anode 36 LED_A LED anode 37 Vss Power Ground 38 Vss Power Ground 39 LED_K LED cathode	18	PD13	Data 13
21 PD0 Data 0 22 PD1 Data 1 23 PD2 Data 2 24 PD3 Data 3 25 PD4 Data 4 26 PD5 Data 5 27 PD6 Data 6 28 PD7 Data 7 29 Vss Power Ground 30 DCLK Clock Signals 31 NC NC 32 Hsync Honizontal SYNC. (Sync mode used) 33 Vsync Vertical SYNC. (Sync mode used) 34 DE Data Enable 35 LED_A LED anode 36 LED_A LED anode 37 Vss Power Ground 38 Vss Power Ground 39 LED_K LED cathode	19	PD14	Data 14
22 PD1 Data 1 23 PD2 Data 2 24 PD3 Data 3 25 PD4 Data 4 26 PD5 Data 5 27 PD6 Data 6 28 PD7 Data 7 29 Vss Power Ground 30 DCLK Clock Signals 31 NC NC 32 Hsync Honizontal SYNC. (Sync mode used) 33 Vsync Vertical SYNC. (Sync mode used) 34 DE Data Enable 35 LED_A LED anode 36 LED_A LED anode 37 Vss Power Ground 38 Vss Power Ground 39 LED_K LED cathode	20	PD15	Data 15
23 PD2 Data 2 24 PD3 Data 3 25 PD4 Data 4 26 PD5 Data 5 27 PD6 Data 6 28 PD7 Data 7 29 Vss Power Ground 30 DCLK Clock Signals 31 NC NC 32 Hsync Honizontal SYNC. (Sync mode used) 33 Vsync Vertical SYNC. (Sync mode used) 34 DE Data Enable 35 LED_A LED anode 36 LED_A LED anode 37 Vss Power Ground 38 Vss Power Ground 39 LED_K LED cathode	21	PD0	Data 0
24 PD3 Data 3 25 PD4 Data 4 26 PD5 Data 5 27 PD6 Data 6 28 PD7 Data 7 29 Vss Power Ground 30 DCLK Clock Signals 31 NC NC 32 Hsync Honizontal SYNC. (Sync mode used) 33 Vsync Vertical SYNC. (Sync mode used) 34 DE Data Enable 35 LED_A LED anode 36 LED_A LED anode 37 Vss Power Ground 38 Vss Power Ground 39 LED_K LED cathode	22	PD1	Data 1
25 PD4 Data 4 26 PD5 Data 5 27 PD6 Data 6 28 PD7 Data 7 29 Vss Power Ground 30 DCLK Clock Signals 31 NC NC 32 Hsync Honizontal SYNC. (Sync mode used) 33 Vsync Vertical SYNC. (Sync mode used) 34 DE Data Enable 35 LED_A LED anode 36 LED_A LED anode 37 Vss Power Ground 38 Vss Power Ground 39 LED_K LED cathode	23	PD2	Data 2
26 PD5 Data 5 27 PD6 Data 6 28 PD7 Data 7 29 Vss Power Ground 30 DCLK Clock Signals 31 NC NC 32 Hsync Honizontal SYNC. (Sync mode used) 33 Vsync Vertical SYNC. (Sync mode used) 34 DE Data Enable 35 LED_A LED anode 36 LED_A LED anode 37 Vss Power Ground 38 Vss Power Ground 39 LED_K LED cathode	24	PD3	Data 3
27 PD6 Data 6 28 PD7 Data 7 29 Vss Power Ground 30 DCLK Clock Signals 31 NC NC 32 Hsync Honizontal SYNC. (Sync mode used) 33 Vsync Vertical SYNC. (Sync mode used) 34 DE Data Enable 35 LED_A LED anode 36 LED_A LED anode 37 Vss Power Ground 38 Vss Power Ground 39 LED_K LED cathode	25	PD4	Data 4
28 PD7 Data 7 29 Vss Power Ground 30 DCLK Clock Signals 31 NC NC 32 Hsync Honizontal SYNC. (Sync mode used) 33 Vsync Vertical SYNC. (Sync mode used) 34 DE Data Enable 35 LED_A LED anode 36 LED_A LED anode 37 Vss Power Ground 38 Vss Power Ground 39 LED_K LED cathode	26	PD5	Data 5
29 Vss Power Ground 30 DCLK Clock Signals 31 NC NC 32 Hsync Honizontal SYNC. (Sync mode used) 33 Vsync Vertical SYNC. (Sync mode used) 34 DE Data Enable 35 LED_A LED anode 36 LED_A LED anode 37 Vss Power Ground 38 Vss Power Ground 39 LED_K LED cathode	27	PD6	Data 6
30 DCLK Clock Signals 31 NC NC 32 Hsync Honizontal SYNC. (Sync mode used) 33 Vsync Vertical SYNC. (Sync mode used) 34 DE Data Enable 35 LED_A LED anode 36 LED_A LED anode 37 Vss Power Ground 38 Vss Power Ground 39 LED_K LED cathode	28	PD7	Data 7
31 NC NC 32 Hsync Honizontal SYNC. (Sync mode used) 33 Vsync Vertical SYNC. (Sync mode used) 34 DE Data Enable 35 LED_A LED anode 36 LED_A LED anode 37 Vss Power Ground 38 Vss Power Ground 39 LED_K LED cathode	29	Vss	Power Ground
32 Hsync Honizontal SYNC. (Sync mode used) 33 Vsync Vertical SYNC. (Sync mode used) 34 DE Data Enable 35 LED_A LED anode 36 LED_A LED anode 37 Vss Power Ground 38 Vss Power Ground 39 LED_K LED cathode	30	DCLK	Clock Signals
33 Vsync Vertical SYNC. (Sync mode used) 34 DE Data Enable 35 LED_A LED anode 36 LED_A LED anode 37 Vss Power Ground 38 Vss Power Ground 39 LED_K LED cathode	31	NC	NC
33 Vsync Vertical SYNC. (Sync mode used) 34 DE Data Enable 35 LED_A LED anode 36 LED_A LED anode 37 Vss Power Ground 38 Vss Power Ground 39 LED_K LED cathode	32	Hsync	Honizontal SYNC. (Sync mode used)
35 LED_A LED anode 36 LED_A LED anode 37 Vss Power Ground 38 Vss Power Ground 39 LED_K LED cathode	33	Vsync	
36 LED_A LED anode 37 Vss Power Ground 38 Vss Power Ground 39 LED_K LED cathode	34	DE	Data Enable
37 Vss Power Ground 38 Vss Power Ground 39 LED_K LED cathode	35	LED_A	LED anode
38 Vss Power Ground 39 LED_K LED cathode	36	LED_A	LED anode
39 LED_K LED cathode	37	Vss	Power Ground
_	38	Vss	Power Ground
40 LED K LED cathode	39	LED_K	LED cathode
.o LED_IX LED OUTIONO	40	LED_K	LED cathode

6. OPTICAL CHARACTERISTICS

Item		Symbol	Condition	Min.	Тур.	Max.	Unit	Note
	Front	θf		55	60			
Viewing	Back	θb	CD > 10	55	60		dog	Viewing
Angle	Left	θΙ	- CR≥10	65	70		deg.	Angle
	Right	θr		65	70			
Contrast ratio		CR	Θ=Φ=0°	250	400			(1)(3)
Response Tin	20	T _r	0 + 00		5	10	ms	(1)(4)
Response III	ile	T_f			11	16	ms	(1)(4)
Color	\	Wx	Θ=Ф=0°	0.239	0.299	0.359		Color
chromaticity	White	Wy		0.268	0.328	0.388		chromati city
Luminance		L	Θ=Φ=0°	400	500		cd/m ²	(1)(5)
Luminance Uniformity		ΔL	Θ=Ф=0°	70			%	(1)(5)(6)

Note 1: Ta=25°C. To be measured on the center area of panel after 10 minutes operation.

Note 2: Definition of Viewing Angle



Note 3: Definition of contrast ratio:

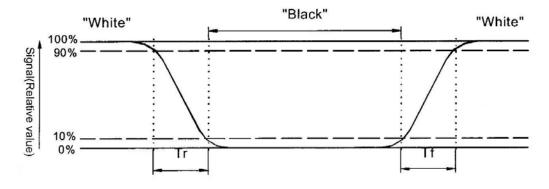
Date: 2012/06/26

Contrast ratio is calculated with the following formula.

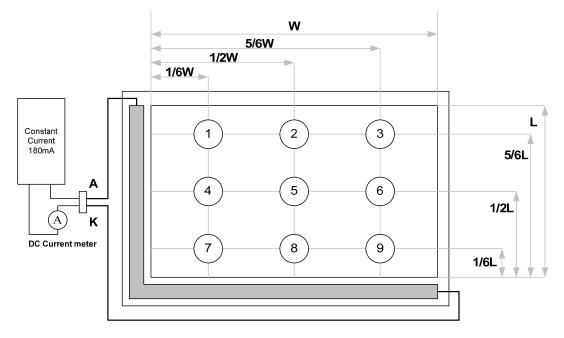
Contrast ratio(CR)= Photo detector output when LCD is at "White" state
Photo detector Output when LCD is at "Black" state

Note 4: Definition of response time:

The output signals of photo detector are measured when the input signals are changed from "black" to "white" (falling time) and from "white" to "black" (rising time) respectively. The response time is defined as the time interval between the 10% and 90% of amplitudes. Refer to figure as below.



Note 5: Luminance is measured at point 5 of the display.



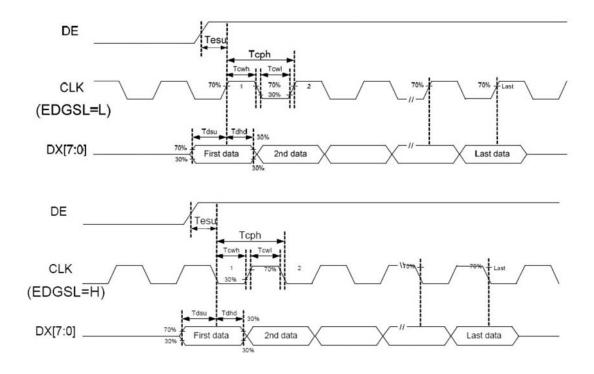
Note 6: Definition of Luminance Uniformity

 $\Delta L = [L(min.) \text{ of 9 points} / L(max.) \text{ of 9 points}] X 100\%$

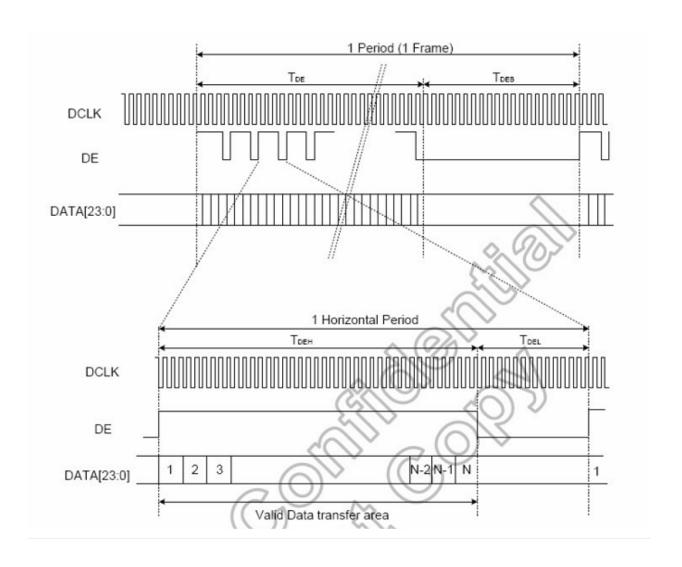
7. INPUT SIGNAL (DE ONLY MODE)

Parameter	Symbol				Unit
Parameter	Symbol	Min.	Тур.	Max.	Offic
Data setup time	Tdsu	6	-	-	ns
Data hold time	Tdhd	6	-	-	Tcph
DE setup time	Tesu	6	-	-	Tcph
CLK frequency	F срн		33.26		MHz
CLK period	Тсрн		30.06		ns
CLK pulse duty	Tcwh	40	50	60	%
DE period	TDEH+TDEL	1000	1056	1200	Тсрн
DE pulse width	TDEH	1	800	-	Тсрн
DE frame blanking	TDEB	10	45	110	TDEH+TDEL
DE frame width	TDE	-	480	-	TDEH+TDEL

Note: We suggest using the typical value, so it can have better performance.



Date: 2012/06/26



8. INCOMING INSPECTION STANDARD FOR TFT-LCD PANEL

Reliability Test Items

Test Item	Test Conditions	Note
High Temperature Operation	70±3°C , t=240 hrs	
Low Temperature Operation	-20±3°C , t=240 hrs	
High Temperature Storage	80±3°C , t=240 hrs	1,2
Low Temperature Storage	-30±3°C , t=240 hrs	1,2
Storage at High Temperature and Humidity	60°C, 90% RH , 240 hrs	1,2
Thermal Shock Test	-20°C (30min) ~ 70°C (30min) 100 cycles	1,2
Vibration Test (Packing)	Sweep frequency: 10 ~ 55 ~ 10 Hz/1min Amplitude: 0.75mm Test direction: X.Y.Z/3 axis Duration: 30min/each axis	2

Note 1: Condensation of water is not permitted on the module.

Note 2 : The module should be inspected after 1 hour storage in normal conditions (15-35 $^{\circ}$ C , 45-65 $^{\circ}$ RH).

9. USE PRECAUTIONS

9-1 Handling precautions

- (1) The polarizing plate may break easily so be careful when handling it. Do not touch, press or rub it with a hard-material tool like tweezers.
- (2) Do not touch the polarizing plate surface with bare hands so as not to make it dirty. If the surface or other related part of the polarizing plate is dirty, soak a soft cotton cloth or chamois leather in benzine and wipe off with it. Do not use chemical liquids such as acetone, toluene and isopropyl alcohol. Failure to do so may bring chemical reaction phenomena and deteriorations.
- (3) Remove any spit or water immediately. If it is left for hours, the suffered part may deform or decolorize.
- (1) If the LCD element breaks and any LC stuff leaks, do not suck or lick it. Also if LC stuff is stuck on your skin or clothing, wash thoroughly with soap and water immediately.

9-2 Installing precautions

- (1) The PCB has many ICs that may be damaged easily by static electricity. To prevent breaking by static electricity from the human body and clothing, earth the human body properly using the high resistance and discharge static electricity during the operation. In this case, however, the resistance value should be approx. $1M\Omega$ and the resistance should be placed near the human body rather than the ground surface. When the indoor space is dry, static electricity may occur easily so be careful. We recommend the indoor space should be kept with humidity of 60% or more. When a soldering iron or other similar tool is used for assembly, be sure to earth it.
- (2) When installing the module and ICs, do not bend or twist them. Failure to do so may crack LC element and cause circuit failure.
- (3) To protect LC element, especially polarizing plate, use a transparent protective plate (e.g., acrylic plate, glass etc) for the product case.
- (4) Do not use an adhesive like a both-side adhesive tape to make LCD surface (polarizing plate) and product case stick together. Failure to do so may cause the polarizing plate to peel off

9-3 Storage precautions

- (1) Avoid a high temperature and humidity area. Keep the temperature between 0°C and 35°C and also the humidity under 60%.
- (2) Choose the dark spaces where the product is not exposed to direct sunlight or fluorescent light.
- (3) Store the products as they are put in the boxes provided from us or in the same conditions as we recommend.

9-4 Operating precautions

- (1) Do not boost the applied drive voltage abnormally. Failure to do so may break ICs. When applying power voltage, check the electrical features beforehand and be careful. Always turn off the power to the LC module controller before removing or inserting the LC module input connector. If the input connector is removed or inserted while the power is turned on, the LC module internal circuit may break.
- (2) The display response may be late if the operating temperature is under the normal standard, and the display may be out of order if it is above the normal standard. But this is not a failure; this will be restored if it is within the normal standard.
- (3) The LCD contrast varies depending on the visual angle, ambient temperature, power voltage etc. Obtain the optimum contrast by adjusting the LC dive voltage.
- (4) When carrying out the test, do not take the module out of the low-temperature space suddenly. Failure to do so will cause the module condensing, leading to malfunctions.
- (5) Make certain that each signal noise level is within the standard (L level: 0.2Vdd or less and H level: 0.8Vdd or more) even if the module has functioned properly. If it is beyond the standard, the module may often malfunction. In addition, always connect the module when making noise level measurements.
- (6) The CMOS ICs are incorporated in the module and the pull-up and pull-down function is not adopted for the input so avoid putting the input signal open while the power is ON.
- (7) The characteristic of the semiconductor element changes when it is exposed to light emissions, therefore ICs on the LCD may malfunction if they receive light emissions. To prevent these malfunctions, design and assemble ICs so that they are shielded from light emissions.
- (8) Crosstalk occurs because of characteristics of the LCD. In general, crosstalk occurs when the regularized display is maintained. Also, crosstalk is affected by the LC drive voltage. Design the contents of the display, considering crosstalk.

9-5 Other

- (1) Do not disassemble or take the LC module into pieces. The LC modules once disassembled or taken into pieces are not the guarantee articles.
- (2) The residual image may exist if the same display pattern is shown for hours. This residual image, however, disappears when another display pattern is shown or the drive is interrupted and left for a while. But this is not a problem on reliability.
- (3) AMIPRE will provide one year warrantee for all products and three months warrantee for all repairing products..

10. OUTLINE DIMENSION

