晶采光電科技股份有限公司
AMPIRE CO., LTD.

SPECIFICATIONS FOR LCD MODULE

CUSTOMER	
CUSTOMER PART NO.	
AMPIRE PART NO.	AM-800480D1TMQW-T00H
APPROVED BY	
DATE	

☐ Preliminary Specification
☐ Approved Specification

AMPIRE CO., LTD.
4F., No.116, Sec. 1, Xintai 5th Rd., Xizhi Dist., New Taipei City221, Taiwan (R.O.C.)
新北市汐止區新台五路一段 **116** 號 **4** 樓**(**東方科學園區 **A** 棟**)**

APPROVED BY	CHECKED BY	ORGANIZED BY

RECORD OF REVISION

Revision Date	Page	Contents	Editor
2012/10/19	-	New Release	Bob

1. Features

8 inch Amorphous-TFT-LCD (Thin Film Transistor Liquid Crystal Display) module. This module is composed of a 8" TFT-LCD panel and backlight unit.

(1) Construction: 8" a-Si TFT active matrix, White LED Backlight and Touch Panel.
(2) Resolution (pixel): 800(R.G.B) X 480
(3) Number of the Colors : 16.7M (R , G , B 8 bit digital each)
(4) LCD type : Transmissive , normally White
(5) Interface: 50 pin
(6) Touch Panel Included
Viewing Direction: 6 O'clock (Gray Inversion)

2. PHYSICAL SPECIFICATIONS

No.	Item	Specification	Remark
1	LCD size	8.0 inch(Diagonal)	
2	Driver element	a-Si TFT active matrix	
3	Resolution	800 X 3(RGB) X 480	
4	Display mode	Normally White, Transmissive	
5	Dot pitch	0.0736(W) X 0.2070(H) mm	
6	Active area	176.64(W) X 99.36(H) mm	
7	Module size	192.8(W) X 116.9(H) X **7.96(D)** mm	Note 1
8	Color arrangement	RGB-stripe	
9	Interface	Digital	
10	Backlight Power consumption	2.232 W (Typ)	
11	Panel Power consumption	0.226 W (Typ)	

Note 1: Refer to Mechanical Drawing.

3. ABSOLUTE MAX. RATINGS

(GND=AV_{SS}=0V, Note 1)

Item	Symbol	Values		Unit	Remark
		Min.	Max.		
Power voltage	V_{CC}	-0.3	5.0	V	
	AV_{DD}	6.5	13.5	V	
	V_{GH}	-0.3	40.0	V	
	V_{GL}	-20.0	0.3	V	
	V_{GH}-V_{GL}	-	40.0	V	
Operation Temperature	T_{OP}	-20	60	℃	
Storage Temperature	T_{ST}	-30	70	℃	
LED Reverse Voltage	Vr	-	1.2	V	Each LED Note 2
LED Forward Current	I_f	-	25	mA	Each LED

Note 1: The absolute maximum rating values of this product are not allowed to be exceeded at any times. Should a module be used with any of the absolute maximum ratings exceeded, the characteristics of the module may not be recovered, or in an extreme case, the module may be permanently destroyed.

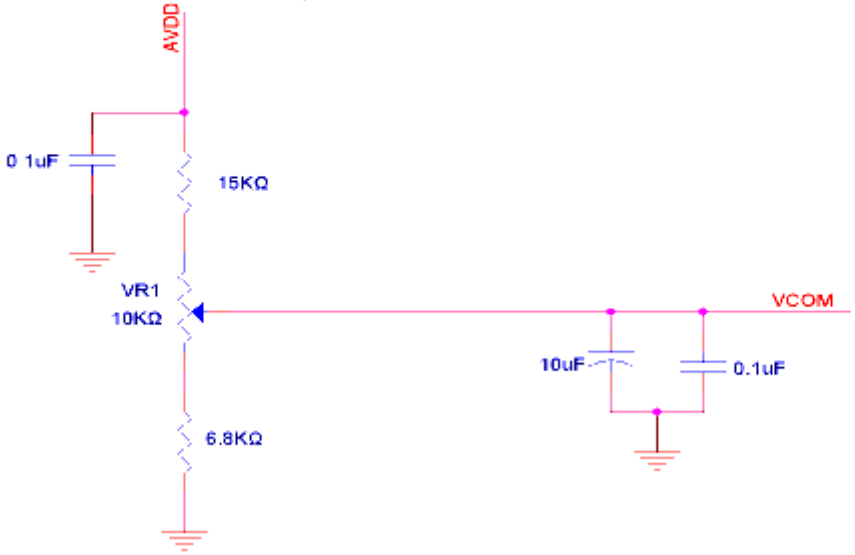
Note 2: V_R Conditions: Zener Diode 20mA

4. ELECTRICAL CHARACTERISTICS

4-1 Typical Operation Conditions

Item	Symbol	Values			Unit	Remark
		Min.	Typ.	Max.		
Power voltage	DV_{DD}	3.0	3.3	3.6	V	Note 2
	AV_{DD}	10.2	10.4	10.6	V	
	V_{GH}	15.3	16.0	16.7	V	
	V_{GL}	-7.7	-7.0	-6.3	V	
Input signal voltage	V_{COM}	3.4	4.4	5.4	V	Note 4
Input logic high voltage	V_{IH}	0.7 DV_{DD}	-	DV_{DD}	V	Note 3
Input logic low voltage	V_{IL}	0	-	0.3 DV_{DD}	V	

Note 1: Be sure to apply DV_{DD} and V_{GL} to the LCD first, and then apply V_{GH}.
Note 2: DV_{DD} setting should match the signals output voltage (refer to Note 3) of customer's system board.
Note 3: DCLK,HS,VS,RESET,U/D, L/R,DE,R0~R7,G0~G7,B0~B7,MODE,DITHB.
Note 4: Typ. Vcom is only a reference value, it must be optimized according to each LCM. Be sure to use VR;



4-2 Current Consumption

Item	Symbol	Values			Unit	Remark
		Min.	Typ.	Max.		
Current for Driver	I_{GH}	-	0.2	1	mA	
	I_{GL}	-	0.2	1	mA	
	IDV_{DD}	-	4	10	mA	
	IAV_{DD}	-	20	50	mA	

(GND = AVSS = 0V)

4-3 Backlight Driving Conditions

Item	Symbol	Values			Unit	Remark
		Min.	Typ.	Max.		
Voltage for LED backlight	VL	8.4	9.3	10.2	V	Note 1
Current for LED backlight	I_L	216	240	264	mA	
LED life time	-	20,000	-	-	Hr	Note 2

Note 1: The LED Supply Voltage is defined by the number of LED at Ta=25 C and I_L =240mA.

Note 2: The "LED life time" is defined as the module brightness decrease to 50% original brightness at Ta=25 C and I_L =240mA. The LED lifetime could be decreased if operating I_L is lager than 240 mA.

5. Optical Specifications

Item	Symbol	Condition	Min.	Typ.	Max.	Unit	Remark
Viewing Angle (CR≧10)	θ_L	Φ=180°(9'clock)	60	70	--	deg.	Note 1
	θ_R	Φ=0°(3'clock)	60	70	--		
	θ_T	Φ=90°(12'clock)	40	50	--		
	θ_B	Φ=270°(6'clock)	60	70	--		
Contrast ratio	CR	Θ=Φ=0°	400	500	--	--	Note 4
Response Time	T_{on}	Θ=Φ=0°	--	10	20	ms	Note 3
	T_{off}		--	15	30	ms	
Color chromaticity	Wx		0.26	0.31	0.36	--	Note 2,5,6
	Wy		0.28	0.33	0.38		
Luminance	L	Θ=Φ=0°	288	360	--	cd/m^2	Note 6
Luminance Uniformity	Yu	Θ=Φ=0°	70	75	--	%	Note 7

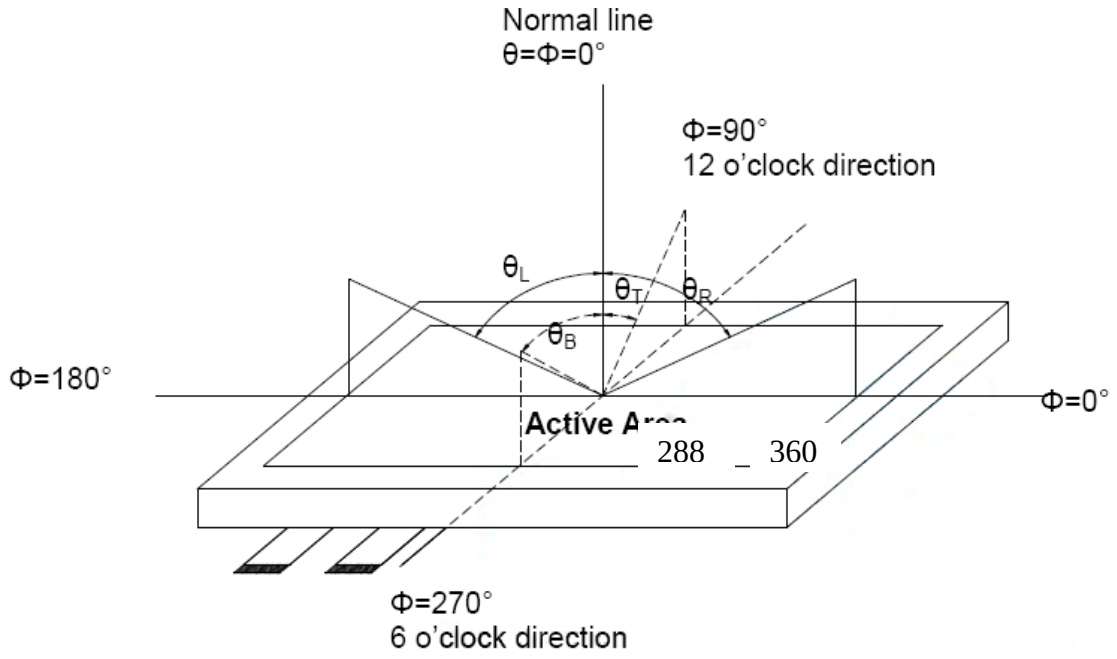
Test Conditions:

VCC = 3.3V, AVDD = 10V, IL = 240mA(Backlight current).

The ambient temperature is 25°C

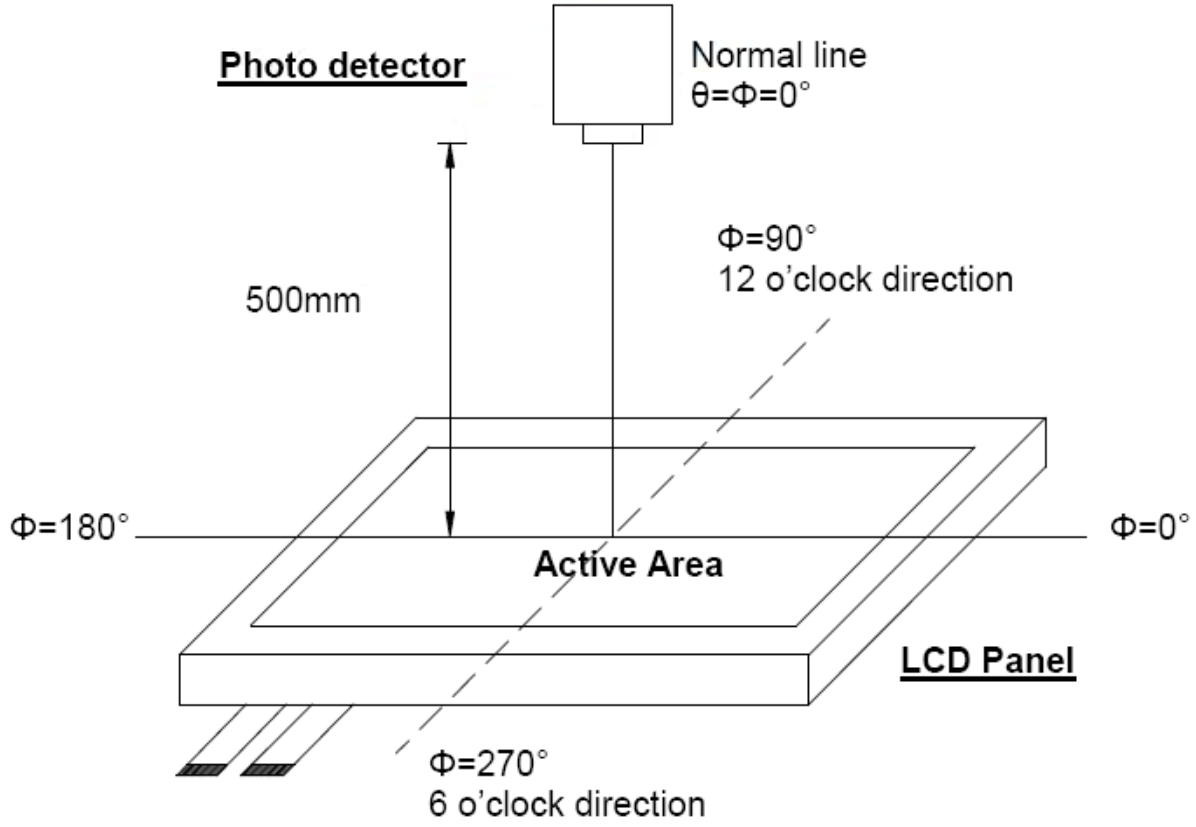
The test systems refer to Note2

Note 1 : Definition of viewing angle range



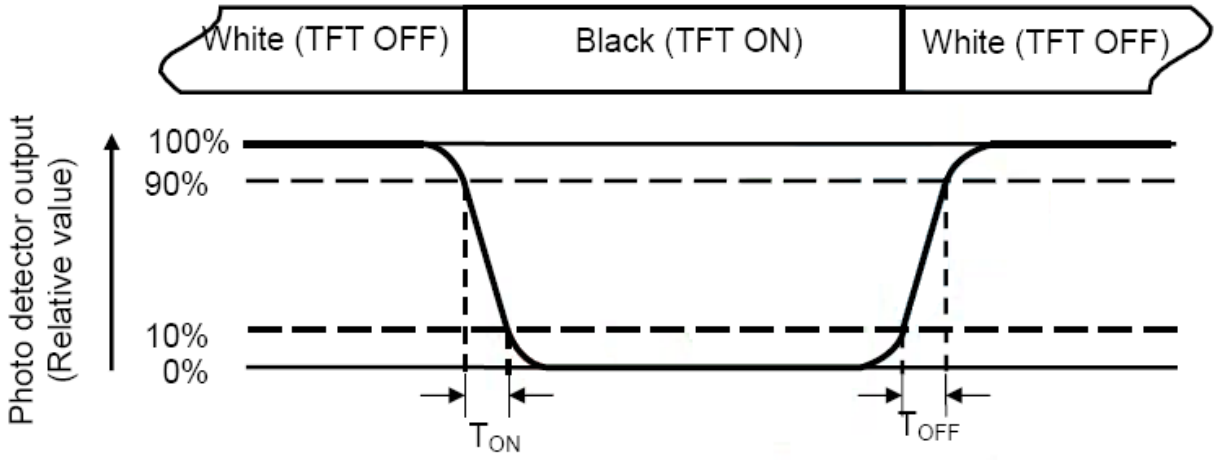
Note 2 : Definition of optical measurement system.

The optical characteristics should be measured in dark room. After 30 minutes operation, the optical properties are measured at the center point of the LCD screen. (Response time is measured by Photo detector TOPCON BM-7, other items are measured by BM-5A/Field of view : 1° / Height : 500mm.)



Note 3 : Definition of Response time

The response time is defined as the LCD optical switching time interval between "White" state and "Black" state. Rise time (T_{ON}) is the time between photo detector output intensity changed from 90% to 10%. And fall time (T_{OFF}) is the time between photo detector output intensity changed from 10% to 90%.



Note 4 : Definition of contrast ratio

$$\text{Contrast ratio (CR)} = \frac{\text{Luminance measured when LCD on the “White” state}}{\text{Luminance measured when LCD on the “Black” state}}$$

Note 5 : Definition of color chromaticity (CIE1931)

Color coordinated measured at center point of LCD.

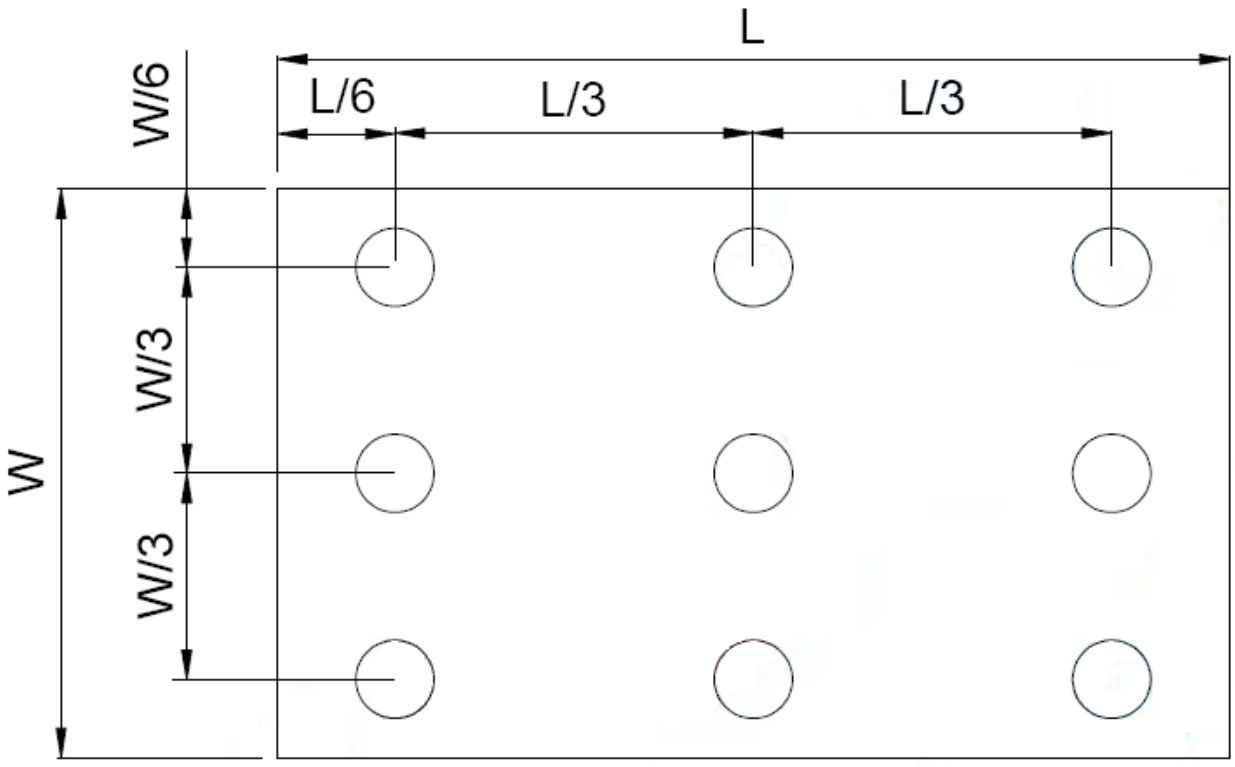
Note 6 : All input terminals LCD panel must be ground when measuring the center area of the panel.

Note 7 : Definition of Luminance Uniformity

Active area is divided into 9 measuring areas (Refer to bellow figure). Every measuring point is placed at the center of each measuring area.

$$\text{Luminance Uniformity (Yu)} = \frac{B_{min}}{B_{max}}$$

L ----- Active area length W ----- Active area width



B_{max} : The measured maximum luminance of all measurement position.

B_{min} : The measured minimum luminance of all measurement position.

6. INTERFACE

FPC Connector is used for the module electronics interface. The recommended model is FH12A-50S-0.5SH manufactured by Hirose.

Pin No.	Symbol	I/O	Function	Remark
1	V_{LED+}	P	Power for LED backlight (Anode)	
2	V_{LED+}	P	Power for LED backlight (Anode)	
3	V_{LED-}	P	Power for LED backlight (Cathode)	
4	V_{LED-}	P	Power for LED backlight (Cathode)	
5	GND	P	Power ground	
6	V_{COM}	I	Common voltage	
7	DV_{DD}	P	Power for Digital Circuit	
8	MODE	I	DE/SYNC mode select	Note 1
9	DE	I	Data Input Enable	
10	VS	I	Vertical Sync Input	
11	HS	I	Horizontal Sync Input	
12	B7	I	Blue data(MSB)	
13	B6	I	Blue data	
14	B5	I	Blue data	
15	B4	I	Blue data	
16	B3	I	Blue data	
17	B2	I	Blue data	
18	B1	I	Blue data	Note 2
19	B0	I	Blue data(LSB)	Note 2
20	G7	I	Green data(MSB)	
21	G6	I	Green data	
22	G5	I	Green data	
23	G4	I	Green data	
24	G3	I	Green data	

I : input, O : output, P : power

25	G2	I	Green data	
26	G1	I	Green data	Note 2
27	G0	I	Green data(LSB)	Note 2
28	R7	I	Red data(MSB)	
29	R6	I	Red data	
30	R5	I	Red data	
31	R4	I	Red data	
32	R3	I	Red data	
33	R2	I	Red data	
34	R1	I	Red data	Note 2
35	R0	I	Red data(LSB)	Note 2
36	GND	P	Power Ground	
37	DCLK	I	Sample clock	Note 3
38	GND	P	Power Ground	
39	L/R	I	Left / right selection	Note 4,5
40	U/D	I	Up/down selection	Note 4,5
41	V_{GH}	P	Gate ON Voltage	
42	V_{GL}	P	Gate OFF Voltage	
43	AV_{DD}	P	Power for Analog Circuit	
44	RESET	I	Global reset pin.	Note 6
45	NC	-	No connection	
46	V_{COM}	I	Common Voltage	
47	DITHB	I	Dithering function	Note 7
48	GND	P	Power Ground	
49	NC	-	No connection	
50	NC	-	No connection	

I: input, O: output, P: Power

Note 1: DE/SYNC mode select. Normally pull high.
When select DE mode, MODE="1", VS and HS can pull high or be ground.
When select SYNC mode, MODE= "0", DE can pull high or be ground.

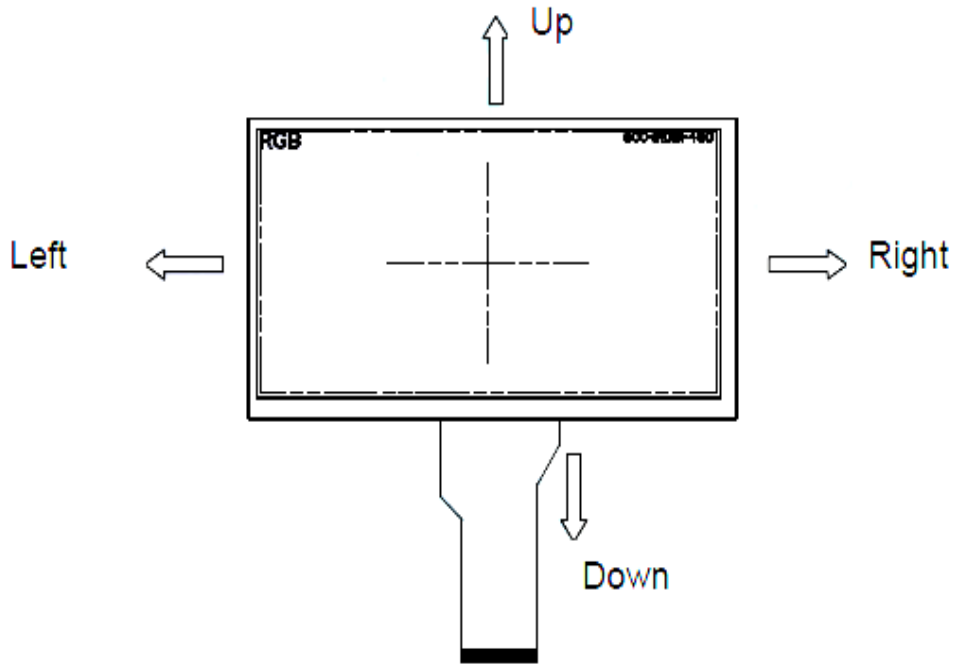
Note 2: When input 18 bits RGB data, the two low bits of R,G and B data must be grounded.

Note 3: Data shall be latched at the falling edge of DCLK.

Note 4: Selection of scanning mode

Setting of scan control input		Scanning direction
U/D	L/R	
GND	DV_{DD}	Up to down, left to right
DV_{DD}	GND	Down to up, right to left
GND	GND	Up to down, right to left
DV_{DD}	DV_{DD}	Down to up, left to right

Note 5: Definition of scanning direction.
Refer to the figure as below:



Note 6: Global reset pin. Active low to enter reset state. Suggest to connect with an RC reset circuit for stability. Normally pull high.

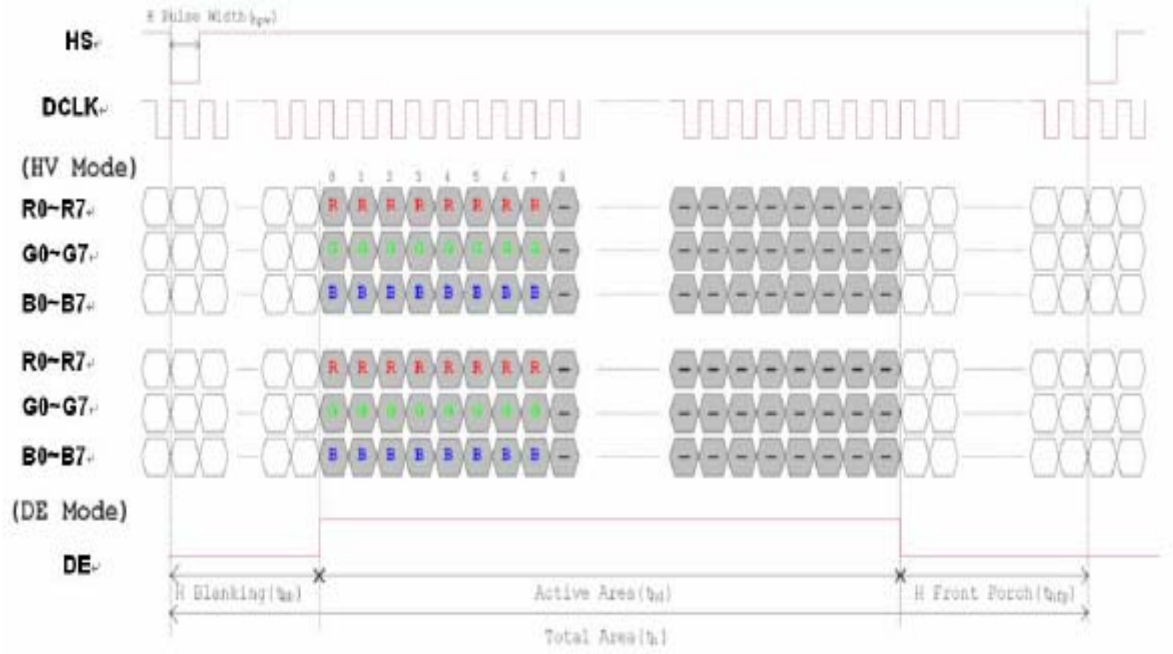
Note 7: Dithering function enable control, normally pull high.
When DITHB="1",Disable internal dithering function,
When DITHB="0",Enable internal dithering function,

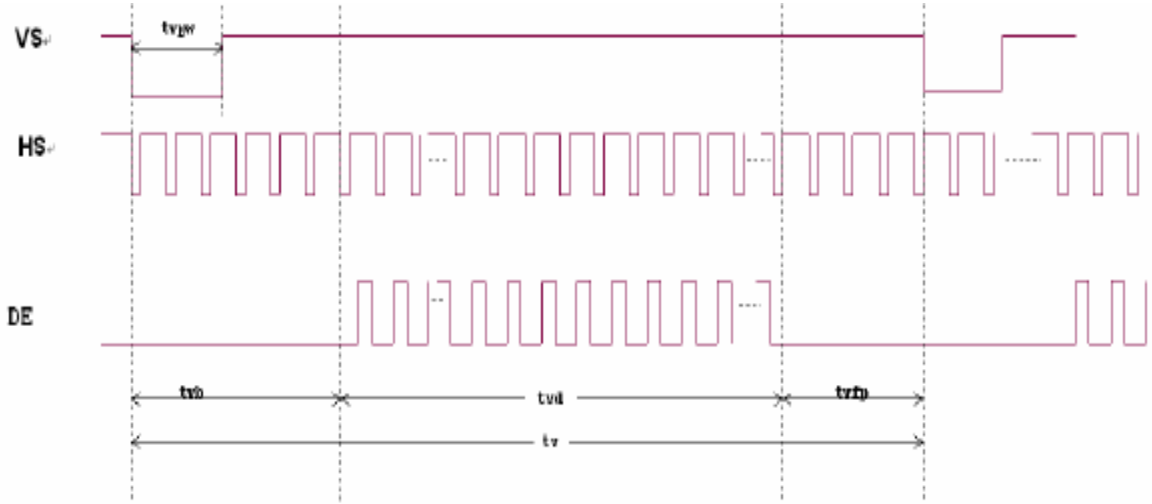
7. INPUT SIGNAL

7-1 Timing Specification

Item	Symbol	Values			Unit	Remark
		Min.	Typ.	Max.		
HS setup time	T_{hst}	8	-	-	ns	
HS hold time	T_{hhd}	8	-	-	ns	
VS setup time	T_{vst}	8	-	-	ns	
VS hold time	T_{vhd}	8	-	-	ns	
Data setup time	T_{dsu}	8	-	-	ns	
Data hole time	T_{dhd}	8	-	-	ns	
DE setup time	T_{esu}	8	-	-	ns	
DE hole time	T_{ehd}	8	-	-	ns	
DV_{DD} Power On Slew rate	T_{POR}	-	-	20	ms	From 0 to 90% DV_{DD}
RESET pulse width	T_{Rst}	1	-	-	ms	
DCLK cycle time	T_{coh}	20	-	-	ns	
DCLK pulse duty	T_{cwh}	40	50	60	%	

7-2 Data Input Format



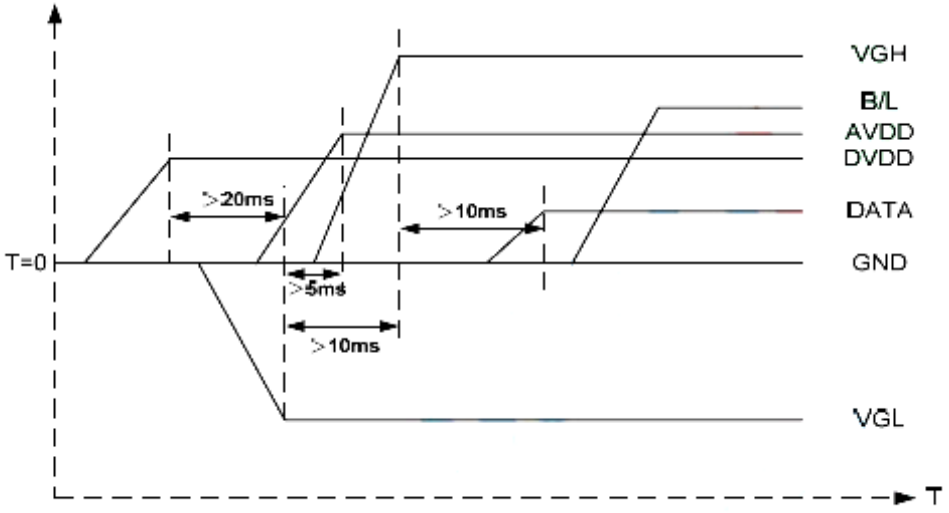


Item	Symbol	Values			Unit	Remark
		Min.	Typ.	Max.		
Horizontal Display Area	thd	-	800	-	DCLK	
DCLK Frequency	fclk	26.4	33.3	46.8	MHz	
One Horizontal Line	th	862	1056	1200	DCLK	
HS pulse width	thpw	1	-	40	DCLK	
HS Blanking	thb	46	46	46	DCLK	
HS Front Porch	thfp	16	210	354	DCLK	

Item	Symbol	Values			Unit	Remark
		Min.	Typ.	Max.		
Vertical Display Area	tvd	-	480	-	TH	
VS period time	tv	510	525	650	TH	
VS pulse width	tvpw	1	-	20	TH	
VS Blanking	tvb	23	23	23	TH	
VS Front Porch	tvfp	7	22	147	TH	

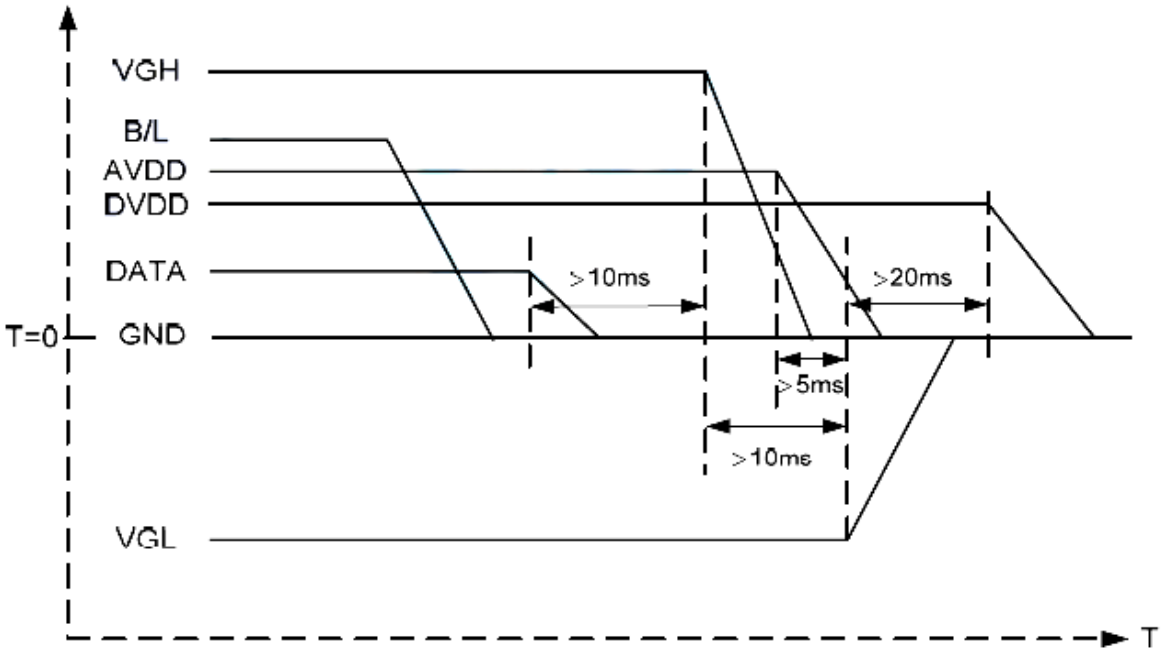
7-3 Power Sequence

7-3-1 Power on



DV_{DD}→VGL→VGH→Data→B/L

7-3-2 Power off



B/L→Data→VGH→VGL→DV_{DD}

Note: Data include R0~R7, B0~B7, GO~G7, U/D, L/R, DCLK, HS, VS, DE.

8. TOUCH PANEL ELECTRICAL SPECIFICATION

Parameter	Condition	Standard Value
Terminal Resistance	X Axis	300 ~ 1400 Ω
	Y Axis	150 ~ 800 Ω
Insulating Resistance	DC 25 V	More than 20MΩ
Linearity	--	≦1.5 %
Notes life by Pen	Note a	100,000 times(min)
Input life by finger	Note b	1,000,000 times (min)

Note A.

Notes area for pen notes life test is 10 x 9 mm.

Size of word is 7.5 x 6.75

Materials of pen : Polyacetal

Shape of pen end: R0.8mm

Load: 250 g

Speed : 60mm/s

Note B.

By Silicon rubber tapping at same point

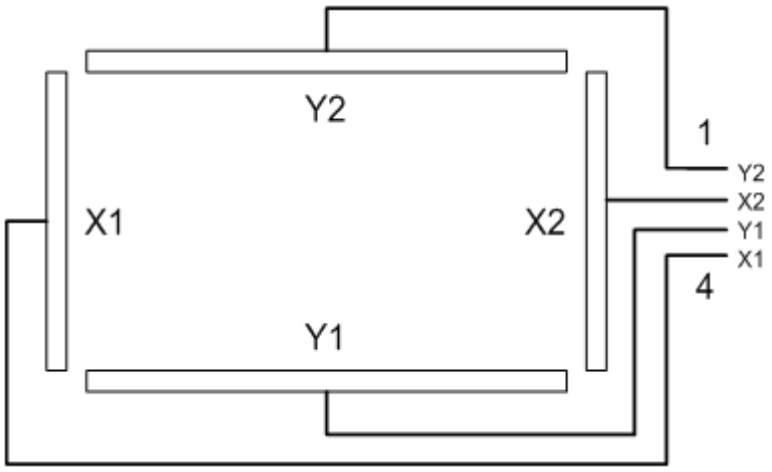
Shape of rubber end: R8

Load: 250gf

Frequency: 5 Hz

Interface

No.	Symbol	Function
1	Y2	Touch Panel Top Signal in Y Axis
2	X2	Touch Panel Right Signal in X Axis
3	Y1	Touch Panel Bottom Signal in Y Axis
4	X1	Touch Panel Left Signal in X Axis



9. RELIABILITY TEST ITEMS

Test Item	Test Conditions	Note
High Temperature Operation	60±3°C , t=240 hrs	
Low Temperature Operation	-20±3°C , t=240 hrs	
High Temperature Storage	70±3°C , t=240 hrs	1,2
Low Temperature Storage	-30±3°C , t=240 hrs	1,2
Storage at High Temperature and Humidity	40°C, 90% RH , 240 hrs	1,2
Thermal Shock Test	-20°C ~ 70°C (30min) 50 cycles	1,2

Note 1 : Condensation of water is not permitted on the module.

Note 2 : The module should be inspected after 1 hour storage in normal conditions (15-35°C , 45-65%RH).

10. General Precautions

10-1 Safety

Liquid crystal is poisonous. Do not put it your month. If liquid crystal touches your skin or clothes, wash it off immediately by using soap and water.

10-2 Handling

1. The LCD panel is plate glass. Do not subject the panel to mechanical shock or to excessive force on its surface.
2. The polarizer attached to the display is easily damaged. Please handle it carefully to avoid scratch or other damages.
3. To avoid contamination on the display surface, do not touch the module surface with bare hands.
4. Keep a space so that the LCD panels do not touch other components.
5. Put cover board such as acrylic board on the surface of LCD panel to protect panel from damages.
6. Transparent electrodes may be disconnected if you use the LCD panel under environmental conditions where the condensation of dew occurs.
7. Do not leave module in direct sunlight to avoid malfunction of the ICs.

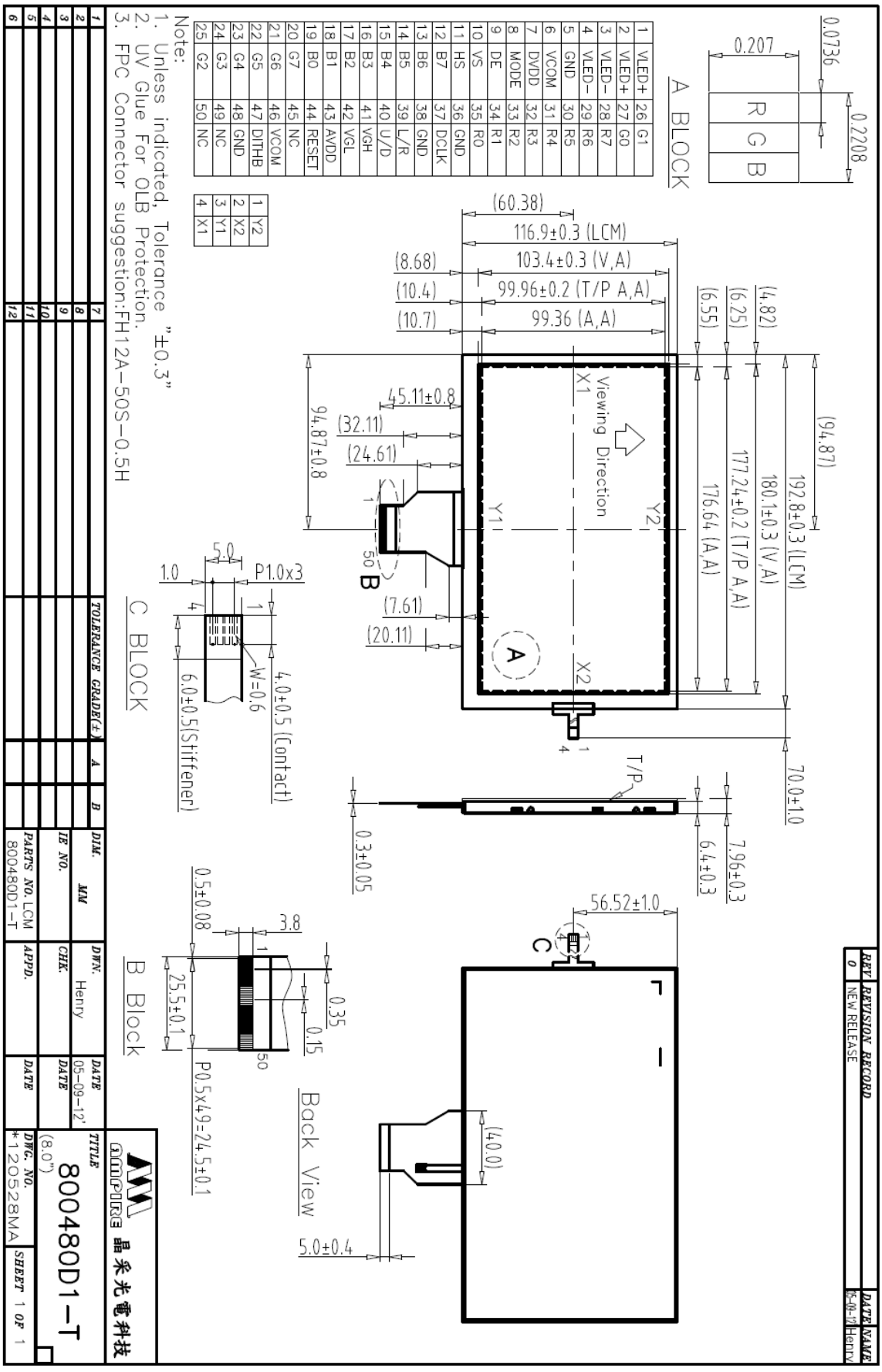
10-3 Static Electricity

1. Be sure to ground module before turning on power or operation module.
2. Do not apply voltage which exceeds the absolute maximum rating value.

10-4 Storage

1. Store the module in a dark room where must keep at +25±10℃ and 65%RH or less.
2. Do not store the module in surroundings containing organic solvent or corrosive gas.
3. Store the module in an anti-electrostatic container or bag.

10-5 Cleaning

1. Do not wipe the polarizer with dry cloth. It might cause scratch.
2. Only use a soft sloth with IPA to wipe the polarizer, other chemicals might permanent damage to the polarizer.

11. OUTLINE DIMENSION