晶采光電科技股份有限公司
AMPIRE CO., LTD.

SPECIFICATIONS FOR LCD MODULE

CUSTOMER	
CUSTOMER PART NO.	
AMPIRE PART NO.	**AM-320240N8TMQW-00H**
APPROVED BY	
DATE	

☐ **Approved For Specifications**
☑ **Approved For Specifications & Sample**

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RECORD OF REVISION

Revision Date	Page	Contents	Editor
2009/2/27	-	New Release	Kokai
2009/3/12	-	Modify LED Back-light data	Kokai
2009/3/18		Modify LED Back-light data	Kokai
2009/3/25		Modify LED Back-light data	Kokai
2009/4/16		Add VLED 5V~18V	Kokai
2009/11/19		Modify LED Back-light data according the back-light handmade sample	Kokai
2010/1/18		Modify Data according the prototype sample IDD=55mA @ 3.0V (VDD) IVLED= 240mA @ 5.0V (VLED) IVLED= 95mA @ 12.0V (VLED) Add Progressive scan timing for Generic TFT LCD controller. Modify the drawing.	Kokai
2010/1/20		Modify Operating Temperature to -20~70 ℃	Kokai
2012/2/10	10	Modify chromaticity value	Titan

1 Features

5.7 inch Amorphous-TFT-LCD (Thin Film Transistor Liquid Crystal Display) module. This module is composed of a 5.7" TFT-LCD panel, Touch Panel, a driver circuit and backlight unit.

(1) Construction: 5.7" a-Si color TFT-LCD, White LED Backlight and PCB.
(2) Resolution (pixel): 320(R.G.B) X240
(3) Number of the Colors : 262K colors (R , G , B 6 bit digital each)
(4) LCD type : Transmissive Color TFT LCD (normally White)
(5) Interface: 40 pin
(6) Power Supply Voltage: 3.3V power input for logic. **VLED 5V ~18V power input for LED driver.**
(7) Viewing Direction: 6 O'clock (The direction it's hard to be discolored)

2 Physical specifications

Item	Specifications	Unit	
Display resolution(dot)	320xRGB (W) x 240(H)	dots	
Active area	115.2 (W) x 86.4 (H)	mm	
Screen size	5.7(Diagonal)	mm	
Pixel size	120 (W) x 360 (H)	um	
Color configuration	R.G.B stripe		
Overall dimension	126.0(W)x101.8(H)x8.4(D)	mm	
Weight	145	g	
Backlight unit	LED		

3 Electrical specification

3.1 Absolute max. ratings

3.1.1 Electrical Absolute max. ratings

Item	Symbol	Condition	Min.	Max.	Unit	Remark
Power voltage	VDD	VSS=0	-0.3	5.0	V	
LED Power voltage	VLED	VSS=0	-0.3	20.0	V	Note 2
Input voltege	V_{in}		-0.3	VDD+0.3	V	Note 1

Note1:Hsync, Vsync, DEN, DCLK, R0~R5, G0~G5, B0~B5
Note2: The LED driver is TPS61165.

3.1.2 Environmental Absolute max. ratings

Item	OPERATING		STORAGE		Remark
	MIN	MAX	MIN	MAX	
Temperature	-20	70	-30	85	Note2,3,4,5,6,7
Humidity	Note1		Note1		
Corrosive Gas	Not Acceptable		Not Acceptable		

Note1 : Ambient temperature Ta <= 40℃ : 85% RH max

Ta > 40℃ : Absolute humidity must be lower than the humidity of 85%RH at 40℃

Note2 : For storage condition Ta at -30℃ < 48h , at 85℃ < 100h

For operating condition Ta at -20℃ < 100h

Note3 : Background color changes slightly depending on ambient temperature. This phenomenon is reversible.

Note4 : The response time will be slower at low temperature.

Note5 : Only operation is guarantied at operating temperature. Contrast , response time, another display quality are evaluated at +25℃

Note6 : When LCM panel is operated over 60℃ (center of the panel surface temperature), the I_{LED} of the LED back-light should be adjusted to 48mA

Note7 : This is center of the panel surface temperature, not ambient temperature.

3.1.3 LED back-light Unit Absolute max. ratings

Item	Symbol	Ratings	Unit	Remark
Peak forward Current	IF	300	mA	150x2
Power Dissipation	Po	7200	mW	600x12

3.2 Electrical characteristics

3.2.1 DC Electrical characteristic of the LCD

Typical operting conditions (VSS=0V)

Item		Symbol	Min.	Typ.	Max.	Unit	Remark
Power supply		VDD	3.0	3.3	3.6	V	
LED Power Supply		VLED	**5.0**	**12**	**18**	V	
Input Voltage for logic	H Level	V_{IH}	0.7 VDD	-	VDD	V	Note 1
	L Level	V_{IL}	0	-	0.3 VDD	V	
Power Supply current		IDD		55	65	mA	Note 2
LED Power Supply current VLED=12V		**I_{VLED}**		**95**	**--**	**mA**	**Note 3**
LED Power Supply current VLED=5V		**I_{VLED}**		**240**	**--**	**mA**	**Note 4**

Note1: Hsync, Vsync, DEN, DCLK, R0~R5, G0~G5, B0~B5

Note2: TFT power supply current. VDD=3.0V fv =60Hz, Ta=25℃ , Display pattern :

All Black

Note3: LED Driver TPS61165 , VLED=12V, ILED=60mA, ADJ=Hi.

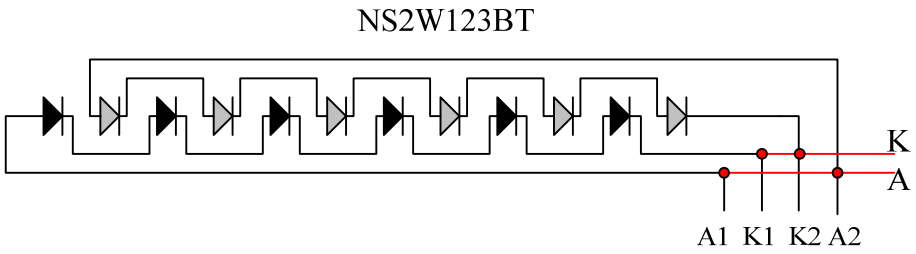
Note4: LED Driver TPS61165 , VLED=5V, ILED=60mA, ADJ=Hi.

3.2.2 Electrical characteristic of LED Back-light

Paramenter	Symbol	Min.	Typ.	Max.	Unit	Condiction
LED voltage	V_{AK}	--	19.98 (3.33x6)	23	V	I_{LED} =60mA, T cs =25℃
LED forward current	I_{LED}	--	60	69	mA	T cs =25℃
	I_{LED}	--	48	55.2	mA	T cs =60℃
Lamp life time		50,000	-	-	Hr	I_{LED} =60mA, T cs =25℃

T cs : Center of the panel surface temperature

NICHIA LED dice NS2W123BT used.

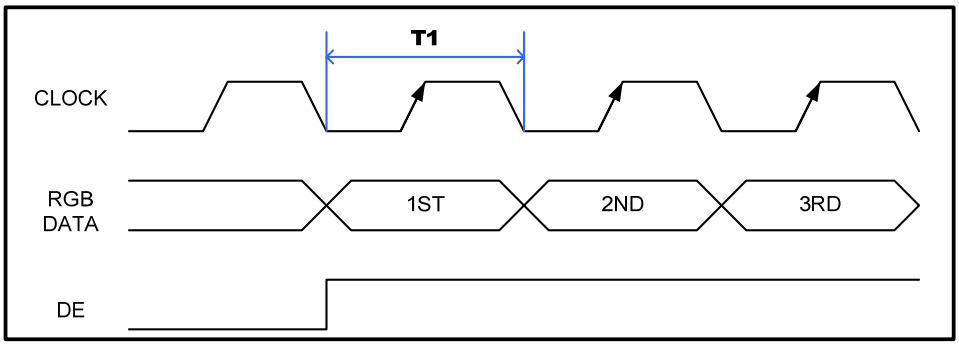


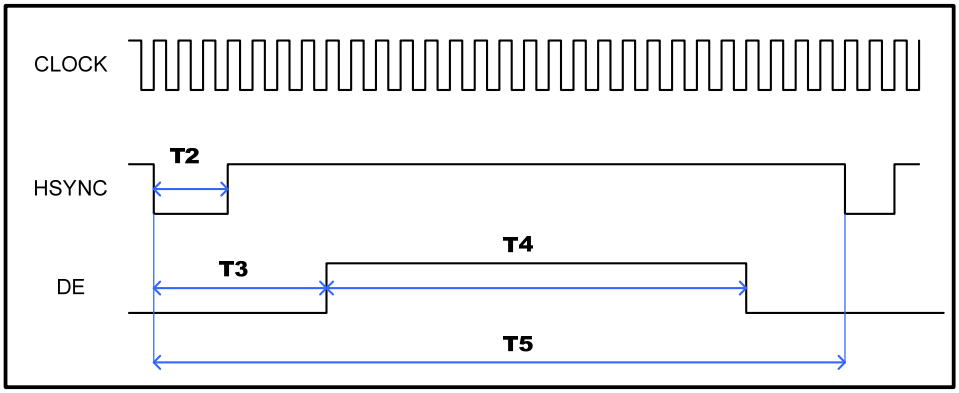
LED Back-light with DBEF.

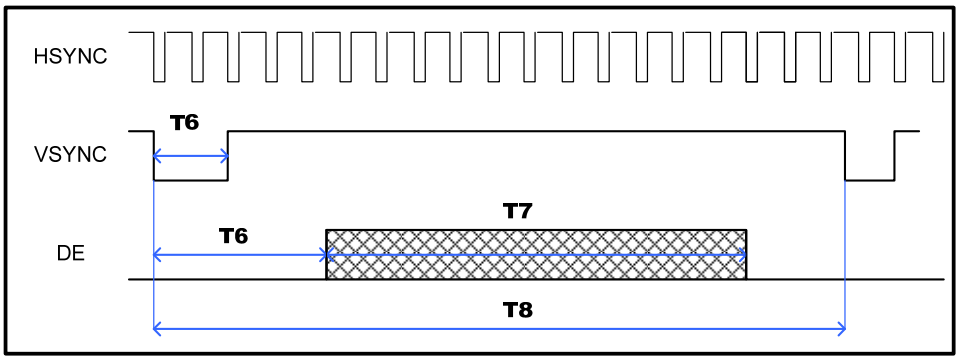
- The constant current source is needed for white LED back-light driving.
- When LCM is operated over 60℃(center of the panel surface temperature), the I_{LED} of the LED back-light should be adjusted to 48mA

4 AC Timing characteristic of the LCD

4.1 Progressive Scan Timing condition for Generic TFT LCD controller.







ITEM	SYMBOL	MIN	TYP	MAX	UNIT
Clock Frequency	1/T1	6.4	8	12	MHz
HSYNC Plus Wide	T2	5	30	--	clocks
HSYNC to DE	T3	--	68	--	Clocks
Horizontal Display Period	T4	--	320	--	Clocks
Horizontal total Period	T5	--	408	--	Clocks
VSYNC Plus Wide	T2	1	3	5	Lines
VSYNC to DE	T6		18		Lines
Vertical Display Period	T7	--	240	--	Lines
Vertical total Period	T8	--	262	--	Lines

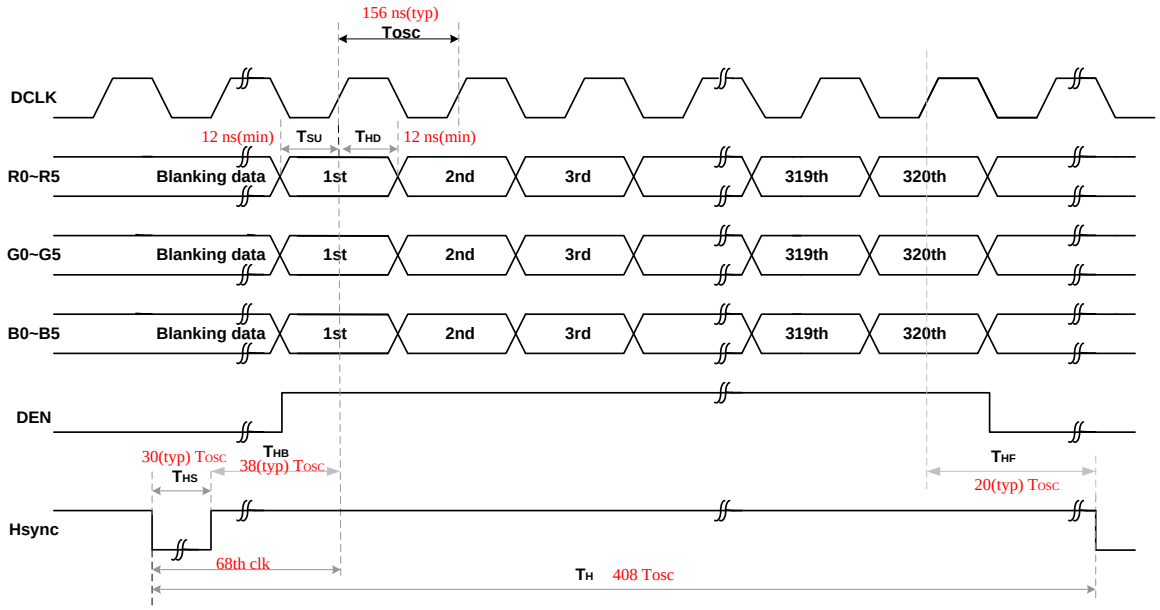
4.2 Interlace Scan Timing condition for Video decoder application

Signal	Parameter		Symbol	Min.	Typ.	Max.	Unit.	Remark
DCLK	DCLK period		TOSC	-	156	-	ns	
	Frequency		FOSC	-	6.4	-	MHz	
	DCLK High plus width		TCH	-	78	-	ns	
	DCLK Low plus width		TCL	-	78	-	ns	
RGB DATA	Data setup time		TSU	12	-	-	ns	
	Data hold time		THD	12	-	-	ns	
Hsync	Hsync period		TH	-	408	-	TOSC	
	Hsync pulse width		THS	5	30	-	TOSC	
	Back-Parch		THB		38		TOSC	
	Front-Parch		THF		20		TOSC	
	Hsync rising time		TCr	-	-	700	ns	
	Hsync falling time		TCf	-	-	300	ns	
Vsync	Vsync period	NTSC		-	262.5	-	TH	
		PAL		-	312.5	-	TH	
	Vsync pulse width		TVS	1	3	5	TH	
	Back-Porch	NTSC	TVB		15		TH	
		PAL			23		TH	
	Display Period		TVD		240		TH	
	Front Porch	NTSC	TVF		4.5		TH	
		PAL			46.5		TH	
	Vsync rising time		TVr	-	-	700	ns	
	Vsync falling time		TVf	-	-	1.5	μs	
	Vsync falling to Hsync rising time for odd field		THVO	1	-	-	TOSC	
	Vsync falling to Hsync falling time for even field		THVE	1	-	-	TOSC	
DEN	Vsync-DEN time	NTSC	TVSE	-	18	-	TH	
		PAL	TVSE	-	26	-	TH	
	Hsync-DEN time		THE	36	68	88	TOSC	
	DEN plus width		TEP	-	320	-	TOSC	

Note : If DEN is fixed to low, the SYNC mode is used. Otherwise DE mode is used. When SYNC mode is used, 1st data start from 68th CLK after Hsync falling

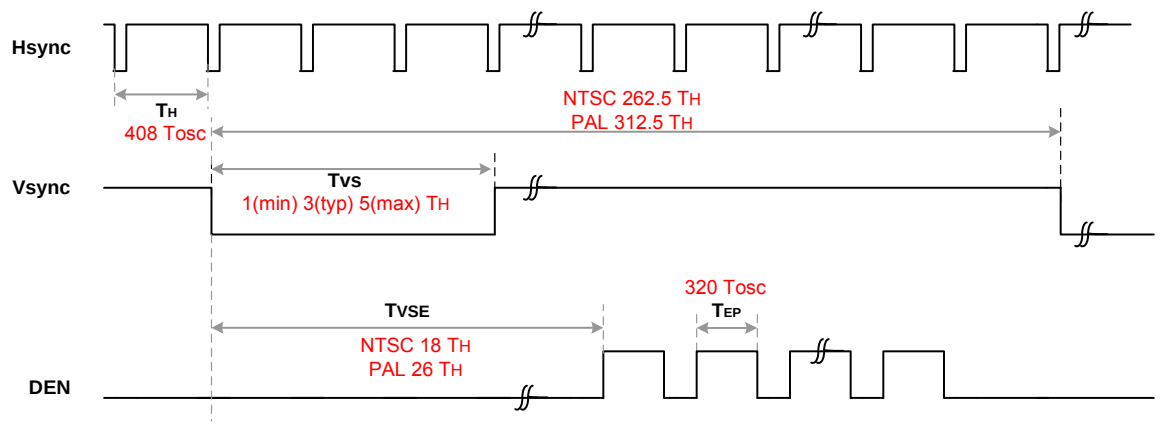
- Horizontal display timing

Note: Data is latched rising edge trigger of CLK.

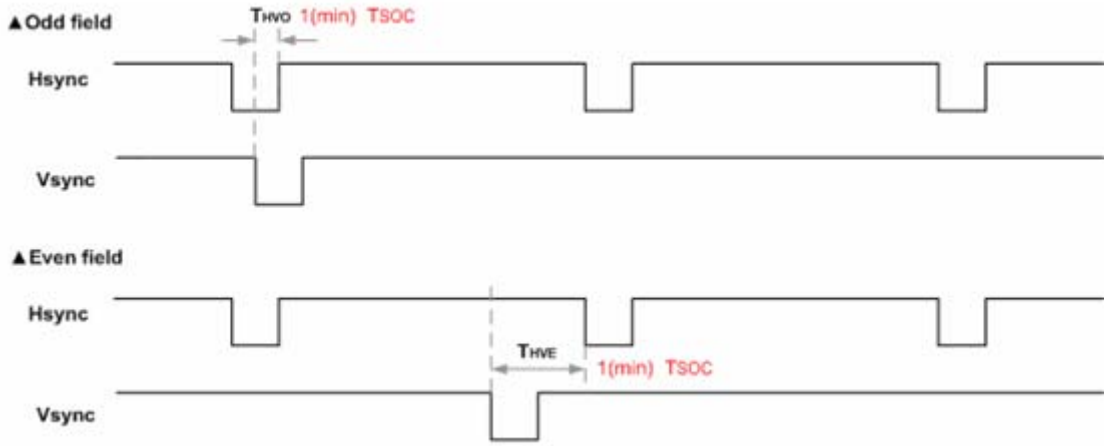


If DEN is fixed to low, the SYNC mode is used. Otherwise DE mode is used.
When SYNC mode is used, 1st data start from 68th CLK after Hsync falling

- Vertical display timing



- Hsync and Vsync timing



5 Optical specification

5.1 Optical characteristic of the LCD

Item		Symbol	Conditon	Min.	Typ.	Max.	Unit	Remark
Response Time	Rise	T_r	Θ=0°	-	15	30	ms	Note 1,2,3,5
	Fall	T_f		-	35	50	ms	
Contrast ratio		CR	At optimized viewing angle	200	350	-		Note 1,2,4,5
Viewing Angle	Top		CR≧10	55	60	-	deg.	Note1,2, 5,6
	Bottom			45	50	-		
	Left			55	60	-		
	Right			55	60	-		
Brightness		Y_L	**I_{LED}=60.0mA, 25℃**	-	500	-	cd/m²	Note 7
Red chromaticity		XR	Θ=0° Θ=0°	0.610	0.640	0.670		Note 7 For reference only. These data should be update according the prototype.
		YR		0.314	0.344	0.374		
Green chromaticity		XG		0.320	0.350	0.380		
		YG		0.595	0.625	0.655		
Blue chromaticity		XB		0.102	0.132	0.162		
		YB		0.107	0.137	0.167		
White chromaticity		XW		0.312	0.342	0.372		
		YW		0.348	0.378	0.408		

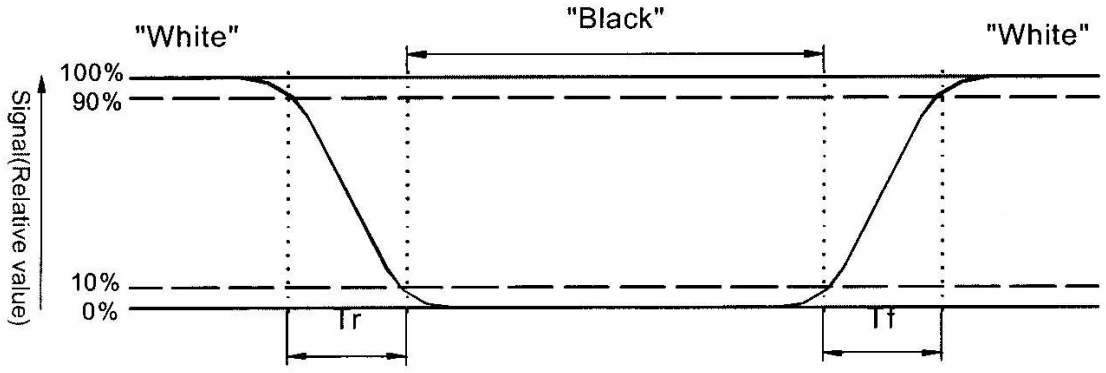
( )For reference only. These data should be update according the prototype.

Note 1:Ambient temperature=25℃,and lamp current I_{LED}=60mA.To be measured in the dark room.

Note 2:To be measured on the center area of panel with a viewing cone of 1°by Topcon luminance meter BM-7,after 10 minutes operation.

Note 3.Definition of response time:

The output signals of photo detector are measured when the input signals are changed from “black“ to “white”(falling time) and from”white” to “black” (rising time),respectively.The response time is defined as the time interval between the 10% and 90% of amplitudes. Refer to figure as below.



Note 4.Definition of contrast ratio:

Contrast ratio is calculated with the following formula.

$$\text{Contrast ratio(CR)} = \frac{\text{Photo detector output when LCD is at "White" state}}{\text{Photo detector Output when LCD is at "Black" state}}$$

Note 5:White $V_i=V_{i50}$ +1.5V

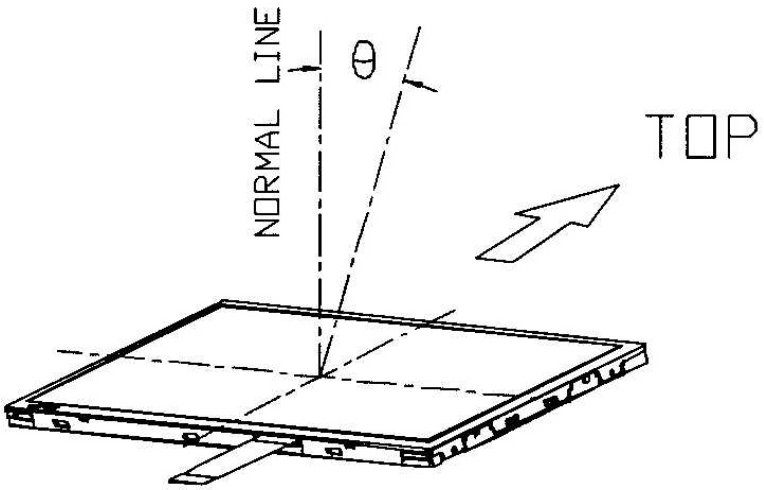
Black $V_i=V_{i50}$ +2.0V

"±"means that the analog input signal swings in phase with V_{COM} signal.

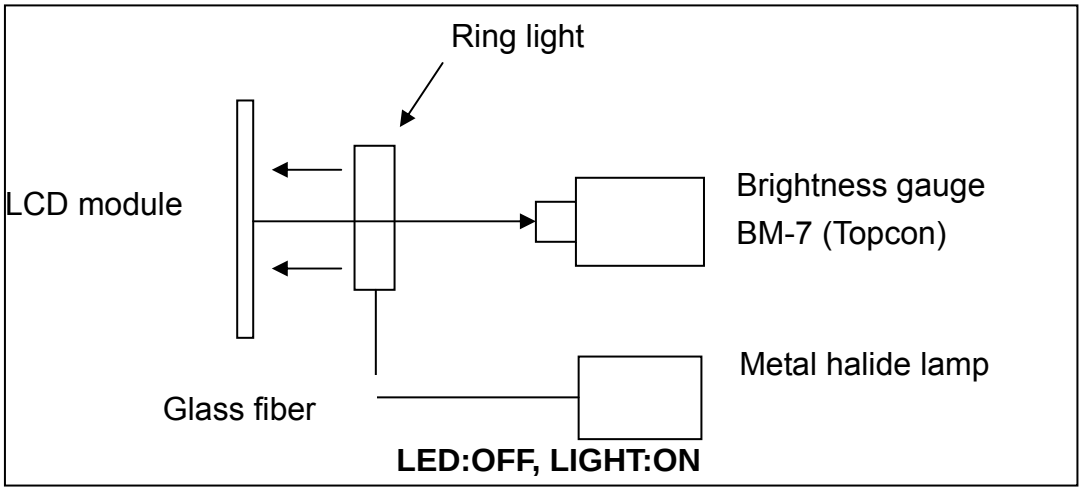
"$\underset{+}{-}$ " means that the analog input signal swings out of phase with V_{COM} signal.

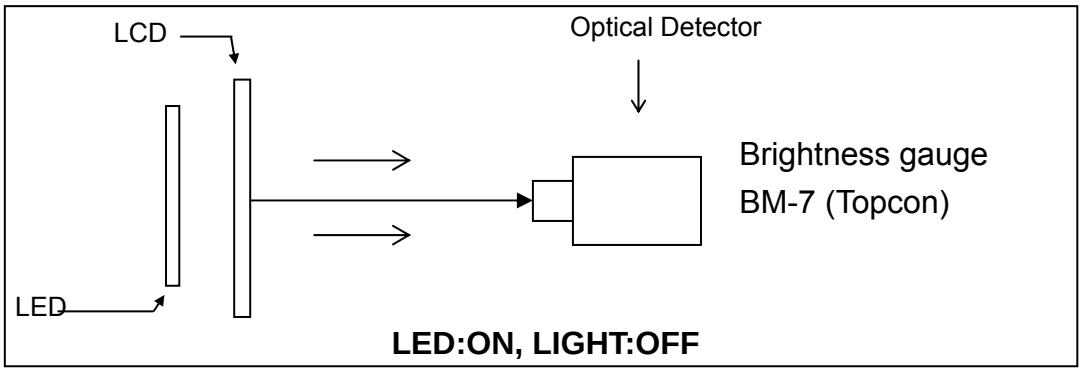
V_{i50} : The analog input voltage when transmission is 50%.The 100% Transmission is defined as the transmission of LCD panel when all the Input terminals of module are electrically opened.

Note 6.Definition of viewing angle,Refer to figure as below.



Note 7.Measured at the center area of the panel when all the input terminals of LCD panel are electrically opened.





5.2 Optical characteristic of the LED Back-light

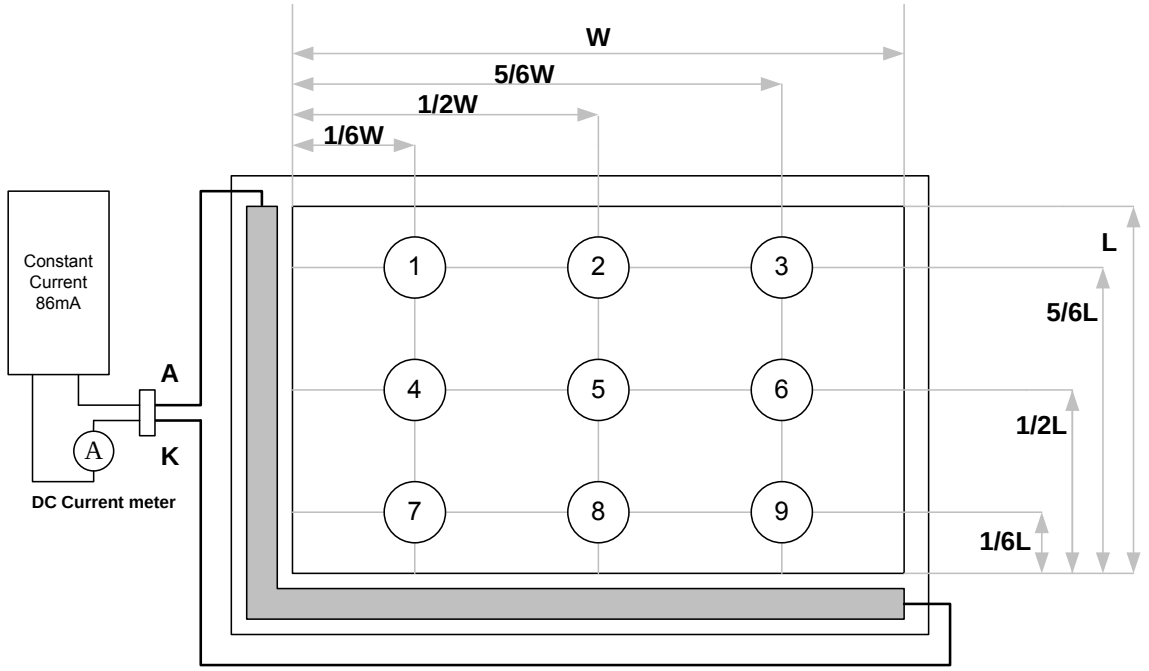
ITEM	MIN	TYP	MAX	UNIT	Condition
Bare Brightness	3600	--	--	Cd/m2	I_{LED} =60mA, T cs =25℃
AVG. X of 1931 C.I.E.	0.26	0.28	0.31	--	I_{LED} =60mA, T cs =25℃
AVG. X of 1931 C.I.E.	0.27	0.29	0.32	--	I_{LED} =60mA, T cs =25℃
Brightness Uniformity	80	--	--	%	I_{LED} =60mA, T cs =25℃

( )For reference only. These data should be update according the prototype.

T cs : Center of the panel surface temperature

Note1 : Measurement after 10 minutes from LED operating.

Note2 : Measurement of the following 9 places on the display.



Note3: The Uniformity definition

(Min Brightness / Max Brightness) x 100%

6 Interface specifications

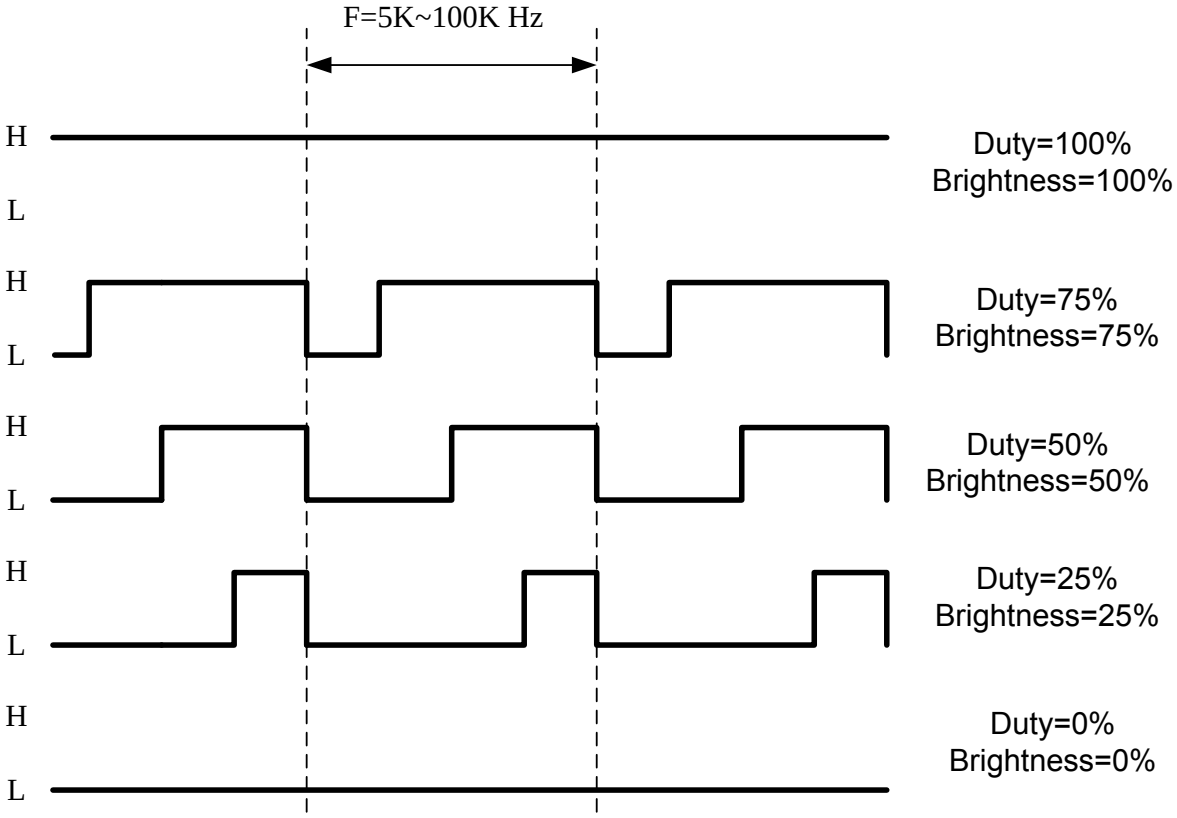
6.1 Driving signals for the TFT panel

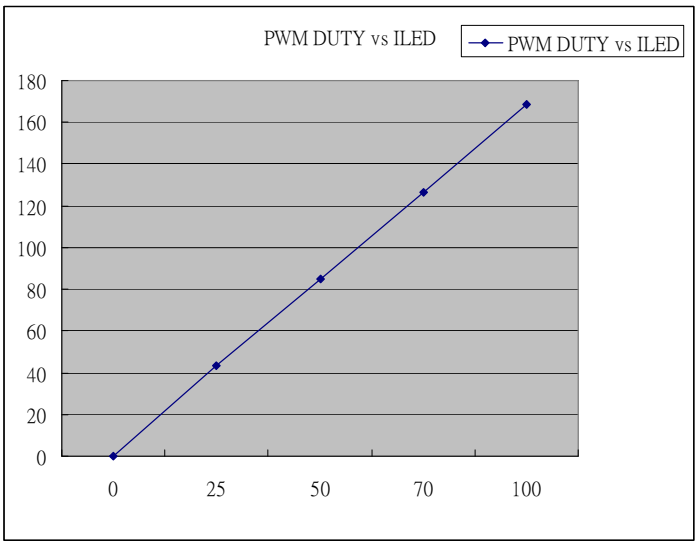
Pin No	Symbol	Function
1	U/D	Up or Down Display Control
2	(NC)	No connection
3	Hsync(NC)	Honizontal SYNC. (Sync mode used)
4	VLED	Power Supply for LED
5	VLED	Power Supply for LED
6	VLED	Power Supply for LED
7	Vcc	Power Supply for LCD
8	Vsync(NC)	Vertical SYNC. (Sync mode used)
9	DE	Data Enable
10	Vss(X2)	Power Ground(Left electrode – differential analog)
11	Vss(Y1)	Power Ground (Top electrode – differential analog)
12	ADJ	Adjust for LED Brightness (Note 1)
13	B5	Blue Data 5 (MSB)
14	B4	Blue Data 4
15	B3	Blue Data 3
16	Vss	Power Ground
17	B2	Blue Data 2
18	B1	Blue Data 1
19	B0	Blue Data 0 (LSB)
20	Vss	Power Ground
21	G5	Green Data 5 (MSB)
22	G4	Green Data 4
23	G3	Green Data 3
24	Vss	Power Ground
25	G2	Green Data 2
26	G1	Green Data 1
27	G0	Green Data 0 (LSB)
28	Vss	Power Ground
29	R5	Red Data 5 (MSB)
30	R4	Red Data 4
31	R3	Red Data 3
32	Vss	Power Ground
33	R2	Red Data 2
34	R1	Red Data 1
35	R0	Red Data 0 (LSB)
36	Vss(X1)	Power Ground (Right electrode – differential analog)
37	Vss(Y2)	Power Ground (Bottom electrode – differential analog)
38	DCLK	Clock Signals
39	Vss	Power Ground
40	L/R	Left or Right Display Control

NOTE :

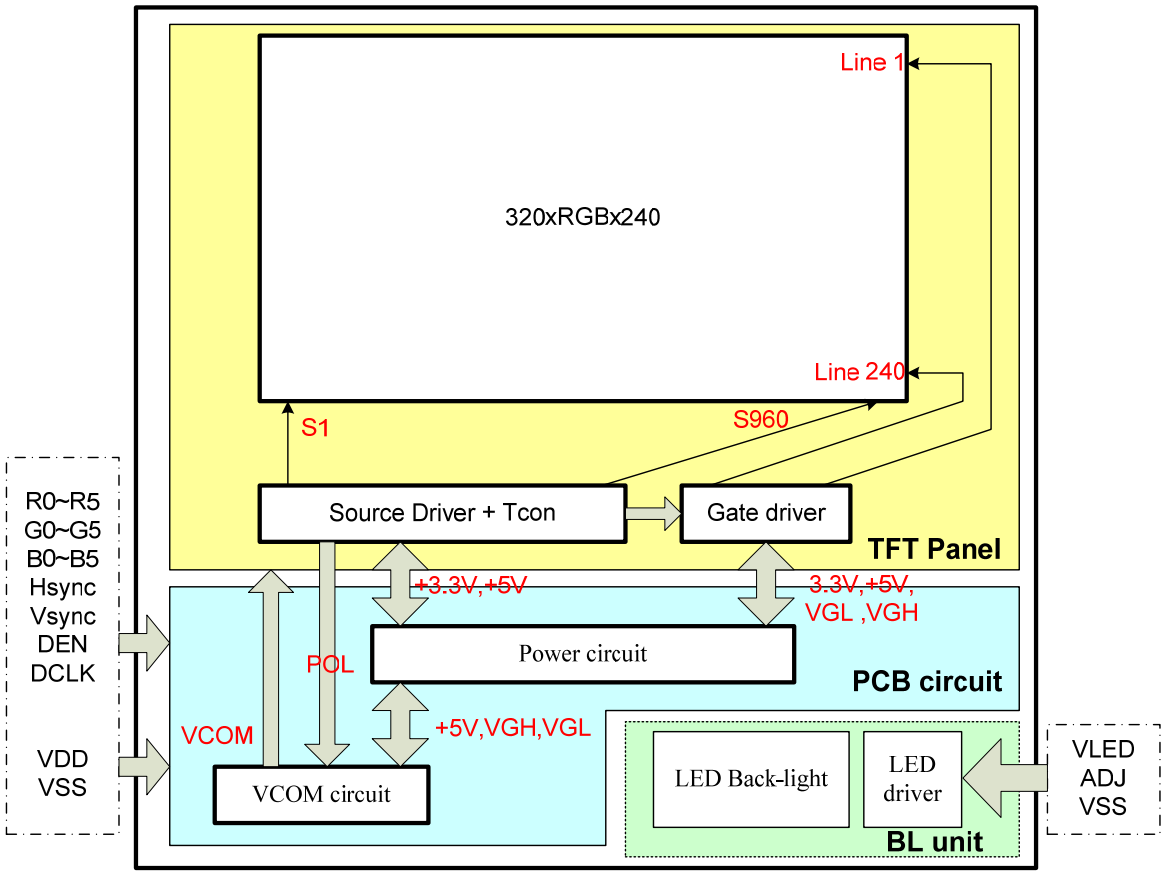
Pin3: ADJ is PWM signal input. It is for brightness control.

ITEM	SYMBOL	MIN	TYP	MAX	UNIT
ADJ signal frequency	f_{PWM}	5	--	100	KHz
ADJ signal logic level High	VIH	1.2V	--	VLED	V
ADJ signal logic level Low	VIL	0	--	0.4	V





7 BLOCK DIAGRAM



8 DISPLAYED COLOR AND INPUT DATA

	Color & Gray Scale	DATA SIGNAL																	
		R5	R4	R3	R2	R1	R0	G5	G4	G3	G2	G1	G0	B5	B4	B3	B2	B1	B0
Basic Color		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(0)	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Green(0)	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
	Blue(0)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1
	Cyan	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	0	0	0	0	0	0	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
Red	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(62)	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0
	Red(61)	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Red(31)	0	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Red(1)	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(0)	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
Green	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green(62)	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0
	Green(61)	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Green(31)	0	0	0	0	0	0	0	1	1	1	1	0	0	0	0	0	0	0
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Green(1)	0	0	0	0	0	0	1	1	1	1	1	0	0	0	0	0	0	0
	Green(0)	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
Blue	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue(62)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
	Blue(61)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Blue(31)	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Blue(1)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	0
	Blue(0)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1

9 QUALITY AND RELIABILITY

9.1 TEST CONDITIONS

Tests should be conducted under the following conditions :
Ambient temperature : 25 ± 5°C
Humidity : 60 ± 25% RH.

9.2 SAMPLING PLAN

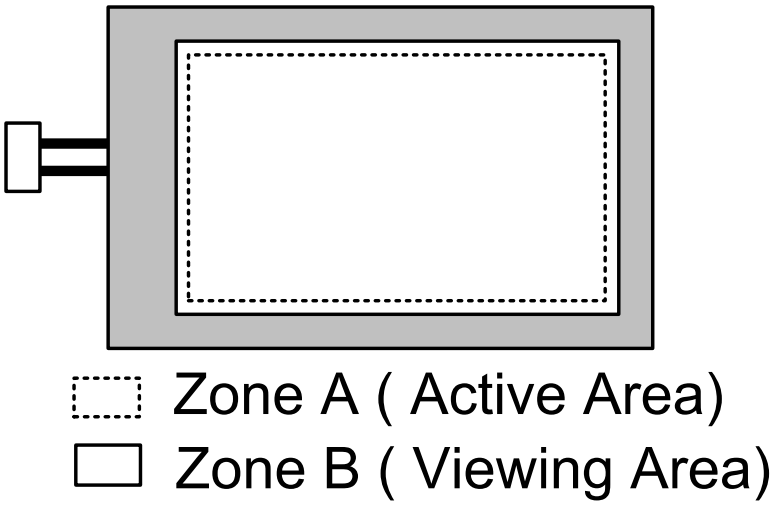
Sampling method shall be in accordance with MIL-STD-105E , level II, normal single sampling plan .

9.3 ACCEPTABLE QUALITY LEVEL

A major defect is defined as one that could cause failure to or materially reduce the usability of the unit for its intended purpose. A minor defect is one that does not materially reduce the usability of the unit for its intended purpose or is an infringement from established standards and has no significant bearing on its effective use or operation.

9.4 APPEARANCE

An appearance test should be conducted by human sight at approximately 30 cm distance from the LCD module under flourescent light. The inspection area of LCD panel shall be within the range of following limits.



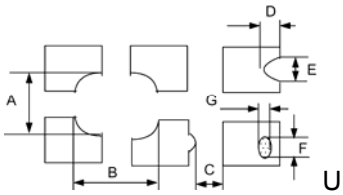
9.5 INSPECTION QUALITY CRITERIA

<table>
<tr><th>No.</th><th>Item</th><th>Criterion for defects</th><th>Defect type</th></tr>
<tr><td>1</td><td>Non display</td><td>No non display is allowed</td><td>Major</td></tr>
<tr><td>2</td><td>Irregular operation</td><td>No irregular operation is allowed</td><td>Major</td></tr>
<tr><td>3</td><td>Short</td><td>No short are allowed</td><td>Major</td></tr>
<tr><td>4</td><td>Open</td><td>Any segments or common patterns that don’t activate are rejectable.</td><td>Major</td></tr>
<tr><td>5</td><td>Black/White spot

(Zone A Active Area)</td><td>
<table>
<tr><th>Size D (mm)</th><th>Acceptable number</th></tr>
<tr><td>D ≤ 0.15</td><td>Ignore</td></tr>
<tr><td>0.15 < D ≤ 0.20</td><td>3</td></tr>
<tr><td>0.20 < D ≤ 0.30</td><td>2</td></tr>
<tr><td>0.30 < D</td><td>0</td></tr>
</table>
</td><td>Minor</td></tr>
<tr><td>6</td><td>Black/White line

(Zone B Viewing Area)</td><td>
<table>
<tr><th>Length(mm)</th><th></th><th>Acceptable number</th></tr>
<tr><td>10 < L</td><td>0.03 < W ≤ 0.04</td><td>5</td></tr>
<tr><td>5.0 < L ≤10</td><td>0.04 < W ≤ 0.06</td><td>3</td></tr>
<tr><td>1.0 < L ≤ 5.0</td><td>0.06 < W ≤ 0.07</td><td>2</td></tr>
<tr><td>L ≤ 1.0</td><td>0.07 < W ≤ 0.09</td><td>1</td></tr>
</table>
</td><td>Minor</td></tr>
<tr><td>7</td><td>Black/White sport

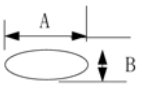
(Zone A Active Area)</td><td>
<table>
<tr><th>Size D (mm)</th><th>Acceptable number</th></tr>
<tr><td>D ≤ 0.30</td><td>Ignore</td></tr>
<tr><td>0.30 < D ≤ 0.50</td><td>5</td></tr>
<tr><td>0.50 < D ≤ 1.20</td><td>3</td></tr>
<tr><td>1.20 < D</td><td>0</td></tr>
</table>
</td><td>Minor</td></tr>
<tr><td>8</td><td>Black/White line

(Zone B Viewing Area)</td><td>
<table>
<tr><th>Length (mm)</th><th>Width (mm)</th><th>Acceptable number</th></tr>
<tr><td>20 < L</td><td>0.05 < W ≤ 0.07</td><td>5</td></tr>
<tr><td>10 < L ≤ 20</td><td>0.07 < W ≤ 0.09</td><td>3</td></tr>
<tr><td>5.0 < L ≤ 10</td><td>0.09 < W ≤ 0.10</td><td>2</td></tr>
<tr><td>L ≤ 5.0</td><td>0.10 < W ≤ 0.15</td><td>1</td></tr>
</table>
</td><td>Minor</td></tr>
<tr><td>9</td><td>Back Light</td><td>1. No Lighting is rejectable
2. Flickering and abnormal lighting are rejectable</td><td>Major</td></tr>
<tr><td>10</td><td>Display pattern</td><td> 

Unit:mm
<table>
<tr><td>$\frac{A+B}{2} \le 0.30$</td><td>0 < C</td><td>$\frac{D+E}{2} \le 0.25$</td><td>$\frac{F+G}{2} \le 0.25$</td></tr>
</table>
Note: 1. Acceptable up to 3 damages
2. NG if there’re to two or more pinholes per dot</td><td>Minor</td></tr>
</table>

<table>
<tr><td>11</td><td>Blemish & Foreign matters

Size:
$D = \frac{A+B}{2}$</td><td><table><tr><th>Size D (mm)</th><th>Acceptable number</th></tr><tr><td>D ≤ 0.15</td><td>Ignore</td></tr><tr><td>0.15 < D ≤ 0.20</td><td>3</td></tr><tr><td>0.20 < D ≤ 0.30</td><td>2</td></tr><tr><td>0.30 < D</td><td>0</td></tr></table></td><td>Minor</td></tr>
<tr><td>12</td><td>Scratch on Polarizer

  </td><td><table><tr><th>Width (mm)</th><th>Length (mm)</th><th>Acceptable number</th></tr><tr><td>W≤0.03</td><td>Ignore</td><td>Ignore</td></tr><tr><td>0.03<W≤0.05</td><td>L ≤ 2.0</td><td>Ignore</td></tr><tr><td></td><td>L > 2.0</td><td>1</td></tr><tr><td>0.05<W≤0.08</td><td>L > 1.0</td><td>1</td></tr><tr><td></td><td>L ≤ 1.0</td><td>Ignore</td></tr><tr><td>0.08<W</td><td>Note (1)</td><td>Note(1)</td></tr></table>Note(1) Regard as a blemish</td><td>Minor</td></tr>
<tr><td>13</td><td>Bubble in polarizer</td><td><table><tr><th>Size D (mm)</th><th>Acceptable number</th></tr><tr><td>D ≤ 0.20</td><td>Ignore</td></tr><tr><td>0.20 < D ≤ 0.50</td><td>3</td></tr><tr><td>0.50 < D ≤ 0.80</td><td>2</td></tr><tr><td>0.80 < D</td><td>0</td></tr></table></td><td>Minor</td></tr>
<tr><td>14</td><td>Stains on LCD panel surface</td><td>Stains that cannot be removed even when wiped lightly with a soft cloth or similar cleaning too are rejectable.</td><td>Minor</td></tr>
<tr><td>15</td><td>Rust in Bezel</td><td>Rust which is visible in the bezel is rejectable.</td><td>Minor</td></tr>
<tr><td>16</td><td>Defect of land surface contact (poor soldering)</td><td>Evident crevices which is visible are rejectable.</td><td>Minor</td></tr>
<tr><td>17</td><td>Parts mounting</td><td>1. Failure to mount parts
2. Parts not in the specifications are mounted
3. Polarity, for example, is reversed</td><td>Major
Major
Major</td></tr>
<tr><td>18</td><td>Parts alignment</td><td>1. LSI, IC lead width is more than 50% beyond pad outline.
2. Chip component is off center and more than 50% of the leads is off the pad outline.</td><td>Minor
Minor</td></tr>
<tr><td>19</td><td>Conductive foreign matter (Solder ball, Solder chips)</td><td>1. 0.45< φ ,N≧1
2. 0.30< φ ≤0.45 ,N≧1
φ :Average diameter of solder ball (unit: mm)
3. 0.50<L ,N≧1
L: Average length of solder chip (unit: mm)</td><td>Major
Minor
Minor</td></tr>
<tr><td>20</td><td>Faulty PCB correction</td><td>1. Due to PCB copper foil pattern burnout, the pattern is connected, using a jumper wire for repair; 2 or more places are corrected per PCB.
2. Short circuited part is cut, and no resist coating has been performed.</td><td>Minor
Minor</td></tr>
</table>

<table>
<tr><td>21</td><td>Defect Dot</td><td>The TFT panel may have bright dot or Dark dot.
The acceptable number defection:<table><tr><th>Bright dot</th><th>Dark dot</th><th>Total dot</th><th>Distance between Dark-- dark</th></tr><tr><td>2</td><td>3</td><td>4</td><td>L≧5 mm</td></tr></table></td><td>Minor</td></tr>
</table>

10 Reliability test items (Note2):

No.	Test items	Conditions	Remark
1	High temperature storage	Ta=85℃ 240Hrs	
2	Low temperature storage	Ta=-30℃ 240Hrs	
3	High temperature operation	Ta=70℃ 240Hrs	
4	Low temperature operation	Ta=-20℃ 240Hrs	
5	High temperature and high humidity	Ta=40℃,85% RH 240Hrs	Operation
6	Heat shock	-30℃~80℃/200 cycles 1Hrs/cycle	Non-operation
7	Electrostatic discharge	±200V,200Pf(0Ω),once for each terminal	Non-operation
8	Vibration	Frequency range :8~33.3Hz Stoke :1.3mm Sweep :2.9G,33.3~400Hz Cycle :15 minutes 2 hours for each direction of X,Z 4 hours for Y direction	JIS C7021, A-10 Condition A
9	Mechanical shock	100G, 6ms,±X, ±Y,±Z 3 times for each direction	JIS C7021, A-7 Condition C
10	Vibration (With carton)	Random vibration: $0.015G^2$/Hz from 5~200Hz -6dB/octave from 200~500Hz	IEC 68~34
11	Drop (with carton)	Height:60cm 1 corner,3 edges,6 surfaces	JIS Z0202

11 USE PRECAUTIONS

11.1 Handling precautions

1) The polarizing plate may break easily so be careful when handling it. Do not touch, press or rub it with a hard-material tool like tweezers.
2) Do not touch the polarizing plate surface with bare hands so as not to make it dirty. If the surface or other related part of the polarizing plate is dirty, soak a soft cotton cloth or chamois leather in benzine and wipe off with it. Do not use chemical liquids such as acetone, toluene and isopropyl alcohol. Failure to do so may bring chemical reaction phenomena and deteriorations.
3) Remove any spit or water immediately. If it is left for hours, the suffered part may deform or decolorize.
4) If the LCD element breaks and any LC stuff leaks, do not suck or lick it. Also if LC stuff is stuck on your skin or clothing, wash thoroughly with soap and water immediately.

11.2 Installing precautions

1) The PCB has many ICs that may be damaged easily by static electricity. To prevent breaking by static electricity from the human body and clothing, earth the human body properly using the high resistance and discharge static electricity during the operation. In this case, however, the resistance value should be approx. 1MΩ and the resistance should be placed near the human body rather than the ground surface. When the indoor space is dry, static electricity may occur easily so be careful. We recommend the indoor space should be kept with humidity of 60% or more. When a soldering iron or other similar tool is used for assembly, be sure to earth it.
2) When installing the module and ICs, do not bend or twist them. Failure to do so may crack LC element and cause circuit failure.
3) To protect LC element, especially polarizing plate, use a transparent protective plate (e.g., acrylic plate, glass etc) for the product case.
4) Do not use an adhesive like a both-side adhesive tape to make LCD surface (polarizing plate) and product case stick together. Failure to do so may cause the polarizing plate to peel off.

11.3 Storage precautions

1) Avoid a high temperature and humidity area. Keep the temperature between 0°C and 35°C and also the humidity under 60%.
2) Choose the dark spaces where the product is not exposed to direct sunlight or fluorescent light.

3) Store the products as they are put in the boxes provided from us or in the same conditions as we recommend.

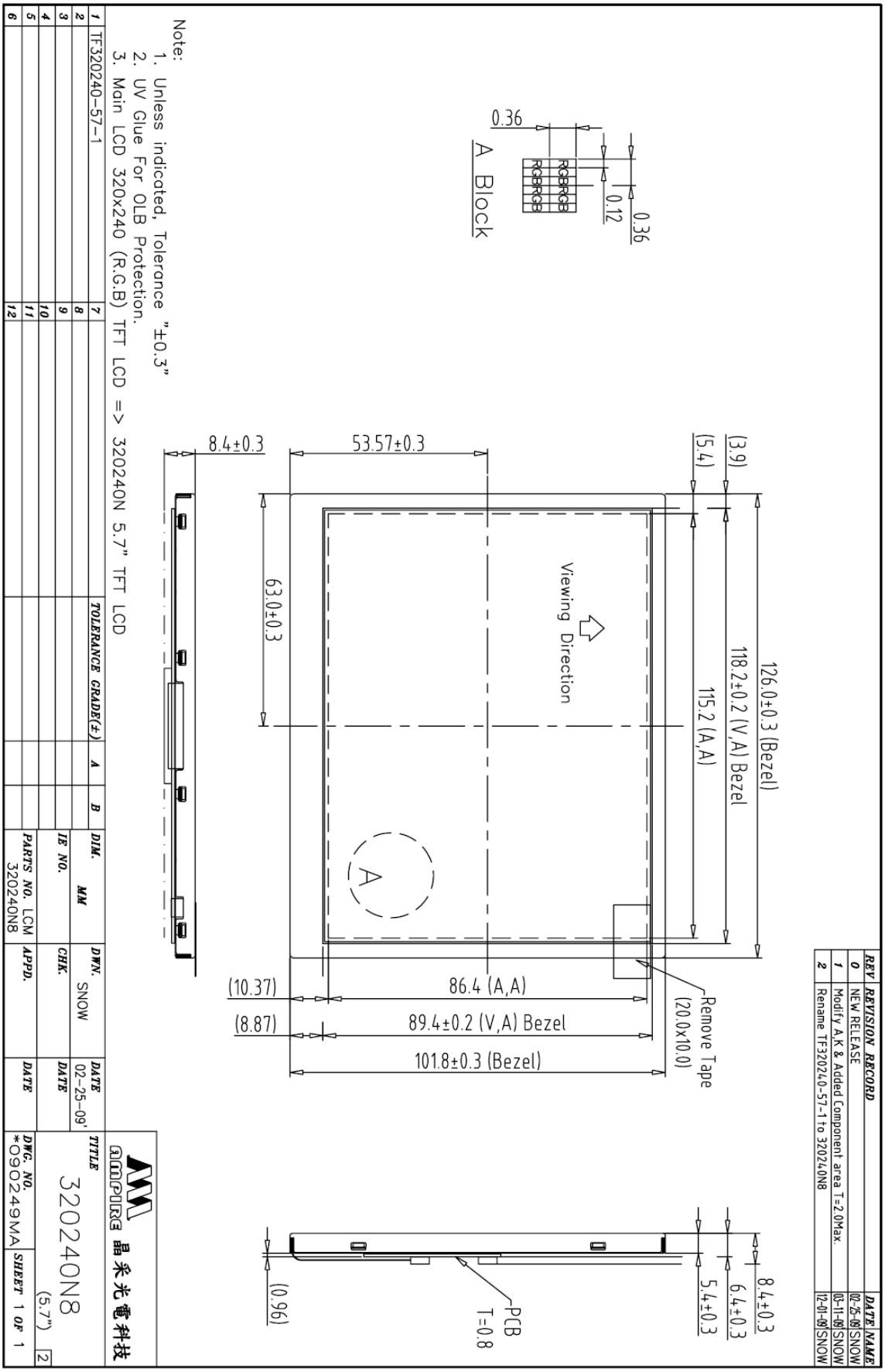
11.4 Operating precautions

1) Do not boost the applied drive voltage abnormally. Failure to do so may break ICs. When applying power voltage, check the electrical features beforehand and be careful. Always turn off the power to the LC module controller before removing or inserting the LC module input connector. If the input connector is removed or inserted while the power is turned on, the LC module internal circuit may break.
2) The display response may be late if the operating temperature is under the normal standard, and the display may be out of order if it is above the normal standard. But this is not a failure; this will be restored if it is within the normal standard.
3) The LCD contrast varies depending on the visual angle, ambient temperature, power voltage etc. Obtain the optimum contrast by adjusting the LC dive voltage.
4) When carrying out the test, do not take the module out of the low-temperature space suddenly. Failure to do so will cause the module condensing, leading to malfunctions.
5) Make certain that each signal noise level is within the standard (L level: 0.2Vdd or less and H level: 0.8Vdd or more) even if the module has functioned properly. If it is beyond the standard, the module may often malfunction. In addition, always connect the module when making noise level measurements.
6) The CMOS ICs are incorporated in the module and the pull-up and pull-down function is not adopted for the input so avoid putting the input signal open while the power is ON.
7) The characteristic of the semiconductor element changes when it is exposed to light emissions, therefore ICs on the LCD may malfunction if they receive light emissions. To prevent these malfunctions, design and assemble ICs so that they are shielded from light emissions.
8) Crosstalk occurs because of characteristics of the LCD. In general, crosstalk occurs when the regularized display is maintained. Also, crosstalk is affected by the LC drive voltage. Design the contents of the display, considering crosstalk.

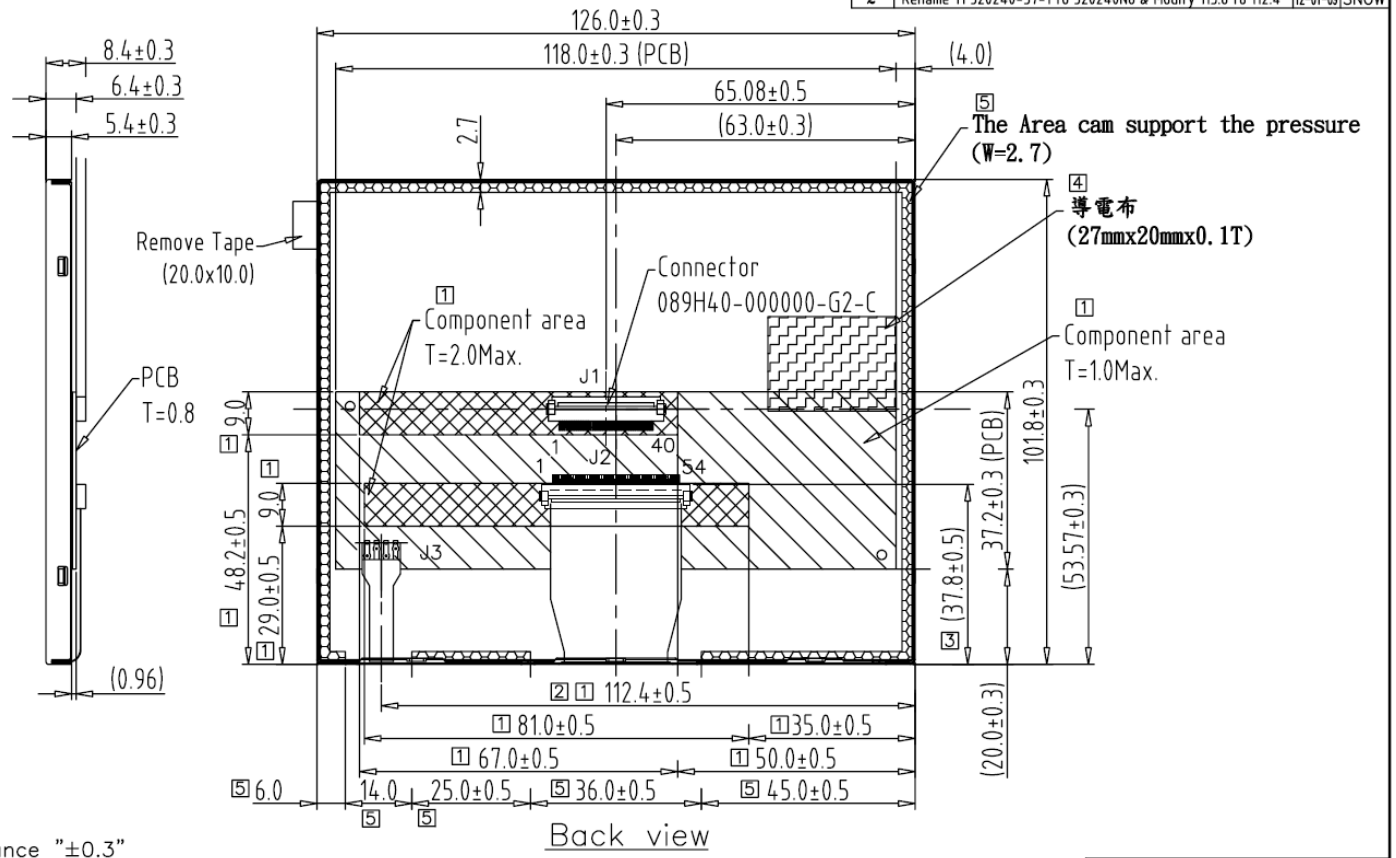
11.5 Other

1) Do not disassemble or take the LC module into pieces. The LC modules once disassembled or taken into pieces are not the guarantee articles.
2) The residual image may exist if the same display pattern is shown for hours. This residual image, however, disappears when another display pattern is shown or the drive is interrupted and left for a while. But this is not a problem on reliability.
3) AMIPRE will provide one year warranty for all products and three months warrantee for all repairing products.

12 OUTLINE DIMENSION



REV	REVISION RECORD	DATE	NAME
3	Modify 34.8 to 37.8	12-29-09	SNOW
4	Added 導電布 (27mmx20mmx0.1T)	01-08-10'	SNOW
5	Added The Area cam support the pressure (W=2.7)	01-12-10'	SNOW
0	NEW RELEASE	02-25-09	SNOW
1	Modify A,K & Added Component area T=2.0Max.	03-11-09	SNOW
2	Rename TF320240-57-1 to 320240N8 & Modify 113.6 to 112.4	12-01-09	SNOW



J1

1	U/D	21	G5
2	DMS(NC)	22	G4
3	HSYNC(NC)	23	G3
4	VLED	24	VSS
5	VLED	25	G2
6	VLED	26	G1
7	VCC	27	G0
8	VSYNC(NC)	28	VSS
9	DE	29	R5
10	VSS(X2)	30	R4
11	VSS(Y1)	31	R3
12	ADJ	32	VSS
13	B5	33	R2
14	B4	34	R1
15	B3	35	R0
16	VSS	36	VSS(X1)
17	B2	37	VSS(Y2)
18	B1	38	DCLK
19	B0	39	VSS
20	VSS	40	L/R

Note:
1. Unless indicated, Tolerance "±0.3"
2. UV Glue For OLB Protection.
3. Main LCD 320x240 (R.G.B) TFT LCD => 320240N 5.7" TFT LCD

1	TF320240-57-1	7		TOLERANCE GRADE(±)	A	B	DIM. MM	DWN. SNOW	DATE 02-25-09'	TITLE 320240N8 (5.7") 5
2		8					IE NO.	CHK.	DATE	
3		9								
4		10								
5		11					PARTS NO. LCM-1 320240N8	APPD.	DATE	DWG. NO. *09025OMA SHEET 1 OF 1
6		12								

AMPIRE 晶采光電科技