

晶采光電科技股份有限公司 AMPIRE CO., LTD.

SPECIFICATIONS FOR LCD MODULE

CUSTOMER	
CUSTOMER PART NO.	
AMPIRE PART NO.	AM-320240N1TMQW -T30H-D(R)
APPROVED BY	
DATE	

- ☐ Approved For Specifications
- ☐ Approved For Specifications & Sample

AMPIRE CO., LTD.

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APPROVED BY	CHECKED BY	ORGANIZED BY

Date: 2009/12/24 AMPIRE CO., LTD.

RECORD OF REVISION

Revision Date	Page	Contents	Editor
2009/12/24		New Release (Improve Vcom & DCLK signal)	Edward
		(improve vcom & DCER signal)	

1 Features

- 5.7 inch Amorphous-TFT-LCD (Thin Film Transistor Liquid Crystal Display) module. This module is composed of a 5.7" TFT-LCD panel, a driver circuit and backlight unit.
- (1) Construction: 5.7" a-Si color TFT-LCD, White LED Backlight, Touch Panel and PCB.
- (2) Resolution (pixel): 320(R.G.B) X240
- (3) Number of the Colors: 262K colors (R, G, B 6 bit digital each)
- (4) LCD type: Transmissive Color TFT LCD (normally White)
- (5) Interface: 40 pin
- (6) Power Supply Voltage: 3.3V single power input. Built-in power supply circuit.
- (7) Viewing Direction: 6 O'clock (The direction it's hard to be discolored)
- (8) DE only mode timing support.

2 Physical specifications

Item	Specifications	Unit	
Display resolution(dot)	960 (W) x 240(H)	mm	
Active area	115.2 (W) x 86.4 (H)	mm	
Screen size	5.7(Diagonal)	mm	
Pixel size	120 (W) x 360 (H)	um	
Color configuration	R.G.B stripe		
Overall dimension	167.0(W)x109.0(H)x10.3(D)	mm	
Weight	T.B.D	mg	
Backlight unit	LED		

3 Electrical specification

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3.1 Absolute max. ratings

3.1.1 Electrical Absolute max. ratings

Item	Symbol	Condition	Min.	Max.	Unit	Remark
Power voltage	VDD	VSS=0	-0.3	6.0	V	
Input voltege	V _{in}		-0.3	VDD+0.3	V	Note 1

Note1: DEN, DCLK, R0~R5, G0~G5, B0~B5

3.1.2 Environmental Absolute max. ratings

_	OPERATING		STOF	RAGE	
Item	MIN	MAX	MIN	MAX	Remark
Temperature	-20	70	-30	80	Note2,3,4,5,6,7,8
Humidity	No	Note1		te1	
Corrosive Gas	Not Acc	eptable	Not Acceptable		

Note1: Ta <= 40°C: 85% RH max

Ta > 40° C : Absolute humidity must be lower than the humidity of 85%RH at 40° C

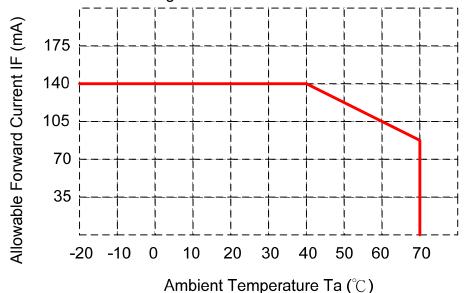
Note2 : For storage condition Ta at -30°C < 48h , at 80°C < 100h For operating condition Ta at -20°C < 100h

Note3: Background color changes slightly depending on ambient temperature. This phenomenon is reversible.

Note4: The response time will be slower at low temperature.

Note5 : Only operation is guarantied at operating temperature. Contrast , response time, another display quality are evaluated at +25°C

Note6 : When LCM is operated over 40°C ambient temperature, the I_{LED} of the LED back-light should be follow :



Note7: This is panel surface temperature, not ambient temperature.

Note8 : When LCM be operated over than 40°C , the life time of the LED back-light will be reduced.

3.1.3 LED back-light Unit Absolute max. ratings

Item	Symbol	Ratings	Unit	Remark
Peak forward Current	IF	210	mA	
Reverse Voltage	VR	30	V	
Power Dissipation	Po	1.2	W	

3.2 Electrical characteristics

3.2.1 DC Electrical characteristic of the LCD

Typical operting conditions (VSS=0V)

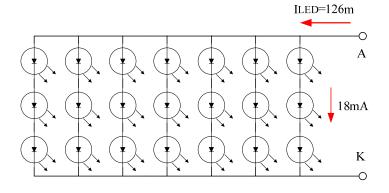
Item	Symbol	Min.	Тур.	Max.	Unit	Remark	
Power supply		VDD	3.0	3.3	3.6	V	
Input Voltage	H Level	V _{IH}	0.7 VDD	-	VDD	V	Note 1
for logic	L Level	V _{IL}	0	-	0.3 VDD	V	Note i
Power Supply c	urrent	IDD		45	55	mA	Note 2

Note1: DEN, DCLK, R0~R5, G0~G5, B0~B5

Note2: fv =60Hz , Ta=25°C , Display pattern : All Black

3.2.2 Electrical characteristic of LED Back-light

Paramenter	Symbol	Min.	Тур.	Max.	Unit	Condiction
I CD veltere	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \		40.5	40	W	I _{LED}
LED voltage	V_{AK}		10.5	12	V	=140mA,Ta=25°C
LED forward current	I _{LED}		126	140	mA	Ta=25°C
	I _{LED}		84	105	mA	Ta=60°C
LED life time			30K		hours	Note1

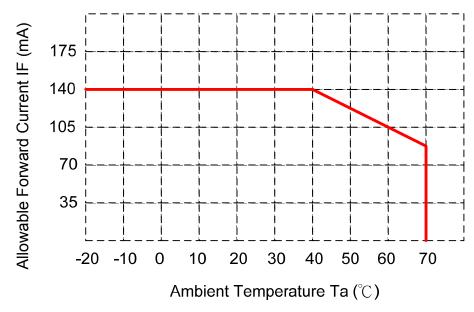


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- Note 1: End of Life shall be determined by the time when any of the following is satisfied under continuous lighting at 25°C and ILED = 126mA.
 - Intensity drops to 50% of the Initial Value (Min. Luminance)
 - Based on LED
 - It is an estimative value
- The constant current source is needed for white LED back-light driving.

When LCM is operated over 60°C ambient temperature, the $I_{\mbox{\scriptsize LED}}$ of the LED back-light

should be adjusted to 105mA max



3.3 AC Timing characteristic of the LCD

a. Timing condition

Signal	Parameter		Symbol	Min.	Тур.	Max	Unit.	Remark
DCLK	DCLK period		Tosc	-	156	-	ns	
	Frequency		Fosc	-	6.4	1	MHz	
	DCLK High plus wid		Тсн	-	78	-	ns	
	DCLK Low plus widt	th	TCL	-	78	-	ns	
RGB	Data setup time		Tsu	12	-	1	ns	
DATA	Data hold time		THD	12	-	ı	ns	
Hsync	Hsync period		TH	-	408	ı	Tosc	
	Hsync pulse width		THS	5	30	ı	Tosc	
	Back-Parch		Тнв		38		Tosc	
	Front-Parch		THF		20		Tosc	
	Hsync rising time		TCr	-	-	700	ns	
	Hsync falling time		TCf	-	-	300	ns	
Vsync	Vsync period	NTSC		-	262.5	-	TH	
		PAL		-	312.5	-	Тн	
	Vsync pulse width	_	Tvs	1	3	5	Тн	
	Back-Porch	NTSC	Тув		15		Тн	
		PAL			23		Тн	
	Display Period	1	TVD		240		Тн	
	Front Porch	NTSC	TvF		4.5		TH	
	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \	PAL			46.5	700	Тн	
	Vsync rising time		TVr	-	-	700	ns	
	Vsync falling time	no rioina	TVf	-	-	1.5	μs	
	Vsync falling to Hsync rising time for odd field		THVO	1	-	-	Tosc	
	Vsync falling to Hsync falling time for even field		THVE	1	-	-	Tosc	
DEN	Vsync-DEN time	NTSC	TVSE	-	18	-	Тн	
	PAL		TVSE	-	26	-	Тн	
	Hsync-DEN time		THE	36	68	88	Tosc	
	DEN plus width		TEP	-	320	-	Tosc	

Note 1: DEN is definition of above timing for Hsync and Vsync.

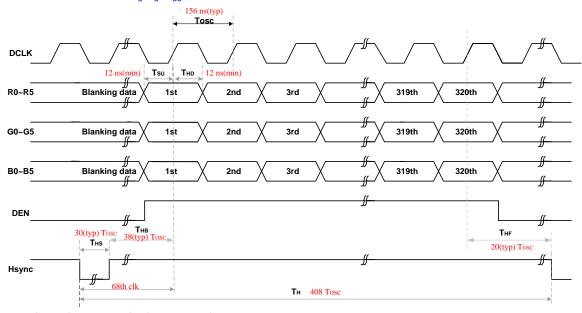
Note 2: No metter when Hsync and Vsync is inputted, the LCM can be drove only

DEN Signal. DEN should be set to low level when it is not input.

Note 3: The Dummy Enable is needed in the end of frame. Please set the LCD controller timing to 241 lines.

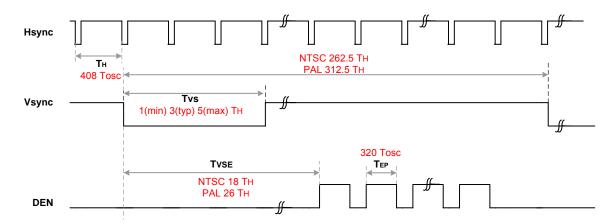
Horizontal display timing

Note: Data is latched rising edge trigger of CLK.

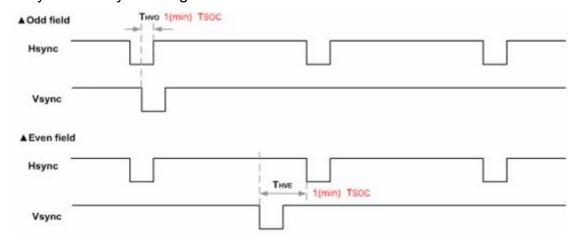


If DEN is fixed to low, the SYNC mode is used. Otherwise DE mode is used. When SYNC mode is used, 1st data start from 68th CLK after Hsync falling

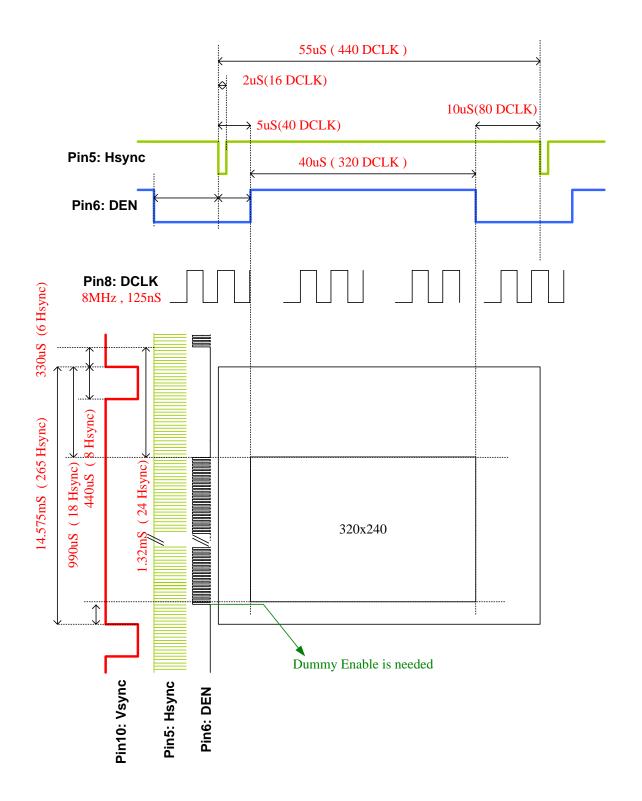
Vertical display timing



Hsync and Vsync timing



Recommend Driving Timing:



4 Optical specification

4.1 Optical characteristic of the LCD

Item		Symbol	Conditon	Min.	Тур.	Max.	Unit	Remark
Response	Rise	T _r	Θ=0°	-	15	30	ms	Note 1,2,3,5
Time	Fall	T_f		-	35	50	ms	14016 1,2,3,3
Contrast r	atio	CR	At optimized viewing angle	200	350	-		Note 1,2,4,5
	Top			60	70	-		
Viewing	Bottom		CR≧10	40	50	-	deg.	Note1,2, 5,6
Angle	Left		UN≦ IU	60	70	-	ueg.	140161,2, 3,0
	Right			60	70	-		
Drightno	D: 14		I _{LED} =126mA, 25°ℂ	342	360	-	cd/m²	Note 7
Brightne	:55	YL	I _{LED} =140mA, 25°C	380	400	-	cd/m²	Note 7
Dod ohrom	oticity.	XR		0.610	0.640	0.670		Niete 7
Red chroma	alicity	YR		0.314	0.344	0.374		Note 7
Croop obrop	acticity.	XG		0.268	0.298	0.328		For reference
Green chron	laticity	YG	Θ=0°	0.553	0.583	0.613		only. These
Dlug obrom	oticity.	Хв	Θ=0°	0.102	0.132	0.162		data should
Blue chrom	aucity	YB		0.107	0.137	0.167		be update according the
White chromaticity		XW		0.282	0.312	0.342		prototype.
write cilion	ialicity	YW		0.299	0.329	0.359		prototype.

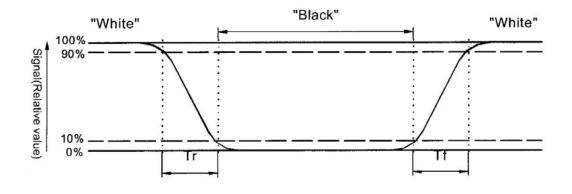
^()For reference only. These data should be update according the prototype.

Note 1: Note 1:Ambient temperature=25°C ,and lamp current I_{LED}=140mA.To be measured in the dark room.

Note 2:To be measured on the center area of panel with a viewing cone of 1°by Topcon luminance meter BM-7,after 10 minutes operation.

Note 3. Definition of response time:

The output signals of photo detector are measured when the input signals are changed from "black" to "white" (falling time) and from "white" to "black" (rising time), respectively. The response time is defined as the time interval between the 10% and 90% of amplitudes. Refer to figure as below.



Note 4. Definition of contrast ratio:

Contrast ratio is calculated with the following formula.

Contrast ratio(CR)= Photo detector output when LCD is at "White" state
Photo detector Output when LCD is at "Black" state

Note 5:White $V_i=V_{i50}+1.5V$

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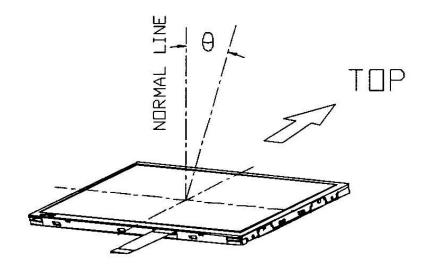
Black $V_i=V_{i50}+2.0V$

"±"means that the analog input signal swings in phase with V_{COM} signal.

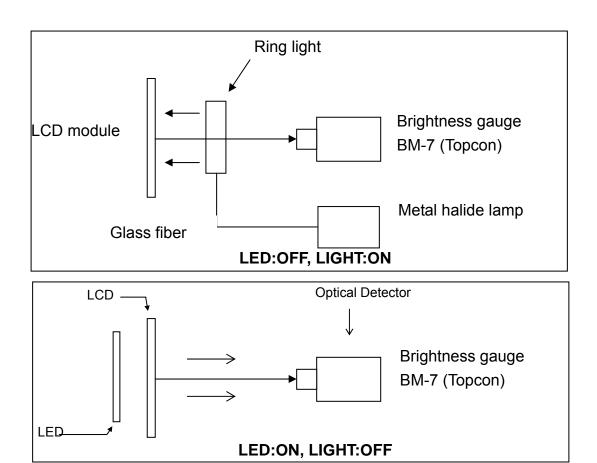
"– " means that the analog input signal swings out of phase with V_{COM} signal.

 V_{i50} : The analog input voltage when transmission is 50%. The 100% Transmission is defined as the transmission of LCD panel when all the Input terminals of module are electrically opened.

Note 6.Definition of viewing angle, Refer to figure as below.



Note 7.Measured at the center area of the panel when all the input terminals of LCD panel are electrically opened.



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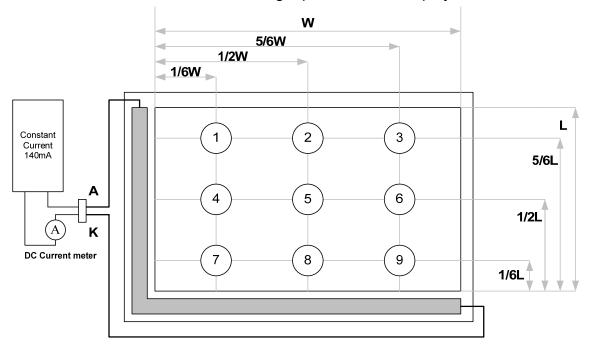
4.2 Optical characteristic of the Back-light

ITEM	MIN	TYP	MAX	UNIT	Condition
Bare Brightness	3600	I	I	Cd/m2	I _{LED} =140mA,Ta=25°C
AVG. X of 1931 C.I.E.	0.26	0.28	0.31		I _{LED} =140mA,Ta=25°ℂ
AVG. Y of 1931 C.I.E.	0.27	0.29	0.32		I _{LED} =140mA,Ta=25°C
Brightness Uniformity	80	-	1	%	I _{LED} =140mA,Ta=25°C

^()For reference only. These data should be update according the prototype.

Note1: Measurement after 10 minutes from LED operating.

Note2: Measurement of the following 9 places on the display.



Note3: The Uniformity definition (Min Brightness / Max Brightness) x 100%

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4.3 Touch Panel Electrical Specification

Parameter	Condition	Standard Value			
Terminal Resistance	X Axis	400 ~ 900 Ω			
Terminal Resistance	Y Axis	200 ~ 500 Ω			
Insulating Resistance	DC 25 V	More than $10M\Omega$			
Linearity		±1.5 %			
Notes life by Pen	Note a	100,000 times(min)			
Input life by finger	Note b	1,000,000 times (min)			

Note A.

Notes area for pen notes life test is 10 x 9 mm.

Size of word is 7.5 x 6.72 Shape of pen end : R0.8

Load : 250 g

Note B

By Silicon rubber tapping at same point

Shape of rubber end: R8

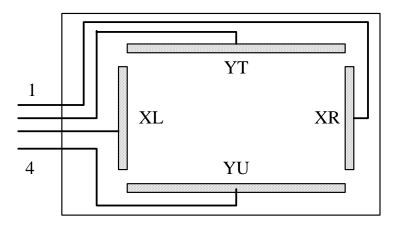
Load: 200g

Frequency: 5 Hz

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Interface

No.	Symbol	Function
1	XR	Touch Panel Right Signal in X Axis
2	YT	Touch Panel Top Signal in Y Axis
3	XL	Touch Panel Left Signal in X Axis
4	YB	Touch Panel Bottom Signal in Y Axis



5 Interface specifications

5.1 Driving signals for the TFT panel

JAE:FA5B040HF1R3000 (Suitable FPC :t=0.3+/-0.03mm , 0.5+/-0.03mm pitch)

Pin no	Symbol	I/O	Description	Remark
1~4	VDD		Power supply for the logic (3.3V)	
F	NC		No connection	Note 1
5	NC	I	Can be OPEN fixed to VDD or GND.	Note1
6	DEN	ı	Input data enable control	
7	VSS		GND	
8	DCLK	ı	Clock signal. Latching data at the rising edge.	
9	VSS		GND	
10	NC	ı	No connection	Note 1
10	NC	ı	Can be OPEN fixed to VDD or GND.	Note1
11	VSS		GND	
12	B5	ı	Blue data	
13	B4	ı		
14	B3	ı		
15	VSS		GND	
16	B2	ı	Blue data	
17	B1	ı		
18	B0	ı		
19	VSS		GND	
20	G5	ı	Green data	
21	G4	ı		
22	G3	ı		
23	VSS		GND	
24	G2	ı	Green data	
25	G1	ı		
26	G0	ı		
27	VSS		GND	
28	R5	ı	Red data	
29	R4	ı		
30	R3	ı		
31	VSS		GND	
32	R2	ı	Red data	
33	R1	ı		
34	R0	ı		
35	NC		No connection	
36	VSS		GND	
37	NC		No connection	
38	NC		No connection	
39	NC		No connection	
40	NC		No connection	

Note 1: This module only support DE only mode.

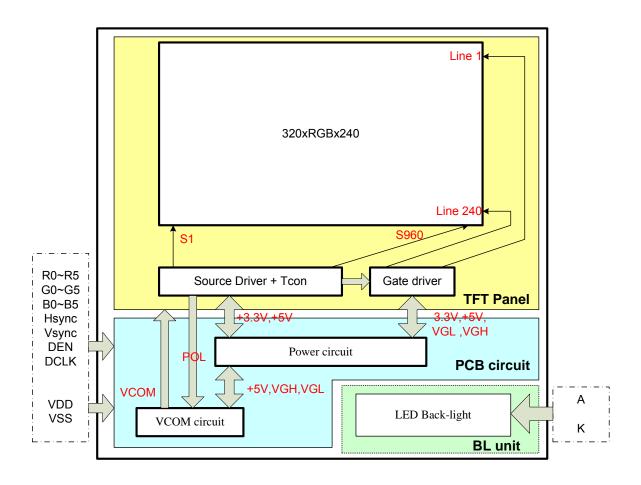
5.2 Driving signals for the LED back-light

JST Housing: BHR-03VS-1

Pin no	Symbol	Level	Description	Remark
1	Α	-	LED Back-light Anode	
2	K	-	LED Back-light Cathode	

6 BLOCK DIAGRAM

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7 DISPLAYED COLOR AND INPUT DATA

	G	olor & Gray								D	ATA S	SIGNA	L							
		cale	R5	R4	R3	R2	R1	R0	G5	G4	G3	G2	G1	G0	B5	B4	В3	B2	B1	B0
		lack	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
		ed(63)	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
		en(63)	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
Basic		ue(63)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1
Color	C	yan	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1
		genta	1	1	1	1	1	1	0	0	0	0	0	0	1	1	1	1	1	1
		ellow	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0
		/hite	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
		lack	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Re	ed(0)	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0
	Re	ed(1)	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0
Red		:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
Neu	Re	ed(31)	0	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
		:	:		••	••	••	:	:	••		••	••	••	:	:	:	:	:	:
	Re	ed(62)	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0
	Re	ed(63)	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	В	lack	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Gre	een(0)	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0
	Gre	een(1)	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0
Green		:	:	:	••	••	••	:	:	••	••	••	••	••	:	:	:	:	:	:
Green	Gre	en(31)	0	0	0	0	0	0	0	1	1	1	1	0	0	0	0	0	0	0
		:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Gre	en(62)	0	0	0	0	0	0	1	1	1	1	1	0	0	0	0	0	0	0
	Gre	en(63)	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
	В	lack	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	BI	ue(0)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
	BI	ue(1)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
Blue		:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Blu	ue(31)	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1
		:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Blu	ıe(62)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	0
	Blu	ue(63)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1

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8 QUALITY AND RELIABILITY

8.1 TEST CONDITIONS

Tests should be conducted under the following conditions:

Ambient temperature : $25 \pm 5^{\circ}$ C Humidity : $60 \pm 25\%$ RH.

8.2 SAMPLING PLAN

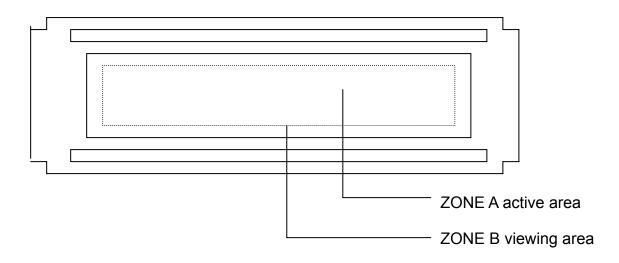
Sampling method shall be in accordance with MIL-STD-105E , level II, normal single sampling plan .

8.3 ACCEPTABLE QUALITY LEVEL

A major defect is defined as one that could cause failure to or materially reduce the usability of the unit for its intended purpose. A minor defect is one that does not materially reduce the usability of the unit for its intended purpose or is an infringement from established standards and has no significant bearing on its effective use or operation.

8.4 APPEARANCE

An appearance test should be conducted by human sight at approximately 30 cm distance from the LCD module under flourescent light. The inspection area of LCD panel shall be within the range of following limits.



8.5 INSPECTION QUALITY CRITERIA

No.	Item	Criterior	Defect type	
1	Non display	No non display is allowed	Major	
2	Irregular operation	No irregular operation is a	Major	
3	Short	No short are allowed		Major
4	Open	Any segments or comm are rejectable.	on patterns that don't activate	Major
5	Black/White spot (I)	Size D (mm) $D \le 0.15$ $0.15 < D \le 0.20$ $0.20 < D \le 0.30$ $0.30 < D \le 0.5$	Acceptable number Ignore 3 2 1	Minor
6	Black/White line (I)	Length(mm) 0.03 < W ≤	0.06 0.07 2	Minor
7	Black/White sport (II)	Size D (mm) D ≤ 0.30 0.30 < D ≤ 0.50 0.50 < D ≤ 1.20 1.20 < D	Acceptable number Ignore 5 3	Minor
8	Black/White line (II)	Length (mm) Width (20 < L	0.07 5 0.09 3 0.10 2	Minor
9	Back Light	 No Lighting is rejectab Flickering and abnorm 	Major	
10	Display pattern	$\frac{A+B}{2} \le 0.30$ 0 < C	Unit:mm $\frac{D+E}{2} \le 0.25 \frac{F+G}{2} \le 0.25$	Minor

11	Blemish & Foreign matters Size: $D = \frac{A+B}{2}$	Size D (r D ≤ 0.15 0.15 < D ≤ 0.20 0.20 < D ≤ 0.30 0.30 < D ≤ 0.5	;))	Ac	ceptable number Ignore 3 2	Minor
12	Scratch on Polarizer	Width (mm) W≤0.03 0.03 <w≤0.05 0.05<w≤0.08="" 0.08<w="" note(1)="" regard<="" td=""><td>Length Igno L ≤ 2 L > 2 L > 1 L ≤ 1 Note as a blemis</td><td>re 2.0 2.0 .0 .0 .0 (1)</td><td>Acceptable number Ignore Ignore 1 1 Ignore Note(1)</td><td>Minor</td></w≤0.05>	Length Igno L ≤ 2 L > 2 L > 1 L ≤ 1 Note as a blemis	re 2.0 2.0 .0 .0 .0 (1)	Acceptable number Ignore Ignore 1 1 Ignore Note(1)	Minor
13	Bubble in polarizer	Size D (r D ≤ 0.20 0.20 < D ≤ 0.50 0.50 < D ≤ 0.80 0.80 < D))	Acc	ceptable number Ignore 3 2 0	Minor
14	Stains on LCD panel surface	Stains that car with a soft clot	Minor			
15	Rust in Bezel	Rust which is	visible in th	ie bezel i	s rejectable.	Minor
16	Defect of land surface contact (poor soldering)	Evident crevice	Minor			
17	Parts mounting	1. Failure to m 2. Parts not in 3. Polarity, for	Major Major Major			
18	Parts alignment	LSI, IC lea outline. Chip compound the leads is	Minor Minor			
19	Conductive foreign matter (Solder ball, Solder chips)	1. 0.45<φ 2. 0.30<φ <u><</u> 0.4 φ:Average 3. 0.50 <l L: Average</l 	Major Minor Minor			
20	Faulty PCB correction	connected places are	, using a ju corrected ted part is	ımper wi per PCB	burnout, the pattern is re for repair; 2 or more I no resist coating has	Minor Minor

		The TFT The acce					
21	Defect Dot	Bright dot	Dark dot	Total dot	Distance between Dark dark		Minor
		2	4	6	L≧5 mm		

8.6 RELIABILITY

Test Item	Test Conditions	Note
High Temperature Operation	70±3°C , t=96 hrs	
Low Temperature Operation	-20±3°C , t=96 hrs	
High Temperature Storage	80±3°C , t=96 hrs	1,2
Low Temperature Storage	-30±3°C , t=96 hrs	1,2
Thermal Shock Test	-20°C ~ 25°C ~ 70°C 30 m in. 5 min. 30 min. (1 cycle) Total 5 cycle	1,2
Humidity Test	40 °C, Humidity 90%, 96 hrs	1,2
Vibration Test (Packing)	Sweep frequency: 10 ~ 55 ~ 10 Hz/1min Amplitude: 0.75mm Test direction: X.Y.Z/3 axis Duration: 30min/each axis	2

Note 1: Condensation of water is not permitted on the module.

Note 2 : The module should be inspected after 1 hour storage in normal conditions

(15-35°C, 45-65%RH).

Definitions of life end point :

- Current drain should be smaller than the specific value.
- Function of the module should be maintained.
- Appearance and display quality should not have degraded noticeably.
- Contrast ratio should be greater than 50% of the initial value.

9 USE PRECAUTIONS

9.1 Handling precautions

- 1) The polarizing plate may break easily so be careful when handling it. Do not touch, press or rub it with a hard-material tool like tweezers.
- 2) Do not touch the polarizing plate surface with bare hands so as not to make it dirty. If the surface or other related part of the polarizing plate is dirty, soak a soft cotton cloth or chamois leather in benzine and wipe off with it. Do not use chemical liquids such as acetone, toluene and isopropyl alcohol. Failure to do so may bring chemical reaction phenomena and deteriorations.
- 3) Remove any spit or water immediately. If it is left for hours, the suffered part may deform or decolorize.
- 4) If the LCD element breaks and any LC stuff leaks, do not suck or lick it. Also if LC stuff is stuck on your skin or clothing, wash thoroughly with soap and water immediately.

9.2 Installing precautions

- 1) The PCB has many ICs that may be damaged easily by static electricity. To prevent breaking by static electricity from the human body and clothing, earth the human body properly using the high resistance and discharge static electricity during the operation. In this case, however, the resistance value should be approx. $1M\Omega$ and the resistance should be placed near the human body rather than the ground surface. When the indoor space is dry, static electricity may occur easily so be careful. We recommend the indoor space should be kept with humidity of 60% or more. When a soldering iron or other similar tool is used for assembly, be sure to earth it.
- 2) When installing the module and ICs, do not bend or twist them. Failure to do so may crack LC element and cause circuit failure.
- 3) To protect LC element, especially polarizing plate, use a transparent protective plate (e.g., acrylic plate, glass etc) for the product case.
- 4) Do not use an adhesive like a both-side adhesive tape to make LCD surface (polarizing plate) and product case stick together. Failure to do so may cause the polarizing plate to peel off.

9.3 Storage precautions

- 1) Avoid a high temperature and humidity area. Keep the temperature between 0°C and 35°C and also the humidity under 60%.
- 2) Choose the dark spaces where the product is not exposed to direct sunlight or fluorescent light.
- 3) Store the products as they are put in the boxes provided from us or in the same conditions as we recommend.

9.4 Operating precautions

- 1) Do not boost the applied drive voltage abnormally. Failure to do so may break ICs. When applying power voltage, check the electrical features beforehand and be careful. Always turn off the power to the LC module controller before removing or inserting the LC module input connector. If the input connector is removed or inserted while the power is turned on, the LC module internal circuit may break.
- 2) The display response may be late if the operating temperature is under the normal standard, and the display may be out of order if it is above the normal standard. But this is not a failure; this will be restored if it is within the normal standard.
- 3) The LCD contrast varies depending on the visual angle, ambient temperature, power voltage etc. Obtain the optimum contrast by adjusting the LC dive voltage.
- 4) When carrying out the test, do not take the module out of the low-temperature space suddenly. Failure to do so will cause the module condensing, leading to malfunctions.
- 5) Make certain that each signal noise level is within the standard (L level: 0.2Vdd or less and H level: 0.8Vdd or more) even if the module has functioned properly. If it is beyond the standard, the module may often malfunction. In addition, always connect the module when making noise level measurements.
- 6) The CMOS ICs are incorporated in the module and the pull-up and pull-down function is not adopted for the input so avoid putting the input signal open while the power is ON.
- 7) The characteristic of the semiconductor element changes when it is exposed to light emissions, therefore ICs on the LCD may malfunction if they receive light emissions. To prevent these malfunctions, design and assemble ICs so that they are shielded from light emissions.

8) Crosstalk occurs because of characteristics of the LCD. In general, crosstalk occurs when the regularized display is maintained. Also, crosstalk is affected by the LC drive voltage. Design the contents of the display, considering crosstalk.

9.5 Other

- 1) Do not disassemble or take the LC module into pieces. The LC modules once disassembled or taken into pieces are not the guarantee articles.
- 2) The residual image may exist if the same display pattern is shown for hours. This residual image, however, disappears when another display pattern is shown or the drive is interrupted and left for a while. But this is not a problem on reliability.
- 3) AMIPRE will provide one year warranty for all products and three months warrantee for all repairing products.

10 OUTLINE DIMENSION

